

IGBT - Power, Single, N-Channel, Field Stop VII (FS7), SCR, Power TO247-4L 1200 V, 1.45 V, 40 A

AFGH4L40T120RW

Description

Using the novel field stop 7th generation IGBT technology in TO247 4-lead package, this device offers the optimum performance with low on state voltage and minimal switching losses for both hard and soft switching topologies in automotive applications.

Features

- Extremely Efficient Trench with Field Stop Technology
- Maximum Junction Temperature – $T_J = 175^{\circ}\text{C}$
- Short Circuit Rated and Low Saturation Voltage
- Fast Switching and Tightened Parameter Distribution
- AEC-Q101 Qualified, PPAP Available Upon Request
- These Device is Pb-Free, Halogen Free/BFR Free and is RoHS Compliant

Applications

- Automotive E-compressor
- Automotive EV PTC Heater
- OBC

MAXIMUM RATINGS ($T_J = 25^{\circ}\text{C}$ unless otherwise noted)

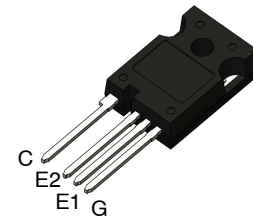
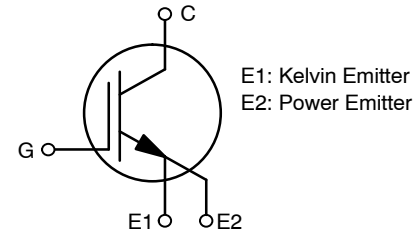
Parameter		Symbol	Value	Unit
Collector to Emitter Voltage		V_{CE}	1200	V
Gate to Emitter Voltage		V_{GE}	± 20	
Transient Gate to Emitter Voltage			± 30	
Collector Current	$T_C = 25^{\circ}\text{C}$	I_C	80	A
	$T_C = 100^{\circ}\text{C}$		40	
Power Dissipation	$T_C = 25^{\circ}\text{C}$	P_D	576	W
	$T_C = 100^{\circ}\text{C}$		288	
Pulsed Collector Current	$T_C = 25^{\circ}\text{C}$, $t_p = 10 \mu\text{s}$ (Note 1)	I_{CM}	120	A
Short Circuit Withstand Time $V_{GE} = 15 \text{ V}$, $V_{CC} = 800 \text{ V}$, $T_C = 150^{\circ}\text{C}$		T_{SC}	6	μs
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to $+175$	$^{\circ}\text{C}$
Lead Temperature for Soldering Purposes		T_L	260	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating; Pulse width limited by max. junction temperature

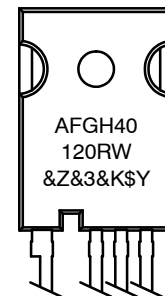
BV_{CES}	$V_{CE(sat)}$ TYP	I_C MAX
1200 V	1.45 V	40 A

PIN CONNECTIONS



TO-247-4LD
CASE 340CJ

MARKING DIAGRAM



AFGH40120RW = Specific Device Code
 &Z = Assembly Plant Code
 &3 = 3-Digit Date Code
 &K = 2-Digit Lot Traceability Code
 \$Y = onsemi Logo

ORDERING INFORMATION

Device	Package	Shipping
AFGH4L40T120RW	TO-247-4L (Pb-Free)	30 Units / Rail

AFGH4L40T120RW

THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case for IGBT	$R_{\theta JC}$	0.26	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	40	

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector to Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0\text{ V}, I_C = 1\text{ mA}$	1200	–	–	V
Zero Gate Voltage Collector Current	I_{CES}	$V_{GE} = 0\text{ V}, V_{CE} = V_{CES}$	–	–	40	μA
Gate-to-Emitter leakage Current	I_{GES}	$V_{GE} = \pm 20\text{ V}, V_{CE} = 0\text{ V}$	–	–	± 400	nA

ON CHARACTERISTICS

Gate to Emitter Threshold Voltage	$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 40\text{ mA}, T_J = 25^\circ\text{C}$	4.98	5.88	6.78	V
Collector to Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = 15\text{ V}, I_C = 40\text{ A}, T_J = 25^\circ\text{C}$	–	1.45	1.78	V
		$V_{GE} = 15\text{ V}, I_C = 40\text{ A}, T_J = 175^\circ\text{C}$	–	1.75	–	

DYNAMIC CHARACTERISTICS

Input Capacitance	C_{IES}	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	–	4721	–	pF
Output Capacitance	C_{OES}		–	144	–	pF
Reverse Transfer Capacitance	C_{RES}		–	24.2	–	pF
Total Gate Charge	Q_G	$V_{CE} = 600\text{ V}, V_{GE} = 15\text{ V}, I_C = 40\text{ A}$	–	171	–	nC
Gate to Emitter Charge	Q_{GE}		–	42.2	–	nC
Gate to Collector Charge	Q_{GC}		–	73.4	–	nC

SWITCHING CHARACTERISTICS, INDUCTIVE LOAD (Note: Si Diode Applied)

Turn-On Delay Time	$t_{d(on)}$	$V_{CE} = 600\text{ V}$ $V_{GE} = 0/15\text{ V}$ $I_C = 20\text{ A}$ $R_G = 6\ \Omega$ $T_J = 25^\circ\text{C}$	–	53.5	–	ns
Turn-Off Delay Time	$t_{d(off)}$		–	311	–	
Rise Time	t_r		–	27.8	–	
Fall Time	t_f		–	189	–	
Turn-On Switching Loss	E_{on}		–	1.26	–	mJ
Turn-Off Switching Loss	E_{off}		–	1.36	–	
Total Switching Loss	E_{ts}		–	2.61	–	
Turn-On Delay Time	$t_{d(on)}$	$V_{CE} = 600\text{ V}$ $V_{GE} = 0/15\text{ V}$ $I_C = 40\text{ A}$ $R_G = 6\ \Omega$ $T_J = 25^\circ\text{C}$	–	58.2	–	ns
Turn-Off Delay Time	$t_{d(off)}$		–	258	–	
Rise time	t_r		–	47.4	–	
Fall Time	t_f		–	122	–	
Turn-On Switching Loss	E_{on}		–	3.38	–	mJ
Turn-Off Switching Loss	E_{off}		–	1.7	–	
Total Switching Loss	E_{ts}		–	5.08	–	

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise specified) (continued)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
SWITCHING CHARACTERISTICS, INDUCTIVE LOAD (Note: Si Diode Applied)						
Turn-On Delay Time	$t_{d(on)}$	$V_{CE} = 600\text{ V}$ $V_{GE} = 0/15\text{ V}$ $I_C = 20\text{ A}$ $R_G = 6\ \Omega$ $T_J = 175^\circ\text{C}$	–	58.7	–	ns
Turn-Off Delay Time	$t_{d(off)}$		–	433	–	
Rise Time	t_r		–	39.4	–	
Fall Time	t_f		–	376	–	
Turn-On Switching Loss	E_{on}	$V_{CE} = 600\text{ V}$ $V_{GE} = 0/15\text{ V}$ $I_C = 40\text{ A}$ $R_G = 6\ \Omega$ $T_J = 175^\circ\text{C}$	–	2.01	–	mJ
Turn-Off Switching Loss	E_{off}		–	2.52	–	
Total Switching Loss	E_{ts}		–	4.53	–	
Turn-On Delay Time	$t_{d(on)}$		–	65.7	–	ns
Turn-Off Delay Time	$t_{d(off)}$		–	343	–	
Rise Time	t_r		–	64.7	–	
Fall Time	t_f		–	233	–	
Turn-On Switching Loss	E_{on}	$V_{CE} = 600\text{ V}$ $V_{GE} = 0/15\text{ V}$ $I_C = 40\text{ A}$ $R_G = 6\ \Omega$ $T_J = 175^\circ\text{C}$	–	5.45	–	mJ
Turn-Off Switching Loss	E_{off}		–	3.04	–	
Total Switching Loss	E_{ts}		–	8.49	–	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

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TYPICAL CHARACTERISTICS

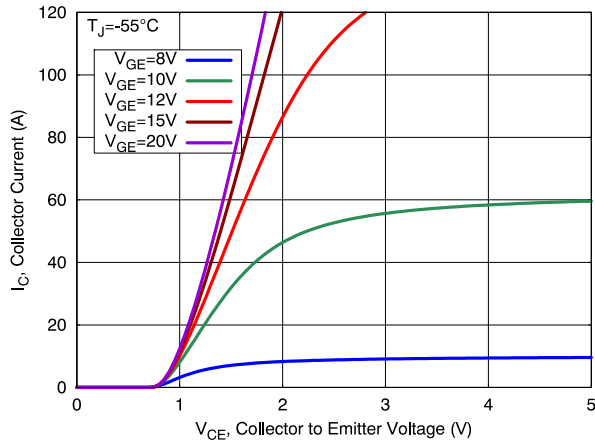


Figure 1. Output Characteristics

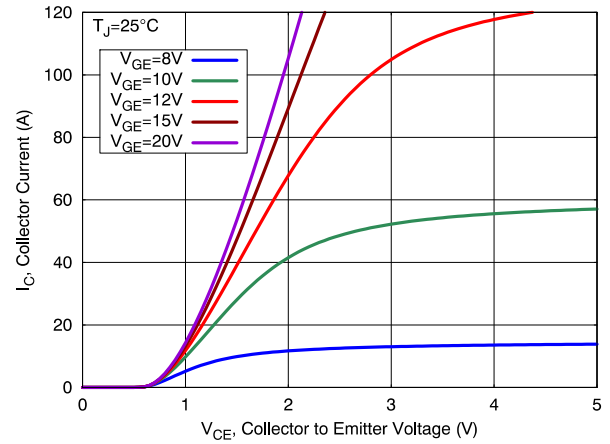


Figure 2. Output Characteristics

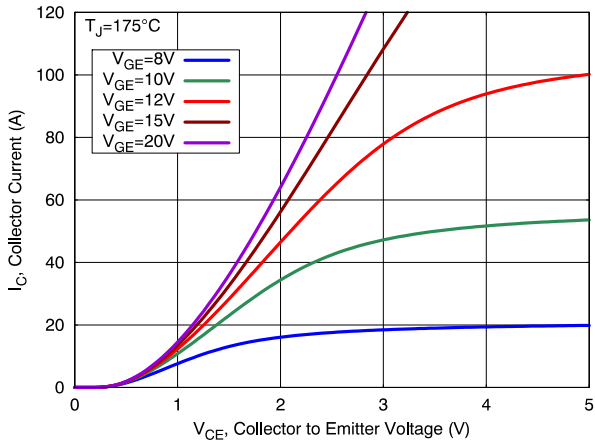


Figure 3. Output Characteristics

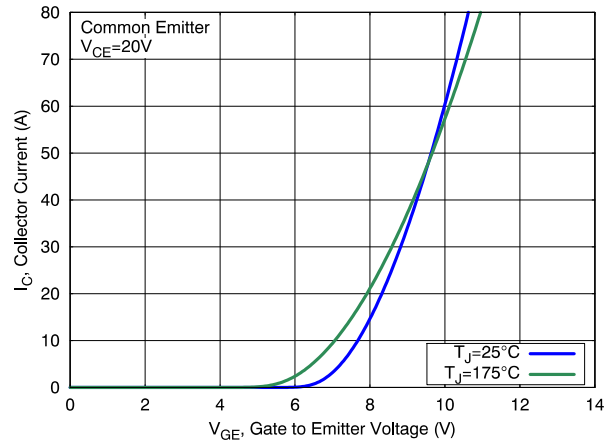


Figure 4. Transfer Characteristics

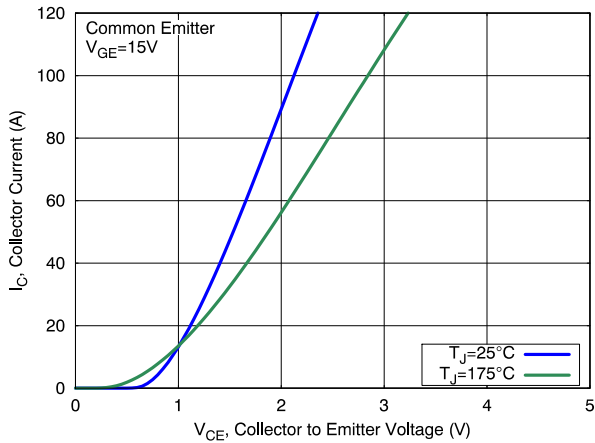


Figure 5. Saturation Characteristics

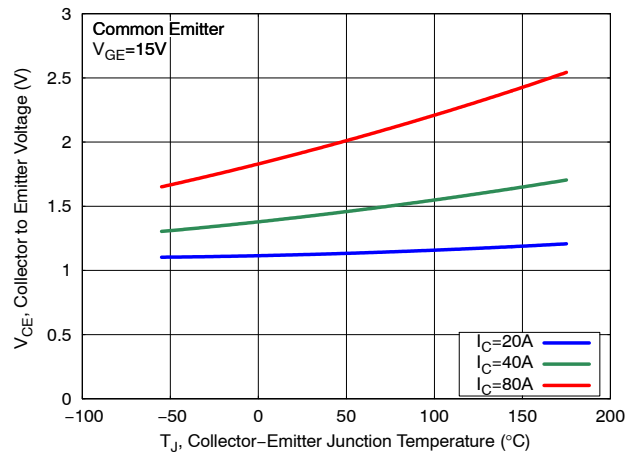


Figure 6. Saturation Voltage vs. Junction Temperature

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TYPICAL CHARACTERISTICS

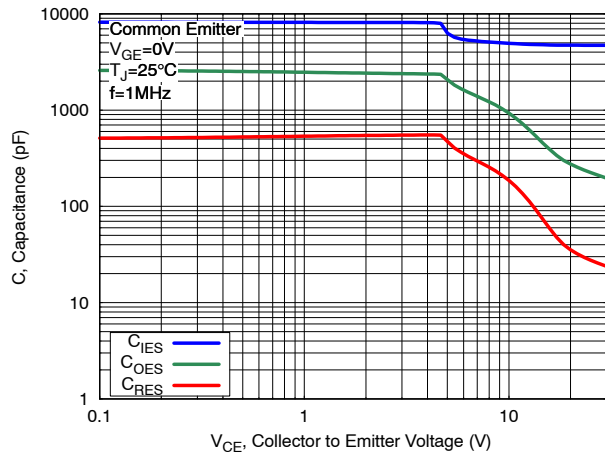


Figure 7. Capacitance Characteristics

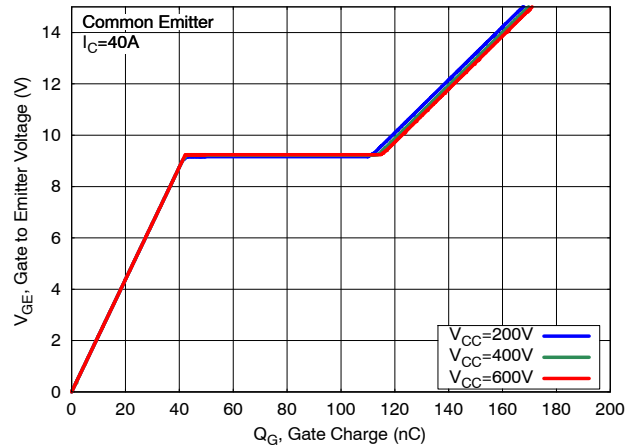


Figure 8. Gate Charge Characteristics

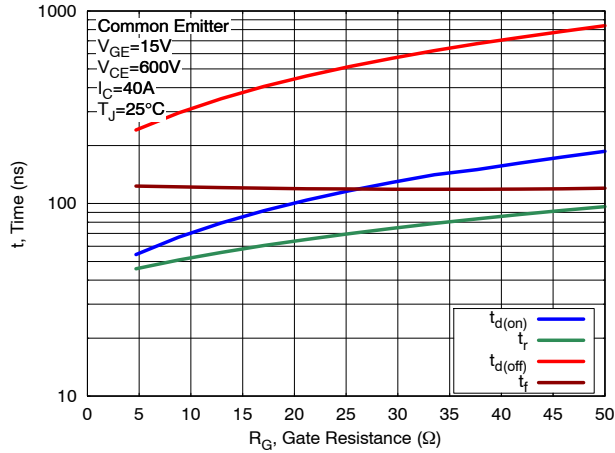


Figure 9. Switching Time vs Gate Resistance

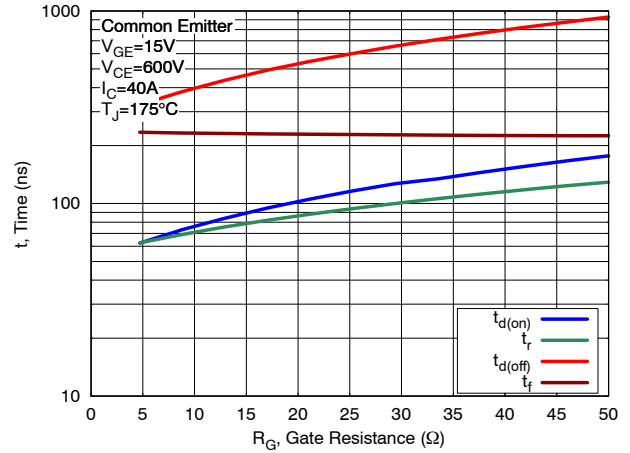


Figure 10. Switching Time vs Gate Resistance

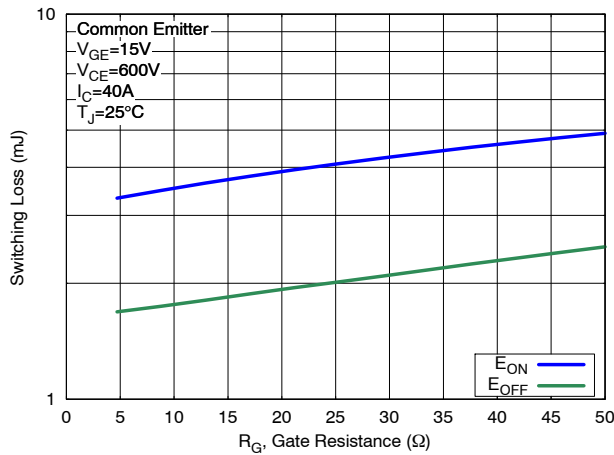


Figure 11. Switching Loss vs Gate Resistance

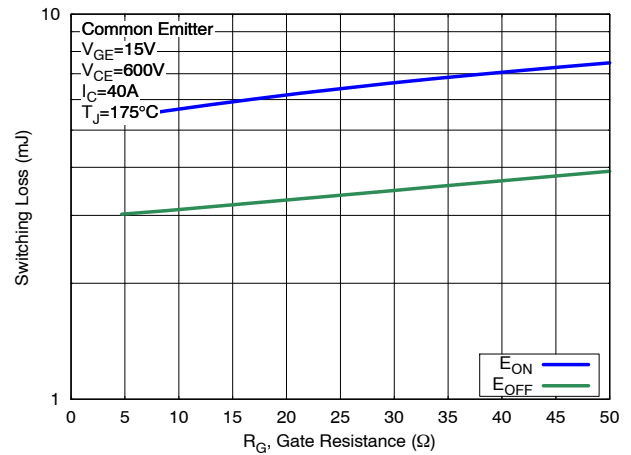


Figure 12. Switching Loss vs Gate Resistance

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TYPICAL CHARACTERISTICS

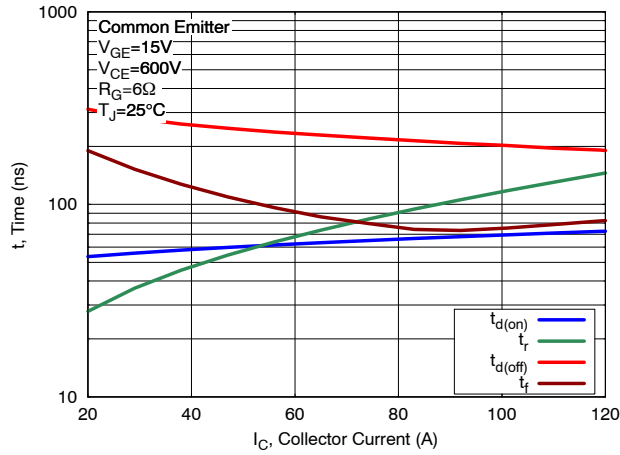


Figure 13. Switching Time vs Collector Current

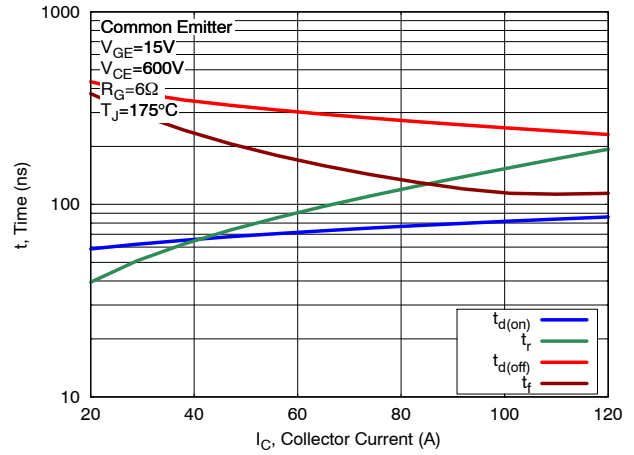


Figure 14. Switching Time vs Collector Current

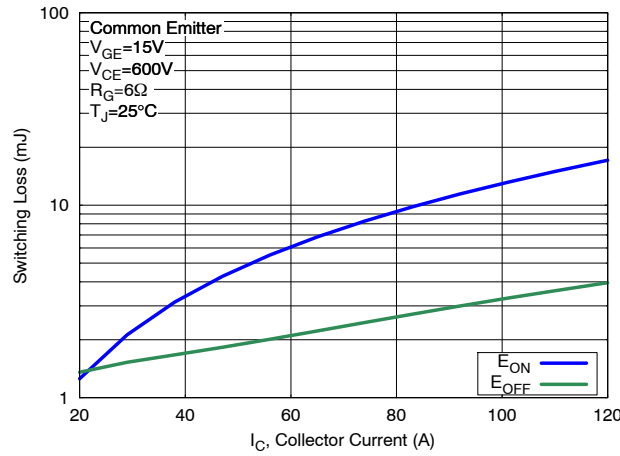


Figure 15. Switching Loss vs Collector Current

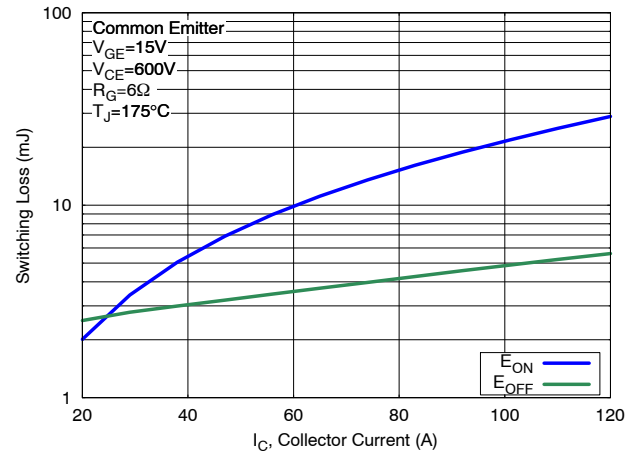


Figure 16. Switching Loss vs Collector Current

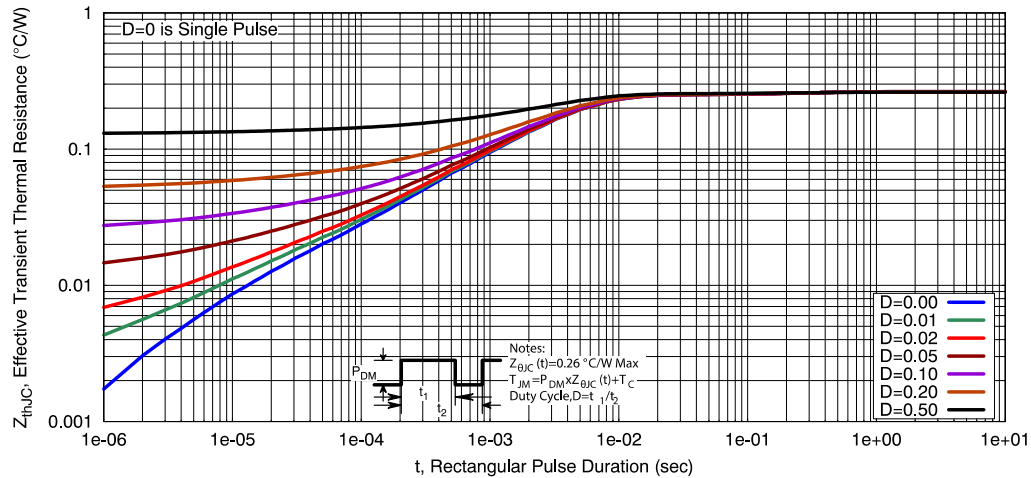


Figure 17. Transient Thermal Impedance of IGBT

TO-247-4LD
CASE 340CJ
ISSUE A

DATE 16 SEP 2019


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