onsemi

MOSFET – Power, Single N-Channel, μ8FL

60	V,	22.6	mΩ,	24 A	
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NTTFS024N06C

Features

- Small Footprint (3.3 x 3.3 mm) for Compact Design
- Low R_{DS(on)} to Minimize Conduction Losses
- Low Q_G and Capacitance to Minimize Driver Losses
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- Power Tools, Battery Operated Vacuums
- UAV/Drones, Material Handling
- BMS/Storage, Home Automation

MAXIMUM RATINGS (T_J = 25° C unless otherwise noted)

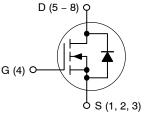
Parar	Symbol	Value	Unit		
Drain-to-Source Voltag	V _{DSS}	60	V		
Gate-to-Source Voltage	V _{GS}	±20	V		
Continuous Drain		$T_{C} = 25^{\circ}C$	I _D	24	А
Current R _{θJC} (Notes 1, 3)	Steady	$T_{C} = 100^{\circ}C$		17	
Power Dissipation	State	$T_{C} = 25^{\circ}C$	PD	28	W
R _{θJC} (Note 1)		T _C = 100°C		14	
Continuous Drain	Steady State	T _A = 25°C	I _D	7	А
Current R _{θJA} (Notes 1, 2, 3)		T _A = 100°C		5	
Power Dissipation		$T_A = 25^{\circ}C$	PD	2.5	W
R _{θJA} (Notes 1, 2)		T _A = 100°C		1.2	
Pulsed Drain Current	Pulsed Drain Current $T_A = 25^{\circ}C, t_p = 10 \ \mu s$			112	А
Operating Junction and Storage Temperature Range			T _J , T _{stg}	–55 to +175	°C
Source Current (Body Diode)			ا _S	23	А
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 5.3 A)			E _{AS}	14	mJ
Lead Temperature Soldering Reflow for Soldering Purposes (1/8" from case for 10 s)			ΤL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Surface-mounted on FR4 board using a 650 $\rm mm^2,$ 2 oz. Cu pad.
- 3. Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

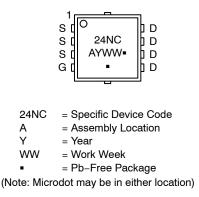
V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX
60 V	22.6 m Ω @ 10 V	24 A







MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State (Note 2)	$R_{ extsf{ heta}JC}$	5.3	°C/W
Junction-to-Ambient - Steady State (Note 2)	R_{\thetaJA}	59.8	

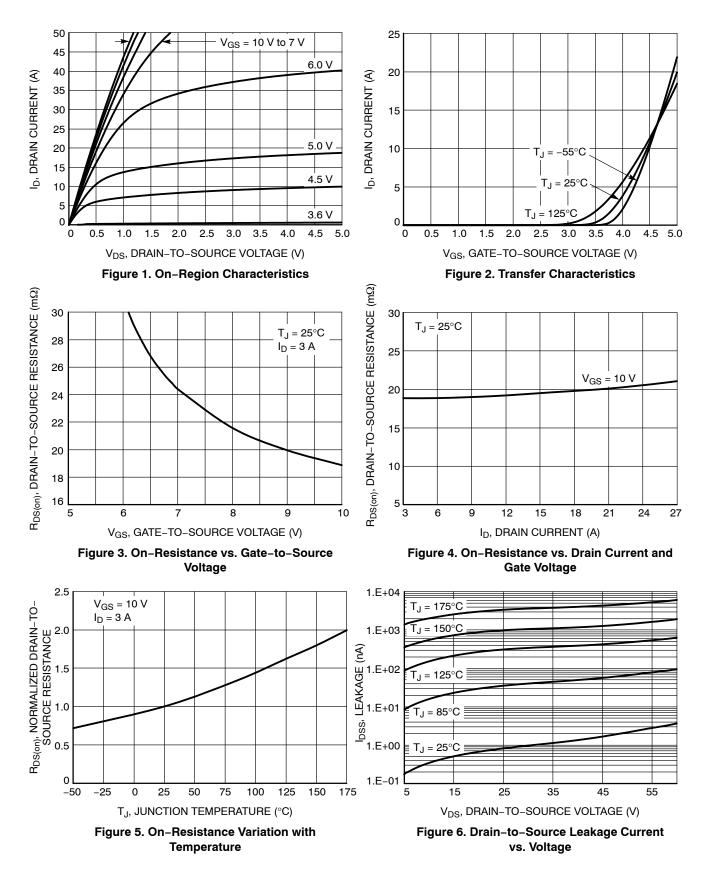
4. Surface-mounted on FR4 board using a 650 mm², 2 oz. Cu pad.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

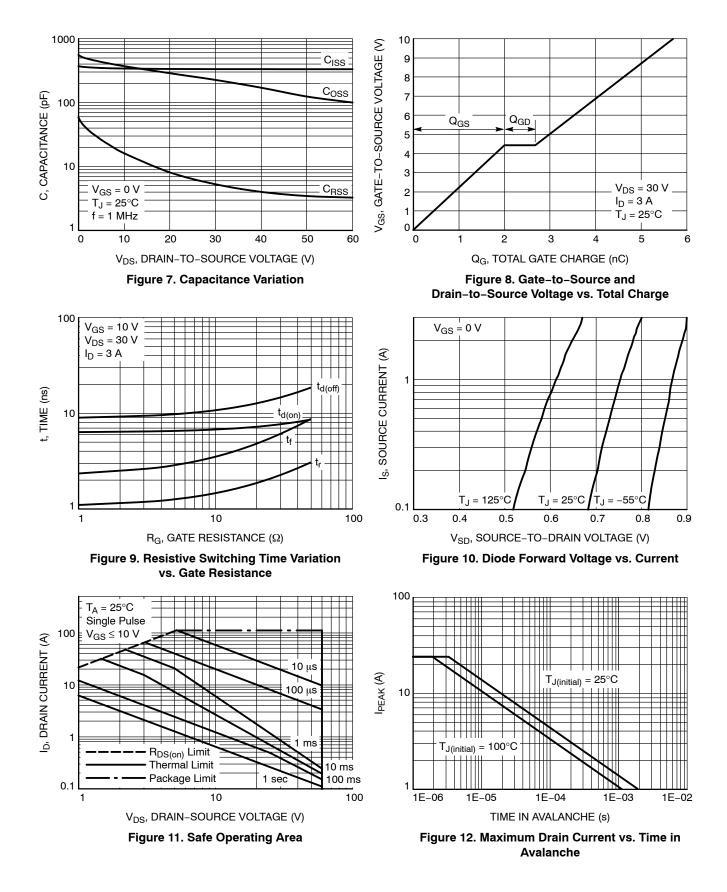
Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS						-	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I _D = 250 μ A		60			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I_D = 250 μ A, referenced to 25°C			27		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$, $T_J = 25^{\circ}C$				10	μA
		$V_{\rm DS} = 60 \rm V$	T _J = 125°C			250	
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 V, V_{G}$	_S = 20 V			100	nA
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{D}$	= 20 μA	2.0		4.0	V
Negative Treshold Temperature Coefficient	V _{GS(TH)} /T _J	$I_D = 20 \ \mu$ A, referenced to 25°C			-7.8		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V, I _D = 3 A			18.8	22.6	mΩ
Forward Transconductance	9 _{FS}	$V_{DS} = 5 V, I_D = 3 A$			10		S
Gate-Resistance	R _G	$T_A = 25^{\circ}C$			0.8		Ω
CHARGES AND CAPACITANCES			1				
Input Capacitance	C _{iss}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 30 V			333		pF
Output Capacitance	C _{oss}				225		
Reverse Transfer Capacitance	C _{rss}				5.05		
Total Gate Charge	Q _{G(TOT)}	V_{GS} = 10 V, V_{DS} = 30 V, I_{D} = 3 A			5.7		nC
Threshold Gate Charge	Q _{G(TH)}				1.3		-
Gate-to-Source Charge	Q _{GS}				2.0		
Gate-to-Drain Charge	Q _{GD}				0.68		
SWITCHING CHARACTERISTICS (No	te 6)						
Turn-On Delay Time	t _{d(on)}				6.6		ns
Rise Time	t _r	Vcc = 10 V. Vp	e = 30 V.		1.3		
Turn-Off Delay Time	t _{d(off)}	V _{GS} = 10 V, V _D I _D = 3 A, R _G	$= 6 \Omega$		10		
Fall Time	t _f				3.0		
DRAIN-SOURCE DIODE CHARACTER	RISTICS						•
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V,	$T_J = 25^{\circ}C$		0.8	1.2	V
		$I_{\rm S} = 3 \rm A$	T _J = 125°C		0.66		
Reverse Recovery Time	t _{RR}	l			23		ns
Charge Time	ta	V _{GS} = 0 V, dI _S /dt = 100 A/μs, V _{DS} = 30 V, I _S = 2 A			11		1
Discharge Time	t _b				12		1
Reverse Recovery Charge	Q _{RR}				11	ļ	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
5. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2%.
6. Switching characteristics are independent of operating junction temperatures.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (continued)



TYPICAL CHARACTERISTICS (continued)

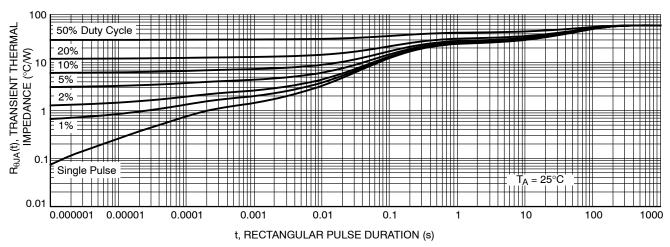


Figure 13. Thermal Response

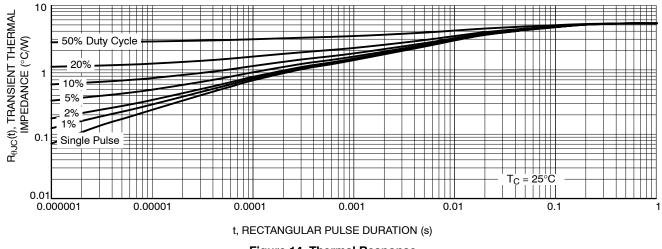


Figure 14. Thermal Response

DEVICE ORDERING INFORMATION

Device	Marking	Package	Shipping [†]
NTTFS024N06CTAG	24NC	μ8FL (Pb–Free)	1500 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, <u>BRD8011/D</u>.





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