Octal Buffer/Line Driver with 3-State Outputs
74AC541, 74ACT541

General Description
The 74AC541 and 74ACT541 are octal buffer/line drivers designed to be employed as memory and address drivers, clock drivers and bus oriented transmitter/receivers.
These devices are similar in function to the 74AC244 and 74ACTC244 while providing flow-through architecture (inputs on opposite side from outputs). This pinout arrangement makes these devices especially useful as an output port for microprocessors, allowing ease of layout and greater PC board density.

Features
• ICC and IOZ Reduced by 50%
• 3−State Outputs
• Inputs and Outputs Opposite Side of Package, allowing easier Interface to Microprocessors
• Outputs Source/Sink 24 mA
• 74AC541 is a Non−inverting Option of the 74AC540
• 74ACT541 has TTL−compatible Inputs
• These are Pb−Free Devices

IEEE/IEC

Truth Table

<table>
<thead>
<tr>
<th>Inputs</th>
<th>Outputs</th>
</tr>
</thead>
<tbody>
<tr>
<td>(\overline{OE}_1)</td>
<td>(\overline{OE}_2)</td>
</tr>
<tr>
<td>L</td>
<td>L</td>
</tr>
<tr>
<td>H</td>
<td>X</td>
</tr>
<tr>
<td>X</td>
<td>H</td>
</tr>
<tr>
<td>L</td>
<td>L</td>
</tr>
</tbody>
</table>

H = HIGH Voltage Level
L = LOW Voltage Level
X = Immaterial
Z = High Impedance

Figure 1. Connection Diagram
Figure 2. Logic Symbol

MARKING DIAGRAMS

SOIC−20W
Case 751D

TSSOP−20
Case 948E

SOIC−20W
Case 751BJ

TSSOP−20
Case 948AQ

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

(© Semiconductor Components Industries, LLC, 1988
August, 2023 – Rev. 2
Publication Order Number:
74ACT541/D)
## ABSOLUTE MAXIMUM RATINGS

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Rating</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>VCC</td>
<td>Supply Voltage</td>
<td>−0.5 to +7.0</td>
<td>V</td>
</tr>
<tr>
<td>I(_{IK})</td>
<td>DC Input Diode Current</td>
<td>−20</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td>(V_I = −0.5) V</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>(V_I = V_{CC} + 0.5) V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>V(_{IIK})</td>
<td>DC Input Voltage</td>
<td>−0.5 to (V_{CC} + 0.5) V</td>
<td>V</td>
</tr>
<tr>
<td>I(_{OK})</td>
<td>DC Output Diode Current</td>
<td>−20</td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td>(V_O = −0.5) V</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>(V_O = V_{CC} + 0.5) V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>V(_{OD})</td>
<td>DC Output Voltage</td>
<td>−0.5 to (V_{CC} + 0.5) V</td>
<td>V</td>
</tr>
<tr>
<td>I(_{O})</td>
<td>DC Output Source or Sink Current</td>
<td>±50</td>
<td>mA</td>
</tr>
<tr>
<td>I(<em>{CC}) or I(</em>{GND})</td>
<td>DC (V_{CC}) or Ground Current per Output Pin</td>
<td>±50</td>
<td>mA</td>
</tr>
<tr>
<td>T(_{STG})</td>
<td>Storage Temperature</td>
<td>−65 to +150</td>
<td>°C</td>
</tr>
<tr>
<td>T(_{J})</td>
<td>Junction Temperature</td>
<td>140</td>
<td>°C</td>
</tr>
</tbody>
</table>

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## RECOMMENDED OPERATING CONDITIONS

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Min</th>
<th>Max</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>VCC</td>
<td>Supply Voltage</td>
<td>2.0</td>
<td>6.0</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>(V_{CC})</td>
<td>4.5</td>
<td>5.5</td>
<td></td>
</tr>
<tr>
<td>V(_{I})</td>
<td>Input Voltage</td>
<td>0</td>
<td>(V_{CC})</td>
<td>V</td>
</tr>
<tr>
<td>V(_{O})</td>
<td>Output Voltage</td>
<td>0</td>
<td>(V_{CC})</td>
<td>V</td>
</tr>
<tr>
<td>T(_{A})</td>
<td>Operating Temperature</td>
<td>−40</td>
<td>85</td>
<td>°C</td>
</tr>
<tr>
<td>(\Delta V/\Delta t)</td>
<td>Minimum Input Edge Rate, AC Devices: (V_{IN}) from 30% to 70% (V_{CC}), (V_{CC} @ 3.3) V, (4.5) V, (5.5) V</td>
<td>125</td>
<td>mV/ns</td>
<td></td>
</tr>
<tr>
<td>(\Delta V/\Delta t)</td>
<td>Minimum Input Edge Rate, ACT Devices: (V_{IN}) from 0.8 V to 2.0 V (V_{CC} @ 4.5) V, (5.5) V</td>
<td>125</td>
<td>mV/ns</td>
<td></td>
</tr>
</tbody>
</table>

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.
## DC ELECTRICAL CHARACTERISTICS FOR AC

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>VCC (V)</th>
<th>Conditions</th>
<th>$T_A = +25^\circ$C</th>
<th>$T_A = -40^\circ$C to $+85^\circ$C</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td>Typ.</td>
<td>Guaranteed Limits</td>
<td></td>
</tr>
<tr>
<td>$V_{IH}$</td>
<td>Minimum HIGH Level Input Voltage</td>
<td>3.0</td>
<td>$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$</td>
<td>1.5</td>
<td>2.1</td>
<td>2.1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5</td>
<td></td>
<td>2.25</td>
<td>3.15</td>
<td>3.15</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td></td>
<td>2.75</td>
<td>3.85</td>
<td>3.85</td>
</tr>
<tr>
<td>$V_{IL}$</td>
<td>Maximum LOW Level Input Voltage</td>
<td>3.0</td>
<td>$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$</td>
<td>1.5</td>
<td>0.9</td>
<td>0.9</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5</td>
<td></td>
<td>2.25</td>
<td>1.35</td>
<td>1.35</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td></td>
<td>2.75</td>
<td>1.65</td>
<td>1.65</td>
</tr>
<tr>
<td>$V_{OH}$</td>
<td>Minimum HIGH Level Output Voltage</td>
<td>3.0</td>
<td>$I_{OUT} = -50 \mu\text{A}$</td>
<td>2.99</td>
<td>2.9</td>
<td>2.9</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5</td>
<td></td>
<td>4.49</td>
<td>4.4</td>
<td>4.4</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td></td>
<td>5.49</td>
<td>5.4</td>
<td>5.4</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3.0</td>
<td>$V_{IN} = V_{IL} \text{ or } V_{IH}, I_{OH} = -12 \text{ mA}$</td>
<td>2.56</td>
<td>2.46</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5</td>
<td>$V_{IN} = V_{IL} \text{ or } V_{IH}, I_{OH} = -24 \text{ mA}$</td>
<td>3.86</td>
<td>3.76</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td>$V_{IN} = V_{IL} \text{ or } V_{IH}, I_{OH} = -24 \text{ mA}$</td>
<td>4.86</td>
<td>4.76</td>
<td></td>
</tr>
<tr>
<td>$V_{OL}$</td>
<td>Maximum LOW Level Output Voltage</td>
<td>3.0</td>
<td>$I_{OUT} = 50 \mu\text{A}$</td>
<td>0.002</td>
<td>0.1</td>
<td>0.1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5</td>
<td></td>
<td>0.001</td>
<td>0.1</td>
<td>0.1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td></td>
<td>0.001</td>
<td>0.1</td>
<td>0.1</td>
</tr>
<tr>
<td></td>
<td></td>
<td>3.0</td>
<td>$V_{IN} = V_{IL} \text{ or } V_{IH}, I_{OL} = 12 \text{ mA}$</td>
<td>0.36</td>
<td>0.44</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5</td>
<td>$V_{IN} = V_{IL} \text{ or } V_{IH}, I_{OL} = 24 \text{ mA}$</td>
<td>0.36</td>
<td>0.44</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td>$V_{IN} = V_{IL} \text{ or } V_{IH}, I_{OL} = 24 \text{ mA}$</td>
<td>0.36</td>
<td>0.44</td>
<td></td>
</tr>
<tr>
<td>$I_{IN}$ (Note 2)</td>
<td>Maximum Input Leakage Current</td>
<td>5.5</td>
<td>$V_{I} = V_{CC}, GND$</td>
<td>±0.1</td>
<td>±1.0</td>
<td>μA</td>
</tr>
<tr>
<td>$I_{OZ}$</td>
<td>Maximum 3–STATE Leakage Current</td>
<td>5.5</td>
<td>$V_{I (OE)} = V_{IL}, V_{IH}, V_{I} = V_{CC}, GND; V_{O} = V_{CC}, GND$</td>
<td>±0.25</td>
<td>±2.5</td>
<td>μA</td>
</tr>
<tr>
<td>$I_{OLD}$</td>
<td>Minimum Dynamic Output Current (Note 3)</td>
<td>5.5</td>
<td>$V_{OLD} = 1.65 \text{ V Max.}$</td>
<td>75</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$I_{OH}$</td>
<td>Minimum Quiescent Supply Current</td>
<td>5.5</td>
<td>$V_{OHD} = 3.85 \text{ V Min.}$</td>
<td>–75</td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$I_{CC}$ (Note 2)</td>
<td>Maximum Quiescent Supply Current</td>
<td>5.5</td>
<td>$V_{IN} = V_{CC} \text{ or } GND$</td>
<td>4.0</td>
<td>40.0</td>
<td>μA</td>
</tr>
</tbody>
</table>

1. All outputs loaded; thresholds on input associated with output under test.
2. $I_{IN}$ and $I_{CC}$ @ 3.0 V are guaranteed to be less than or equal to the respective limit @ 5.5 V $V_{CC}$.
3. Maximum test duration 2.0 ms, one output loaded at a time.
### DC ELECTRICAL CHARACTERISTICS FOR ACT

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>$V_{CC}$ (V)</th>
<th>Conditions</th>
<th>$T_A = +25^\circ$C</th>
<th>$T_A = -40^\circ$C to $+85^\circ$C</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{IH}$</td>
<td>Minimum HIGH Level Input Voltage</td>
<td>4.5</td>
<td>$V_{OUT} = 0.1,V$ or $V_{CC} = 0.1,V$</td>
<td>1.5</td>
<td>1.5</td>
<td>2.0</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td></td>
<td>1.5</td>
<td>1.5</td>
<td>2.0</td>
</tr>
<tr>
<td>$V_{IL}$</td>
<td>Maximum LOW Level Input Voltage</td>
<td>4.5</td>
<td>$V_{OUT} = 0.1,V$ or $V_{CC} = 0.1,V$</td>
<td>1.5</td>
<td>1.5</td>
<td>0.8</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td></td>
<td>1.5</td>
<td>1.5</td>
<td>0.8</td>
</tr>
<tr>
<td>$V_{OH}$</td>
<td>Minimum HIGH Level Output Voltage</td>
<td>4.5</td>
<td>$I_{OUT} = -50,\mu A$</td>
<td>4.49</td>
<td>4.49</td>
<td>4.49</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td></td>
<td>5.49</td>
<td>5.49</td>
<td>5.49</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5</td>
<td>$V_{IN} = V_{IL}$ or $V_{IH}$, $I_{OH} = -24,mA$</td>
<td>3.86</td>
<td>3.86</td>
<td>3.76</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td>$V_{IN} = V_{IL}$ or $V_{IH}$, $I_{OH} = -24,mA$</td>
<td>4.86</td>
<td>4.86</td>
<td>4.76</td>
</tr>
<tr>
<td>$V_{OL}$</td>
<td>Maximum LOW Level Output Voltage</td>
<td>4.5</td>
<td>$I_{OUT} = 50,\mu A$</td>
<td>0.001</td>
<td>0.001</td>
<td>0.001</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td></td>
<td>0.001</td>
<td>0.001</td>
<td>0.001</td>
</tr>
<tr>
<td></td>
<td></td>
<td>4.5</td>
<td>$V_{IN} = V_{IL}$ or $V_{IH}$, $I_{OL} = 24,mA$</td>
<td>0.36</td>
<td>0.36</td>
<td>0.44</td>
</tr>
<tr>
<td></td>
<td></td>
<td>5.5</td>
<td>$V_{IN} = V_{IL}$ or $V_{IH}$, $I_{OL} = 24,mA$</td>
<td>0.36</td>
<td>0.36</td>
<td>0.44</td>
</tr>
<tr>
<td>$I_{IN}$</td>
<td>Maximum Input Leakage Current</td>
<td>5.5</td>
<td>$V_{I} = V_{CC}$, GND</td>
<td>$\pm0.1$</td>
<td>$\pm0.1$</td>
<td>$\pm1.0$</td>
</tr>
<tr>
<td>$I_{OZ}$</td>
<td>Maximum 3–STATE Leakage Current</td>
<td>5.5</td>
<td>$V_{I} = V_{IL}$, $V_{IH}$, $V_{O} = V_{CC}$, GND</td>
<td>$\pm0.25$</td>
<td>$\pm0.25$</td>
<td>$\pm2.5$</td>
</tr>
<tr>
<td>$I_{CCT}$</td>
<td>Maximum ICC/Input</td>
<td>5.5</td>
<td>$V_{I} = V_{CC} - 2.1,V$</td>
<td>0.6</td>
<td>0.6</td>
<td>1.5</td>
</tr>
<tr>
<td>$I_{OLD}$</td>
<td>Minimum Dynamic Output Current (Note 5)</td>
<td>5.5</td>
<td>$V_{OLD} = 1.65,V$ Max.</td>
<td>75</td>
<td>75</td>
<td>mA</td>
</tr>
<tr>
<td>$I_{OHD}$</td>
<td>Maximum Quiescent Supply Current</td>
<td>5.5</td>
<td>$V_{DH} = 3.85,V$ Min.</td>
<td>$-75$</td>
<td>$-75$</td>
<td>mA</td>
</tr>
<tr>
<td>$I_{CC}$</td>
<td>Maximum Quiescent Supply Current</td>
<td>5.5</td>
<td>$V_{IN} = V_{CC}$ or GND</td>
<td>4.0</td>
<td>4.0</td>
<td>40.0</td>
</tr>
</tbody>
</table>

4. All outputs loaded; thresholds on input associated with output under test.
5. Maximum test duration 2.0 ms, one output loaded at a time.
## AC ELECTRICAL CHARACTERISTICS FOR AC

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>( V_{CC} ) (Note 6)</th>
<th>( TA = +25^\circ \text{C}, C_L = 50 \text{ pF} )</th>
<th>( TA = -40^\circ \text{C} ) to (+85^\circ \text{C}, C_L = 50 \text{ pF} )</th>
<th>Units</th>
</tr>
</thead>
</table>
| \( t_{PLH} \) | Propagation Delay, Data to Output | \[
\begin{array}{ccc}
3.3 & 2.0 & 5.5 \\
5.0 & 1.5 & 4.0 \\
\end{array}
\] | \[
\begin{array}{ccc}
8.0 & 1.5 & 9.0 \\
6.0 & 1.0 & 6.5 \\
\end{array}
\] | ns |
| \( t_{PHL} \) | Propagation Delay, Data to Output | \[
\begin{array}{ccc}
3.3 & 2.0 & 5.5 \\
5.0 & 1.5 & 4.0 \\
\end{array}
\] | \[
\begin{array}{ccc}
8.0 & 1.5 & 9.0 \\
6.0 & 1.0 & 6.5 \\
\end{array}
\] | ns |
| \( t_{PZH} \) | Output Enable Time | \[
\begin{array}{ccc}
3.3 & 3.0 & 8.0 \\
5.0 & 2.0 & 6.0 \\
\end{array}
\] | \[
\begin{array}{ccc}
11.5 & 3.0 & 12.5 \\
8.5 & 1.5 & 9.5 \\
\end{array}
\] | ns |
| \( t_{PZL} \) | Output Enable Time | \[
\begin{array}{ccc}
3.3 & 2.5 & 7.0 \\
5.0 & 1.5 & 5.5 \\
\end{array}
\] | \[
\begin{array}{ccc}
10.0 & 2.5 & 11.5 \\
7.5 & 1.0 & 8.5 \\
\end{array}
\] | ns |
| \( t_{PHZ} \) | Output Disable Time | \[
\begin{array}{ccc}
3.3 & 3.5 & 9.0 \\
5.0 & 2.0 & 7.0 \\
\end{array}
\] | \[
\begin{array}{ccc}
12.5 & 2.5 & 14.0 \\
9.5 & 1.0 & 10.5 \\
\end{array}
\] | ns |
| \( t_{PLZ} \) | Output Disable Time | \[
\begin{array}{ccc}
3.3 & 2.5 & 6.5 \\
5.0 & 2.0 & 5.5 \\
\end{array}
\] | \[
\begin{array}{ccc}
9.5 & 2.0 & 10.5 \\
7.5 & 1.0 & 8.5 \\
\end{array}
\] | ns |

6. Voltage range 3.3 is 3.3 V ± 0.3 V. Voltage range 5.0 is 5.0 V ± 0.5 V.

## AC ELECTRICAL CHARACTERISTICS FOR ACT

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>( V_{CC} ) (Note 7)</th>
<th>( TA = +25^\circ \text{C}, C_L = 50 \text{ pF} )</th>
<th>( TA = -40^\circ \text{C} ) to (+85^\circ \text{C}, C_L = 50 \text{ pF} )</th>
<th>Units</th>
</tr>
</thead>
</table>
| \( t_{PLH} \) | Propagation Delay, Data to Output | \[
\begin{array}{ccc}
5.0 & 2.0 & 4.5 \\
5.0 & 2.0 & 5.5 \\
\end{array}
\] | \[
\begin{array}{ccc}
7.0 & 2.0 & 7.5 \\
7.0 & 2.0 & 7.5 \\
\end{array}
\] | ns |
| \( t_{PHL} \) | Propagation Delay, Data to Output | \[
\begin{array}{ccc}
5.0 & 2.0 & 5.5 \\
5.0 & 2.0 & 6.5 \\
\end{array}
\] | \[
\begin{array}{ccc}
9.0 & 2.0 & 9.5 \\
9.0 & 2.0 & 9.5 \\
\end{array}
\] | ns |
| \( t_{PZH} \) | Output Enable Time | \[
\begin{array}{ccc}
5.0 & 2.0 & 5.0 \\
5.0 & 1.5 & 5.5 \\
\end{array}
\] | \[
\begin{array}{ccc}
9.0 & 2.0 & 9.5 \\
7.5 & 1.5 & 8.0 \\
\end{array}
\] | ns |
| \( t_{PZL} \) | Output Enable Time | \[
\begin{array}{ccc}
5.0 & 2.0 & 6.5 \\
5.0 & 1.5 & 5.5 \\
\end{array}
\] | \[
\begin{array}{ccc}
9.5 & 2.0 & 9.5 \\
7.5 & 1.5 & 8.0 \\
\end{array}
\] | ns |
| \( t_{PHZ} \) | Output Disable Time | \[
\begin{array}{ccc}
5.0 & 2.0 & 5.5 \\
5.0 & 1.5 & 5.5 \\
\end{array}
\] | \[
\begin{array}{ccc}
7.5 & 1.5 & 8.0 \\
7.5 & 1.5 & 8.0 \\
\end{array}
\] | ns |
| \( t_{PLZ} \) | Output Disable Time | \[
\begin{array}{ccc}
5.0 & 2.0 & 5.5 \\
5.0 & 1.5 & 5.5 \\
\end{array}
\] | \[
\begin{array}{ccc}
7.5 & 1.5 & 8.0 \\
7.5 & 1.5 & 8.0 \\
\end{array}
\] | ns |

7. Voltage range 5.0 is 5.0 V ± 0.5 V.

## CAPACITANCE

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Parameter</th>
<th>Conditions</th>
<th>Typ.</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>( C_{IN} )</td>
<td>Input Capacitance</td>
<td>( V_{CC} = \text{OPEN} )</td>
<td>4.5</td>
<td>pF</td>
</tr>
<tr>
<td>( C_{PD} )</td>
<td>Power Dissipation Capacitance for AC</td>
<td>( V_{CC} = 5.0 \text{ V} )</td>
<td>30.0</td>
<td>pF</td>
</tr>
<tr>
<td>Power Dissipation Capacitance for ACT</td>
<td></td>
<td></td>
<td>70.0</td>
<td></td>
</tr>
</tbody>
</table>
## ORDERING INFORMATION

<table>
<thead>
<tr>
<th>Device</th>
<th>Device Marking</th>
<th>Package</th>
<th>Shipping†</th>
</tr>
</thead>
<tbody>
<tr>
<td>74AC541MTCX</td>
<td>AC541</td>
<td>TSSOP–20, case 948AQ (Pb−Free)</td>
<td>2500 Units / Tape &amp; Reel</td>
</tr>
<tr>
<td>74AC541SC</td>
<td>AC541</td>
<td>SOIC–20W, case 751D (Pb−Free)</td>
<td>38 Units / Tube</td>
</tr>
<tr>
<td>74AC541SCX</td>
<td>AC541</td>
<td>SOIC–20W, case 751BJ (Pb−Free)</td>
<td>1000 Units / Tape &amp; Reel</td>
</tr>
<tr>
<td>74ACT541MTCX</td>
<td>ACT541</td>
<td>TSSOP–20, case 948AQ (Pb−Free)</td>
<td>2500 Units / Tape &amp; Reel</td>
</tr>
<tr>
<td>74ACT541SC</td>
<td>ACT541</td>
<td>SOIC–20W, case 751D (Pb−Free)</td>
<td>38 Units / Tube</td>
</tr>
<tr>
<td>74ACT541SCX</td>
<td>ACT541</td>
<td>SOIC–20W, case 751BJ (Pb−Free)</td>
<td>1000 Units / Tape &amp; Reel</td>
</tr>
</tbody>
</table>

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

**NOTE:** All packages are lead free per JEDEC: J−STD−020B standard.
SOIC–20, 300 mils
CASE 751BJ–01
ISSUE O

DATE 19 DEC 2008

Notes:
(1) All dimensions are in millimeters. Angles in degrees.
(2) Complies with JEDEC MS-013.
SOIC–20 WB
CASE 751D–05
ISSUE H
DATE 22 APR 2015

NOTES:
1. DIMENSIONS ARE IN MILLIMETERS.
3. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
5. DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF B DIMENSION AT MAXIMUM MATERIAL CONDITION.

RECOMMENDED SOLDERING FOOTPRINT*

DIMENSIONS: MILLIMETERS

PITCH

1.27

DIMENSIONS:

A1

0.18

0.25

b

0.35

0.49

c

0.23

0.32

D

12.65

12.95

E

7.40

7.60

e

1.27

bsc

H

10.05

10.55

h

0.25

0.75

L

0.50

0.90

g

6

7

A

A1

B

C

D

E

H

m

1

10

11.00

1

20

20X

0.52

1.30

20X

1

10

11

20

XX

XXXXXXX

XXXXXXX

AWLYYYYY

WWG

WL

YY

WW

G

XXXX = Specific Device Code
A = Assembly Location
WL = Wafer Lot
YY = Year
WW = Work Week
G = Pb–Free Package

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, “G” or microdot “/”, may or may not be present. Some products may not follow the Generic Marking.
NOTES:
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
5. DIMENSION K DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 (0.003) TOTAL IN EXCESS OF THE K DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. TERMINAL NUMBERS ARE SHOWN FOR REFERENCE ONLY.
7. DIMENSION A AND B ARE TO BE DETERMINED AT DATUM PLANE −W−.

DIMENSIONS: MILLIMETERS

<table>
<thead>
<tr>
<th>MILLIMETERS</th>
<th>INCHES</th>
</tr>
</thead>
<tbody>
<tr>
<td>MIN</td>
<td>MAX</td>
</tr>
<tr>
<td>A</td>
<td>6.40</td>
</tr>
<tr>
<td>B</td>
<td>4.30</td>
</tr>
<tr>
<td>C</td>
<td>0.00</td>
</tr>
<tr>
<td>D</td>
<td>0.50</td>
</tr>
<tr>
<td>G</td>
<td>0.65 0.66</td>
</tr>
<tr>
<td>H</td>
<td>0.27</td>
</tr>
<tr>
<td>J</td>
<td>0.09</td>
</tr>
<tr>
<td>K</td>
<td>0.19</td>
</tr>
<tr>
<td>L</td>
<td>6.40 0.66</td>
</tr>
<tr>
<td>M</td>
<td>0.10</td>
</tr>
</tbody>
</table>

DIMENSIONS: INCHES

<table>
<thead>
<tr>
<th>MIN</th>
<th>MAX</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.25</td>
<td>0.010</td>
</tr>
</tbody>
</table>

PIN 1 IDENTIFICATION

SEATING PLANE

SOLDERING FOOTPRINT

DIMENSIONS: MILLIMETERS

A = Assembly Location
L = Wafer Lot
Y = Year
W = Work Week
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking.
Pb-Free indicator, “G” or microdot “▪”, may or may not be present.
MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS

TSSOP20, 4.4x6.5
CASE 948AQ-01
ISSUE A

DATE 19 MAR 2009

TOP VIEW

SIDE VIEW

END VIEW

SYMBOL | MIN | NOM | MAX
---|---|---|---
A | 1.20 |  |  
A1 | 0.05 | 0.15 |  
A2 | 0.80 | 1.05 |  
b | 0.19 | 0.30 |  
c | 0.09 | 0.20 |  
D | 6.40 | 6.50 | 6.60 |
E | 6.30 | 6.40 | 6.50 |
E1 | 4.30 | 4.40 | 4.50 |
e | 0.65 BSC |  |  
L | 0.45 | 0.60 | 0.75 |
L1 | | 1.00 REF |  
θ | 0° | 8° |  

Notes:
(1) All dimensions are in millimeters. Angles in degrees.
(2) Complies with JEDEC MO-153.