

# Bipolar Transistor

–50 V, –3 A, Low  $V_{CE(sat)}$ , PNP Single  
TP/TP-FA

## 2SA2126

### Features

- Adoption of MBIT Processes
- High Current Capacitance
- Low Collector-to-Emitter Saturation Voltage
- High-speed Switching

### Applications

- DC / DC Converter, Relay Drivers, Lamp Drivers, Motor Drivers

### Specifications

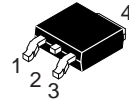
#### ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	$V_{CBO}$		–50	V
Collector-to-Emitter Voltage	$V_{CES}$		–50	V
Collector-to-Emitter Voltage	$V_{CEO}$		–50	V
Emitter-to-Base Voltage	$V_{EBO}$		–6	V
Collector Current	$I_C$		–3	A
Collector Current (Pulse)	$I_{CP}$		–6	A
Base Current	$I_B$		–600	mA
Collector Dissipation	$P_C$		0.8	W
		$T_c = 25^\circ\text{C}$	15	W
Junction Temperature	$T_j$		150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		–55 to +150	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

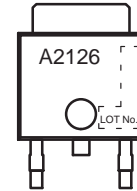


IPAK / TP  
CASE 369AJ

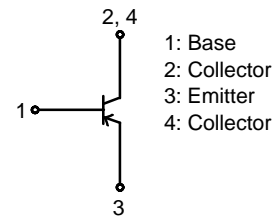


DPAK / TP-FA  
CASE 369AH

### MARKING DIAGRAM



### ELECTRICAL CONNECTION



### ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Parameter	Symbol	Conditions	Ratings			Unit
			Min	Typ	Max	
Collector Cutoff Current	$I_{CBO}$	$V_{CB} = -40 \text{ V}, I_E = 0 \text{ A}$	–	–	–1	$\mu\text{A}$
Emitter Cutoff Current	$I_{EBO}$	$V_{EB} = -4 \text{ V}, I_C = 0 \text{ A}$	–	–	–1	$\mu\text{A}$
DC Current Gain	$h_{FE}$	$V_{CE} = -2 \text{ V}, I_C = -100 \text{ mA}$	200	–	560	
Gain–Bandwidth Product	$f_T$	$V_{CE} = -10 \text{ V}, I_C = -500 \text{ mA}$	–	390	–	MHz
Output Capacitance	$C_{ob}$	$V_{CB} = -10 \text{ V}, f = 1 \text{ MHz}$	–	24	–	pF
Collector–to–Emitter Saturation Voltage	$V_{CE(sat)1}$	$I_C = -1 \text{ A}, I_B = -50 \text{ mA}$	–	–135	–270	mV
	$V_{CE(sat)2}$	$I_C = -2 \text{ A}, I_B = -100 \text{ mA}$	–	–260	–520	mV
Base–to–Emitter Saturation Voltage	$V_{BE(sat)}$	$I_C = -2 \text{ A}, I_B = -100 \text{ mA}$	–	–0.96	–1.2	V
Collector–to–Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C = -10 \mu\text{A}, I_E = 0 \text{ A}$	–50	–	–	V
Collector–to–Emitter Breakdown Voltage	$V_{(BR)CES}$	$I_C = -100 \mu\text{A}, R_{BE} = 0$	–50	–	–	V
Collector–to–Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C = -1 \text{ mA}, R_{BE} = \infty$	–50	–	–	V
Emitter–to–Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E = -10 \mu\text{A}, I_C = 0 \text{ A}$	–6	–	–	V
Turn–On Time	$t_{on}$	See specified Test Circuit.	–	30	–	ns
Storage Time	$t_{stg}$		–	230	–	ns
Fall Time	$t_f$		–	18	–	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit

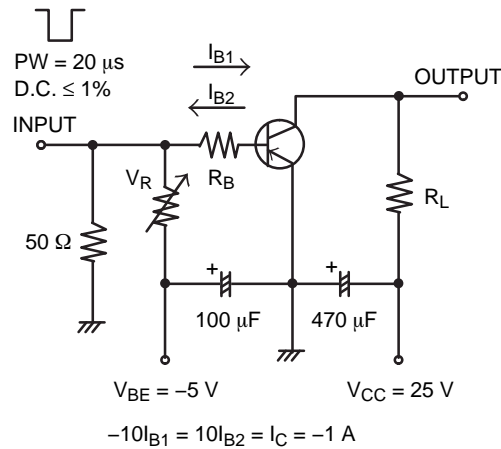


Figure 1. Switching Time Test Circuit

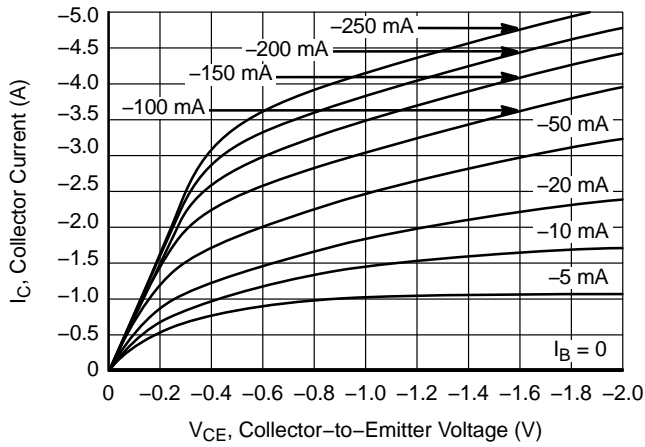


Figure 2.  $I_C - V_{CE}$

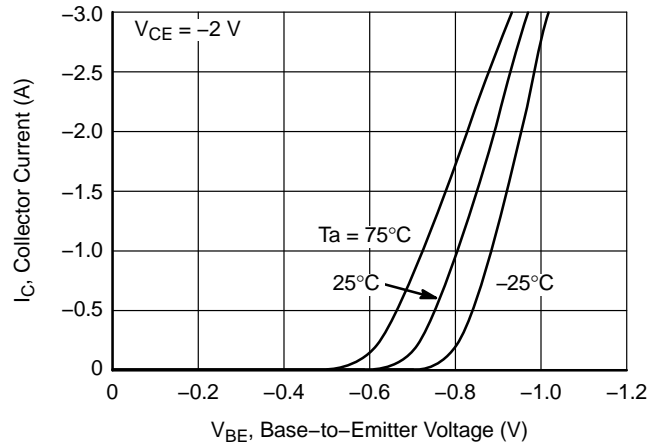


Figure 3.  $I_C - V_{BE}$

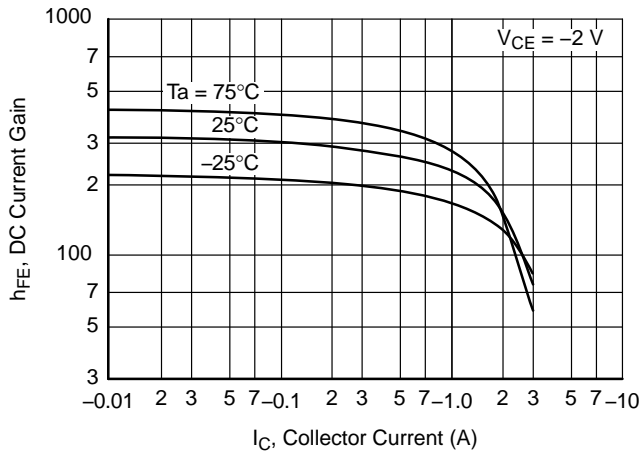


Figure 4.  $h_{FE} - I_C$

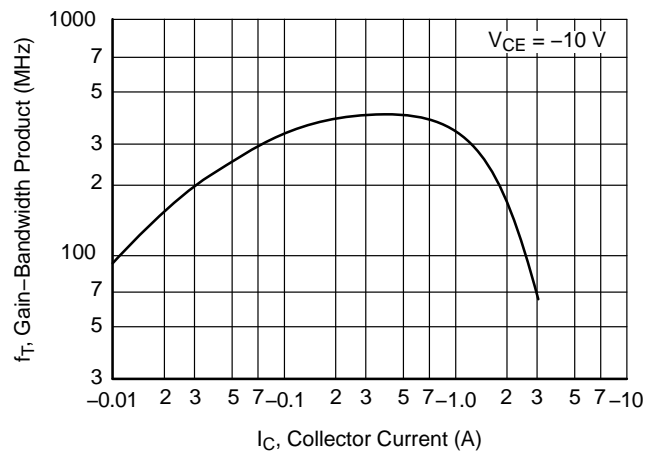


Figure 5.  $f_T - I_C$

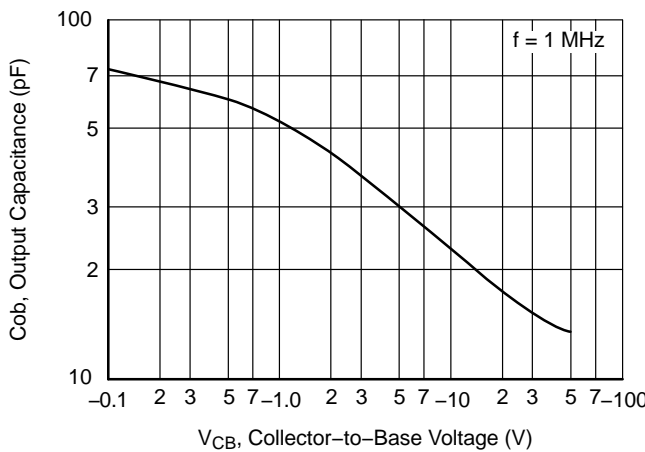


Figure 6.  $C_{ob} - V_{CB}$

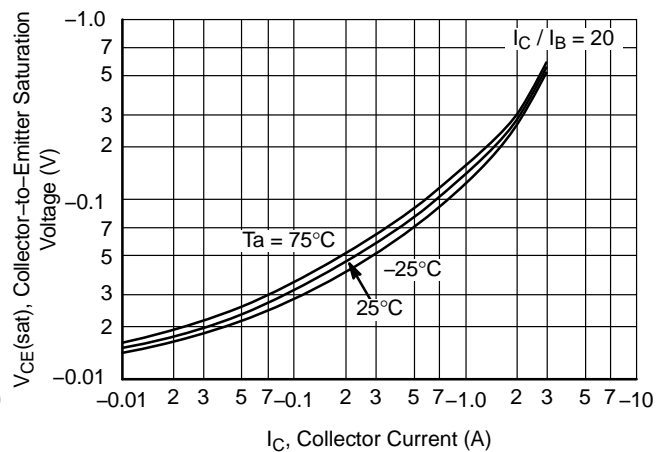


Figure 7.  $V_{CE(sat)} - I_C$

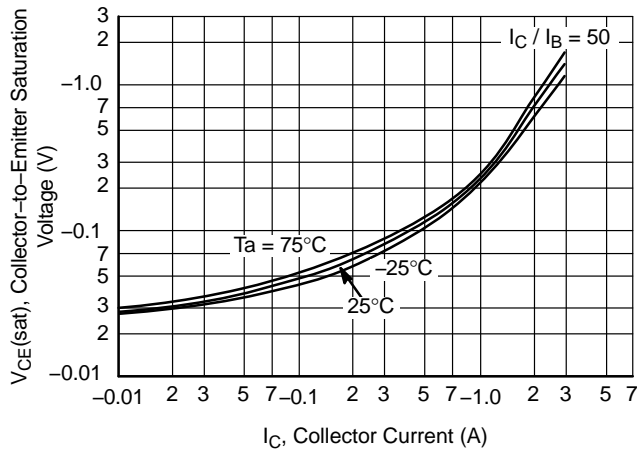


Figure 8.  $V_{CE}(sat) - I_C$

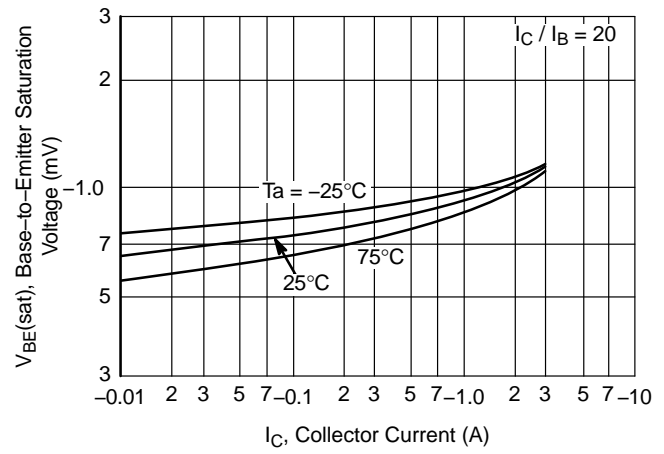


Figure 9.  $V_{BE}(sat) - I_C$

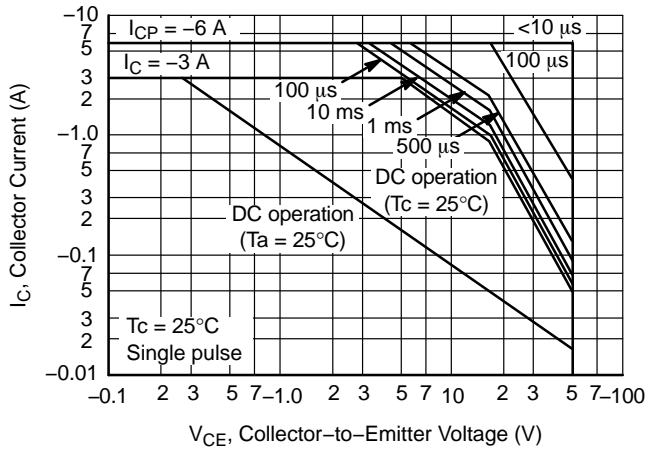


Figure 10. ASO

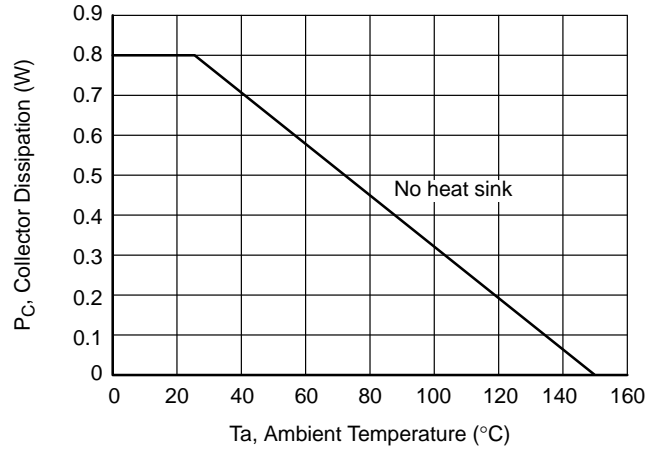


Figure 11.  $P_C - T_a$

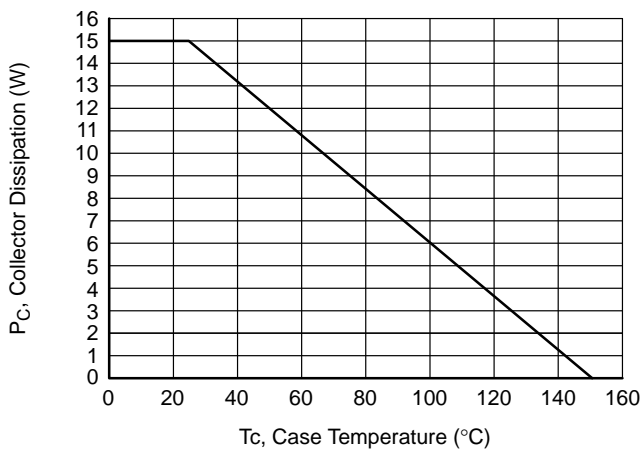


Figure 12.  $P_C - T_c$

## 2SA2126

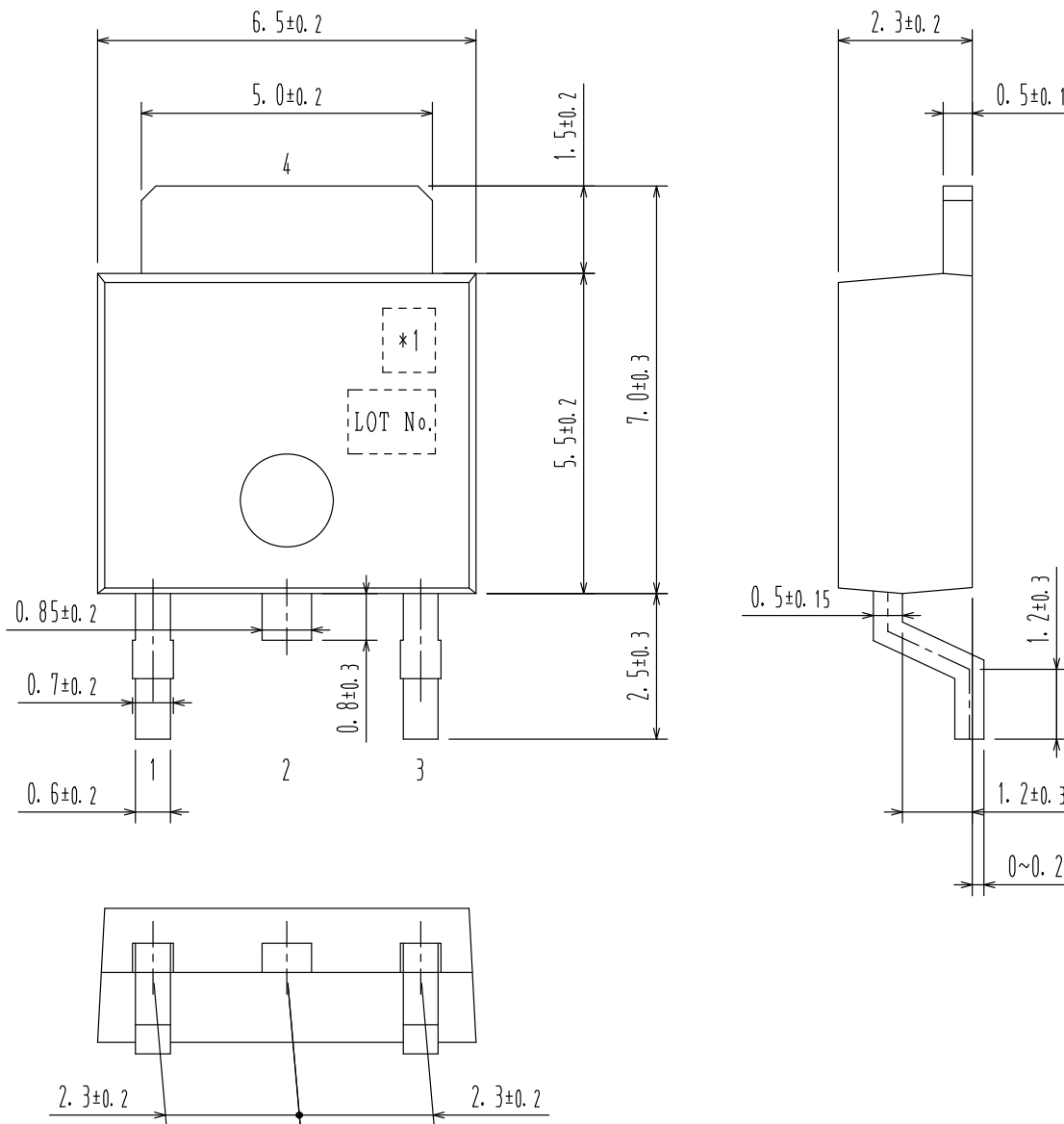
### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
2SA2126-H	TP (Pb-Free, Halogen Free)	500 Units / Bag
2SA2126-TL-E	TP-FA (Pb-Free)	700 Units / Tape & Reel
2SA2126-TL-H	TP-FA (Pb-Free, Halogen Free)	700 Units / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

DPAK / TP-FA  
CASE 369AH  
ISSUE O

DATE 30 JAN 2012



Pin 2 is idle pin with electrical  
designation only carried.

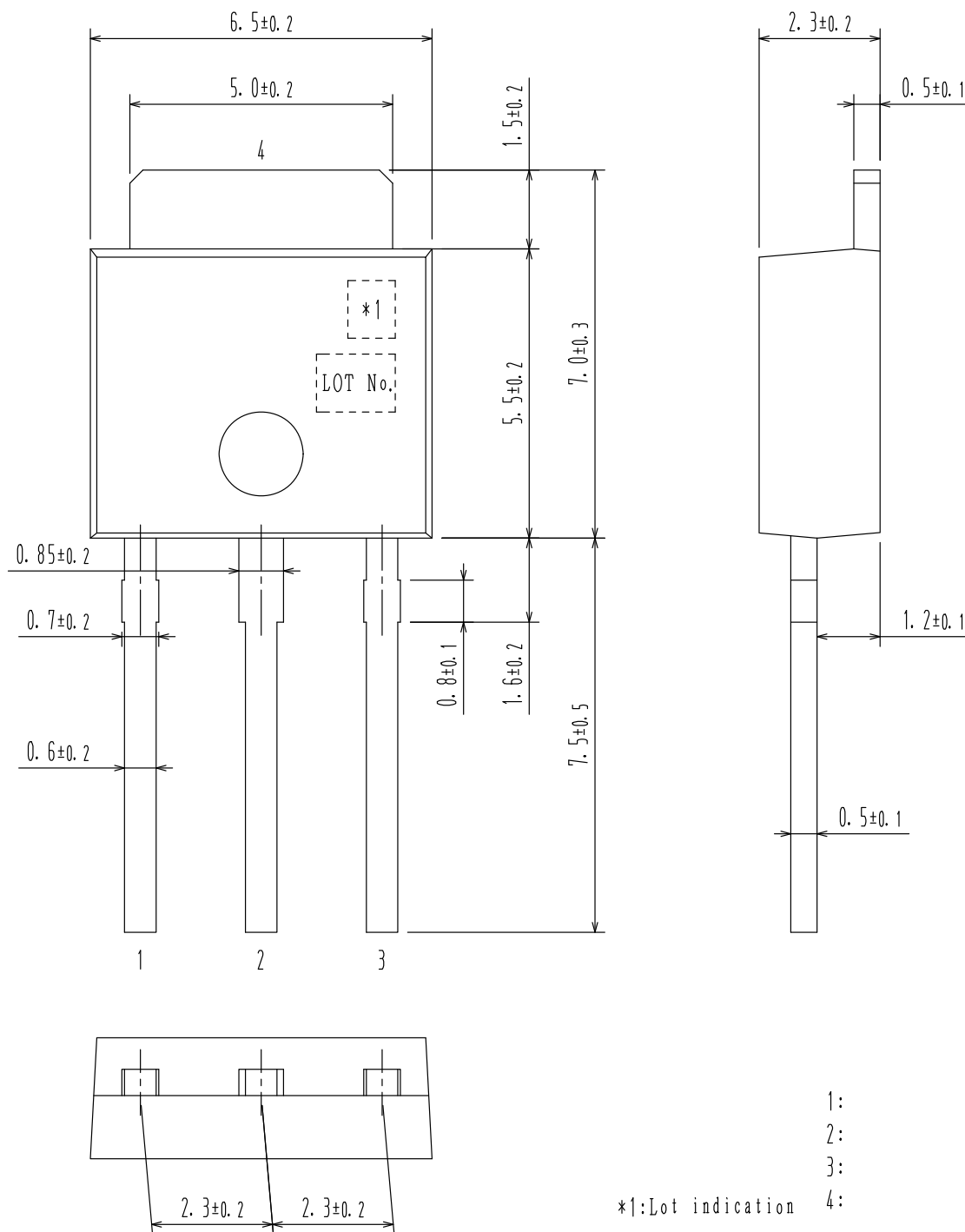
1:  
2:  
3:  
\*1: Lot indication 4:

DOCUMENT NUMBER:	98AON66236E	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	DPAK / TP-FA	PAGE 1 OF 1

onsemi and Onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**IPAK / TP**  
**CASE 369AJ**  
**ISSUE O**

DATE 30 JAN 2012



<b>DOCUMENT NUMBER:</b>	<b>98AON66237E</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>IPAK / TP</b>	<b>PAGE 1 OF 1</b>

onsemi and Onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at  
[www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)