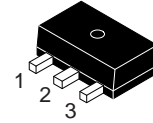


Bipolar Transistor

(-)50 V, (-)7 A, Low VCE(sat), (PNP) NPN
Single PCP

2SA2016, 2SC5569



SOT-89 / PCP-1
CASE 419AU

Features

- Adoption of FBET and MBIT Processes
- Low Collector-to-Emitter Saturation Voltage
- Ultrasmall Package Facilitates Miniaturization in End Products
- High Allowable Power Dissipation
- Large Current Capacity
- High-speed Switching

Applications

- Relay Drivers, Lamp Drivers, Motor Drivers, Flash

Specifications

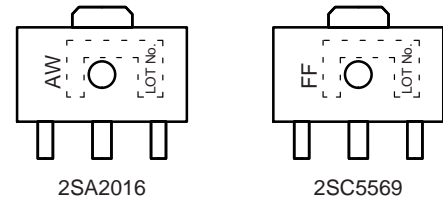
(): 2SA2016

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(-50) 100	V
Collector-to-Emitter Voltage	V _{CES}		(-50) 100	V
Collector-to-Emitter Voltage	V _{CEO}		(-)50	V
Emitter-to-Base Voltage	V _{EBO}		(-)6	V
Collector Current	I _C		(-)7	A
Collector Current (Pulse)	I _{CP}		(-)10	A
Base Current	I _B		(-)1.2	A
Collector Dissipation	P _C	When mounted on ceramic substrate (250 mm ² x 0.8 mm)	1.3	W
		T _c = 25°C	3.5	W
Junction Temperature	T _j		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

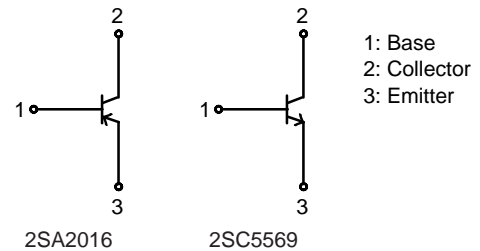
MARKING DIAGRAM



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ELECTRICAL CONNECTION



- 1: Base
2: Collector
3: Emitter

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ORDERING INFORMATION

Device	Package Type (JEITA, JEDEC)	Shipping†
2SA2016-TD-E	PCP (Pb-Free)	1000 / Tape & Reel
2SC5569-TD-E		

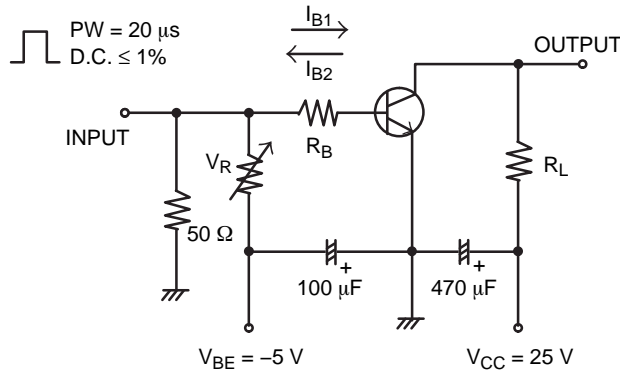
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

ELECTRICAL CHARACTERISTICS (T_A = 25°C)

Parameter	Symbol	Conditions	Ratings			Unit
			Min	Typ	Max	
Collector Cutoff Current	I _{CBO}	V _{CB} = (-)40 V, I _E = 0 A	-	-	(-)0.1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} = (-)4 V, I _C = 0 A	-	-	(-)0.1	μA
DC Current Gain	h _{FE}	V _{CE} = (-)2 V, I _C = (-)500 mA	200	-	560	
Gain-Bandwidth Product	f _T	V _{CE} = (-)10 V, I _C = (-)500 mA	-	(290) 330	-	MHz
Output Capacitance	C _{ob}	V _{CB} = (-)10 V, f = 1 MHz	-	(50) 28	-	pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)1}	I _C = (-)3.5 A, I _B = (-)175 mA	-	(-230) 160	(-390) 240	mV
	V _{CE(sat)2}	I _C = (-)2 A, I _B = (-)40 mA	-	(-240) 110	(-400) 170	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C = (-)2 A, I _B = (-)40 mA	-	(-)0.83	(-)1.2	V
Collector-to-Base Breakdown Voltage	V _{(BR)CBO}	I _C = (-)10 μA, I _E = 0 A	(-50) 100	-	-	V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CES}	I _C = (-)100 μA, R _{BE} = 0 Ω	(-50) 100	-	-	V
Collector-to-Emitter Breakdown Voltage	V _{(BR)CEO}	I _C = (-)1 mA, R _{BE} = ∞	(-)50	-	-	V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E = (-)10 μA, I _C = 0 A	(-)6	-	-	V
Turn-On Time	t _{on}	See specified Test Circuit.	-	(40) 30	-	ns
Storage Time	t _{stg}		-	(225) 420	-	ns
Fall Time	t _f		-	25	-	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit



I_C = 20I_{B1} = -20I_{B2} = 2.5 A
 For PNP, the polarity is reversed.

Figure 1. Switching Time Test Circuit

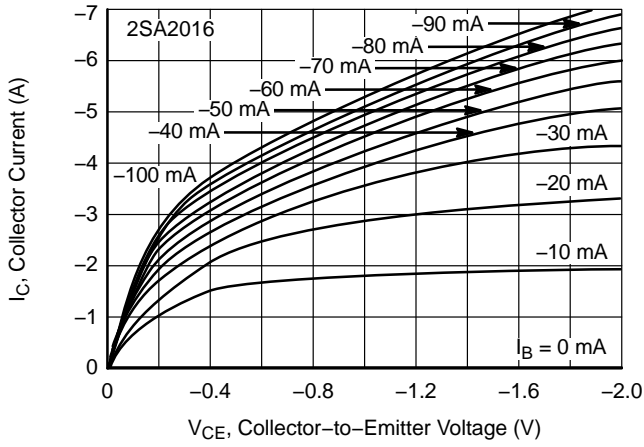


Figure 2. $I_C - V_{CE}$

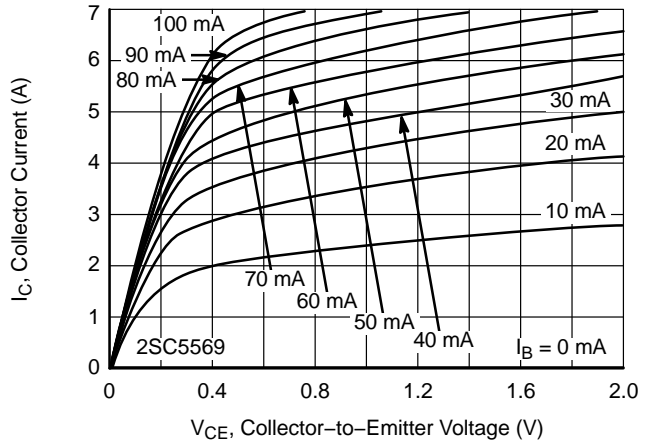


Figure 3. $I_C - V_{CE}$

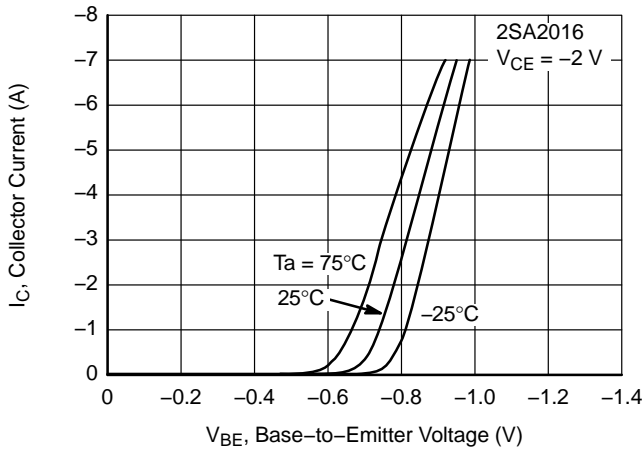


Figure 4. $I_C - V_{BE}$

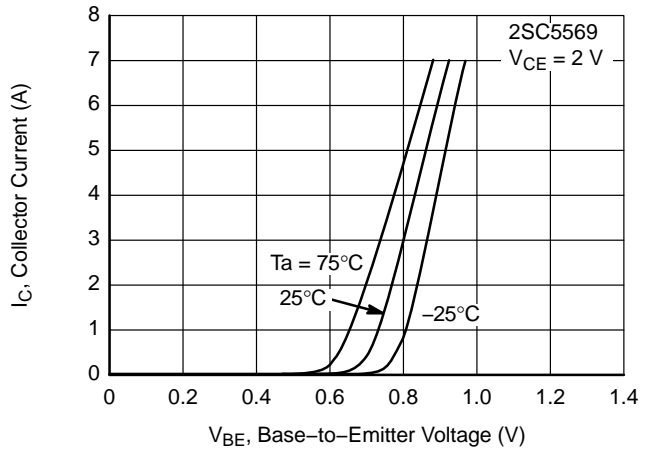


Figure 5. $I_C - V_{BE}$

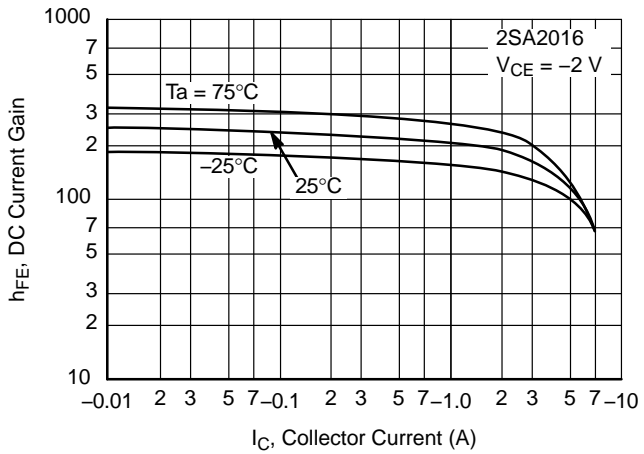


Figure 6. $h_{FE} - I_C$

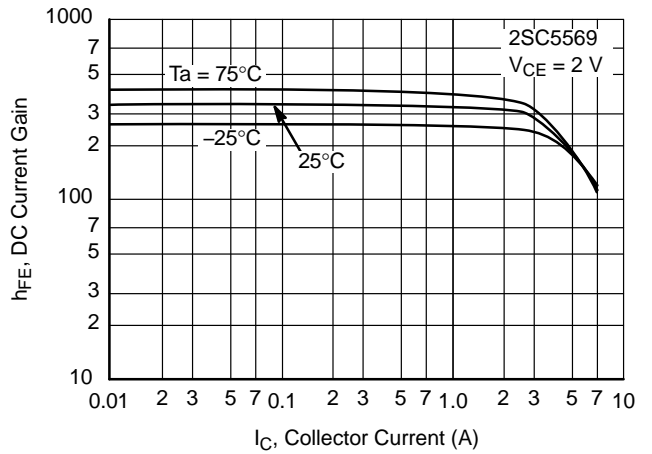


Figure 7. $h_{FE} - I_C$

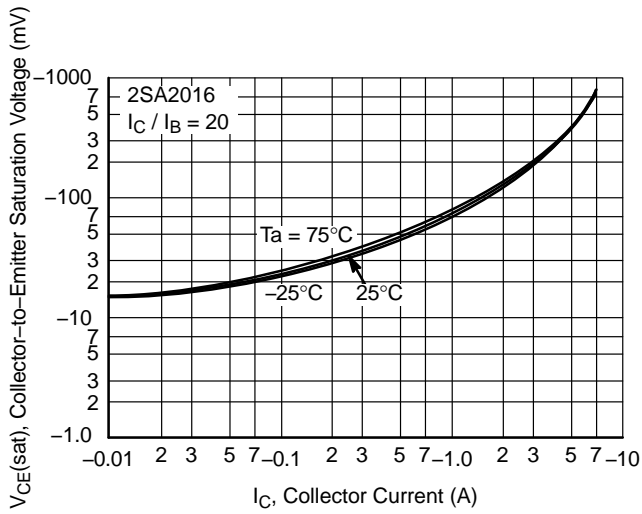


Figure 8. $V_{CE(sat)} - I_C$

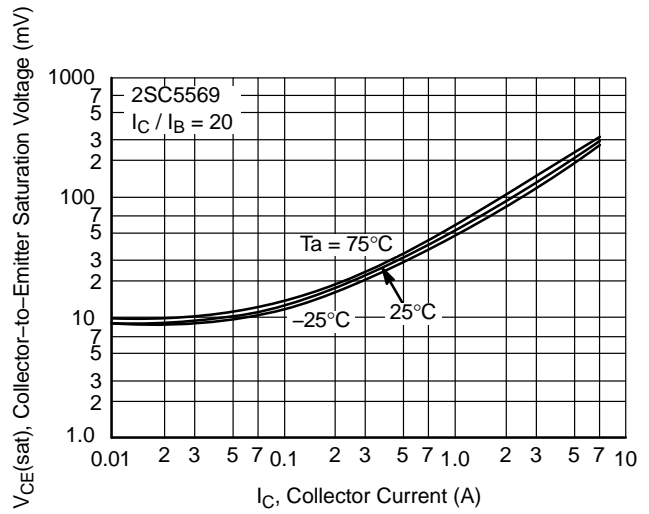


Figure 9. $V_{CE(sat)} - I_C$

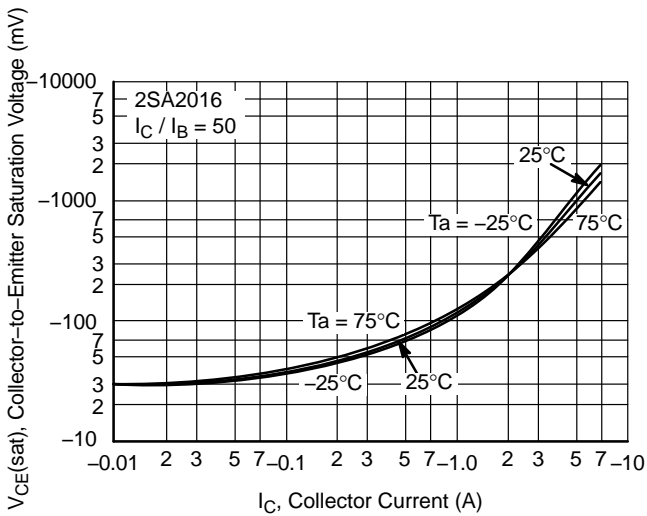


Figure 10. $V_{CE(sat)} - I_C$

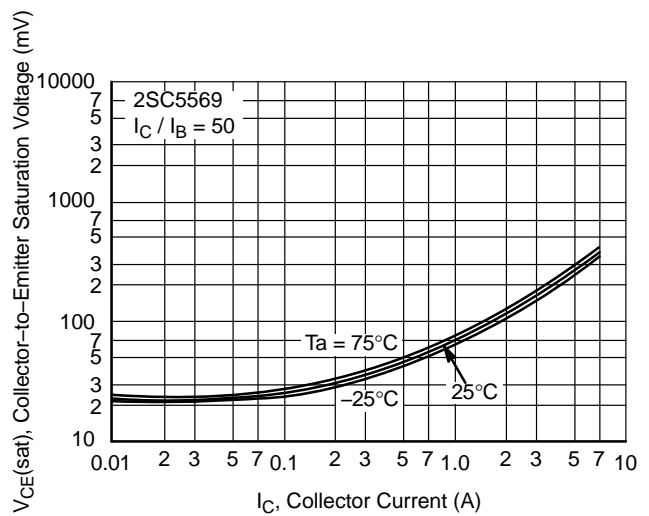


Figure 11. $V_{CE(sat)} - I_C$

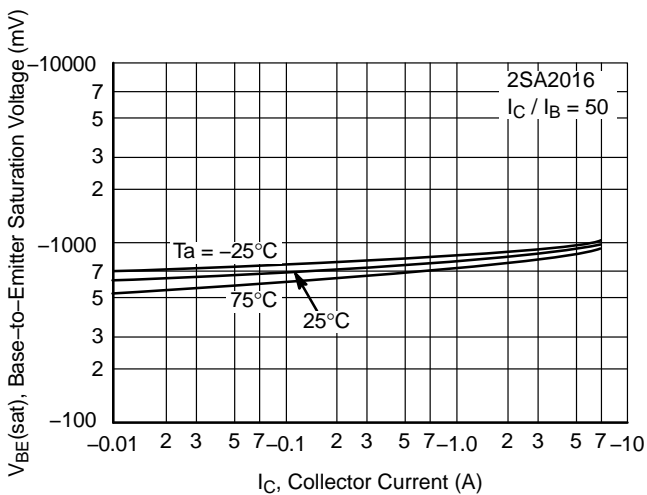


Figure 12. $V_{BE(sat)} - I_C$

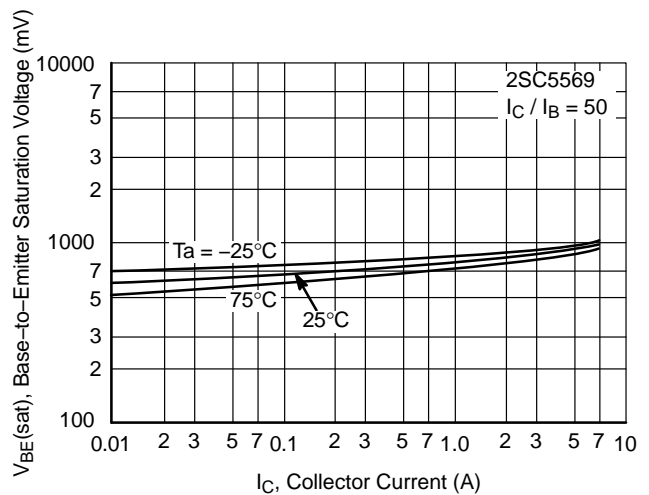


Figure 13. $V_{BE(sat)} - I_C$

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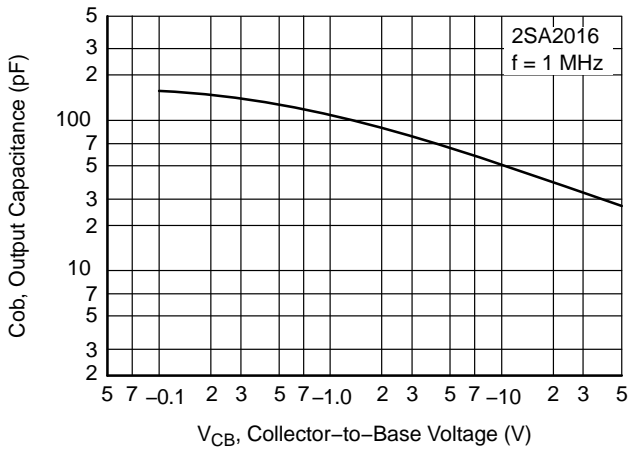


Figure 14. Cob - V_{CB}

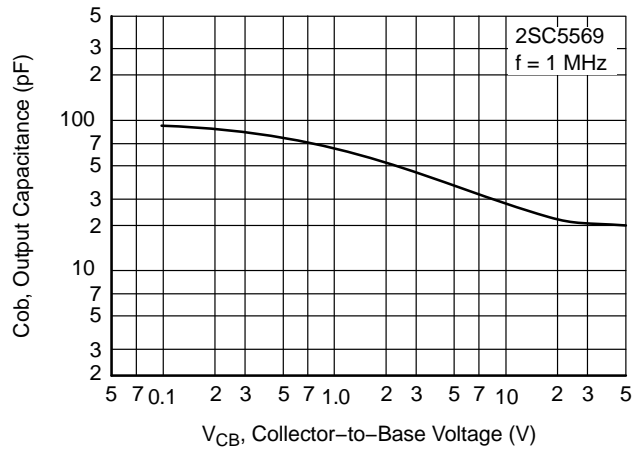


Figure 15. Cob - V_{CB}

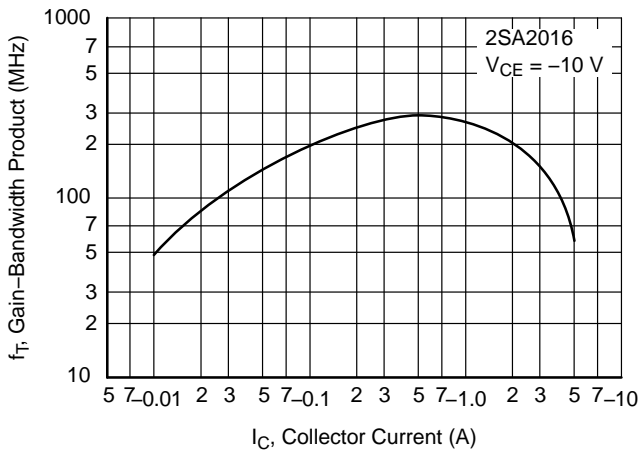


Figure 16. f_T - I_C

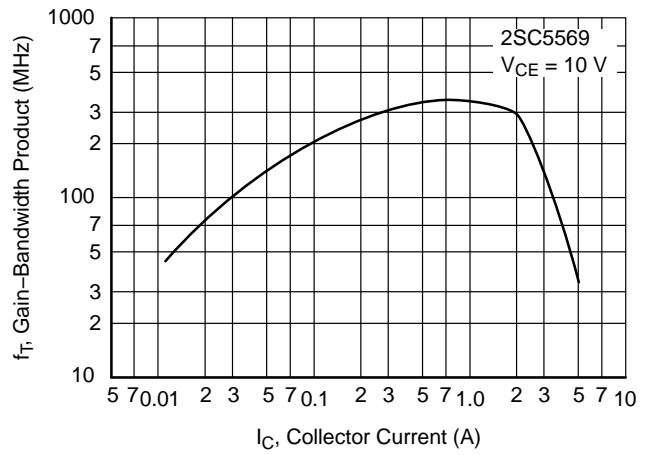


Figure 17. f_T - I_C

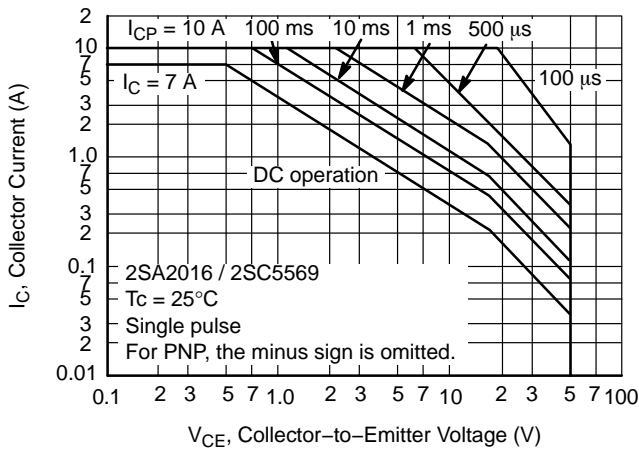


Figure 18. ASO

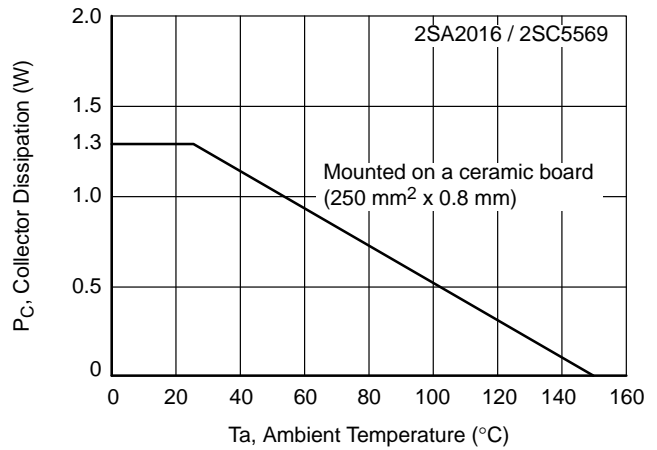


Figure 19. P_C - T_a

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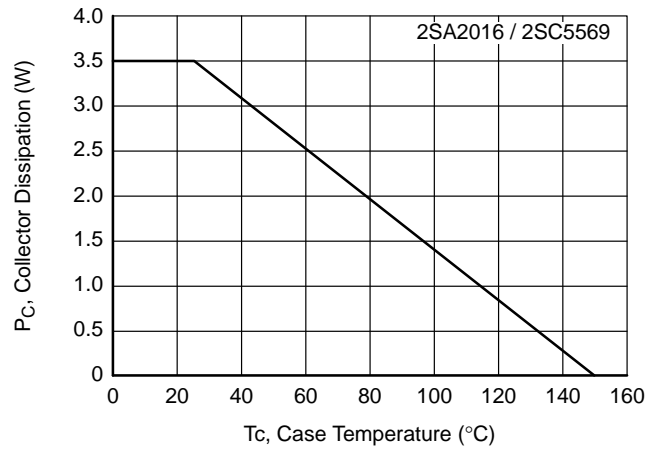
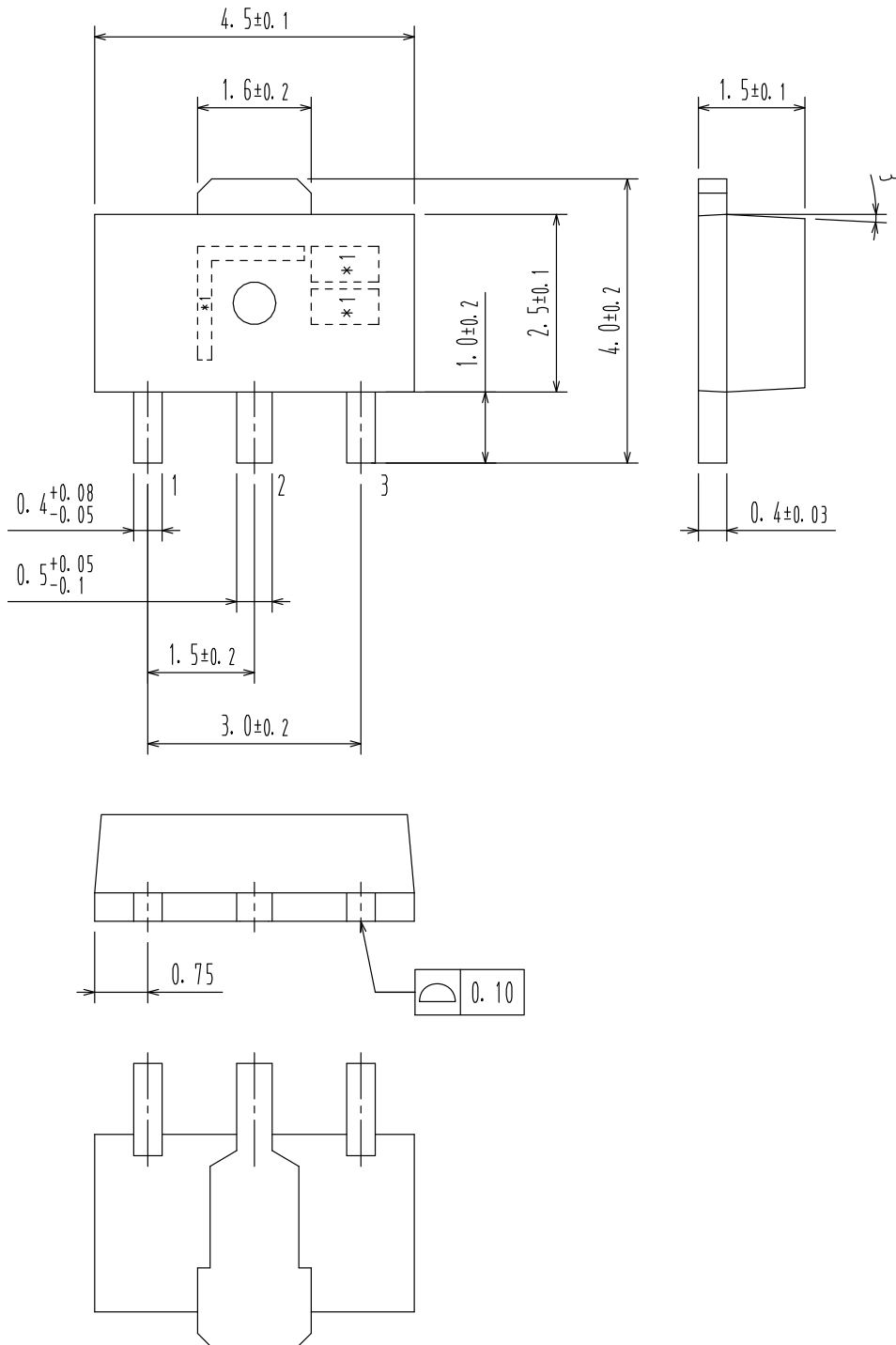


Figure 20. $P_C - T_c$


MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS

SOT-89 / PCP-1
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ISSUE 0

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