NPN Epitaxial Silicon Transistor

2N6517

Features

- High Voltage Transistor
- Collector Dissipation: PC(max) = 625 mW
- Complement to 2N6520
- Suffix "-C" means Center Collector (1. Emitter 2. Collector 3. Base)

ABSOLUTE MAXIMUM RATINGS

(Values are at $T_A = 25^{\circ}C$ unless otherwise noted.)

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-Base Voltage 2N6517 2N6517C	350 400	V
V _{CEO}	Collector–Emitter Voltage 2N6517 2N6517C	350 400	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current	500	mA
P _C	Collector Power Dissipation	625	mW
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	−55 ~ 150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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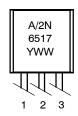




TO-92 3 4.825x4.76 CASE 135AN

TO-92 3 4.83x4.76 **LEADFORMED** CASE 135AR

MARKING DIAGRAM



- 1: Emitter
- 2: Base
- 3: Collector

2N6517/2N6517C YWW

= Assembly Code = Device Code

= Date Code

ORDERING INFORMATION

Device	Package	Shipping
2N6517BU	TO-92 3 (Pb-Free)	10000 / Bulk Bag
2N6517TA	TO-92 3 (Pb-Free)	2000 / Fan-Fold
2N6517CTA	TO-92 3 (Pb-Free)	2000 / Fan–Fold

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2N6517

ELECTRICAL CHARACTERISTICS

(Values are at $T_A = 25^{\circ}C$ unless otherwise noted.)

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV _{CBO}	Collector-Base Breakdown Voltage 2N6517 2N6517C	$I_C = 100 \mu A, I_E = 0$ $I_C = 100 \mu A, I_E = 0$	350 400	- -	V
BV _{CEO}	Collector-Emitter Breakdown Voltage* 2N6517 2N6517C	I _C = 1 mA, I _B = 0 I _C = 1 mA, I _B = 0	350 400	- -	V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = 10 μA, I _C = 0	6	-	V
I _{CBO}	Collector Cut-Off Current	V _{CB} = 250 V, I _E = 0	_	50	nA
I _{EBO}	Emitter Cut-Off Current	V _{EB} = 5 V, I _C = 0	-	50	nA
h _{FE}	DC Current Gain* 2N6517/2N6517C 2N6517/2N6517C 2N6517/2N6517C 2N6517/2N6517C 2N6517/2N6517C 2N6517/2N6517C 2N6517C	$V_{CE} = 10 \text{ V, } I_{C} = 1 \text{ mA}$ $V_{CE} = 10 \text{ V, } I_{C} = 10 \text{ mA}$ $V_{CE} = 10 \text{ V, } I_{C} = 30 \text{ mA}$ $V_{CE} = 10 \text{ V, } I_{C} = 50 \text{ mA}$ $V_{CE} = 10 \text{ V, } I_{C} = 100 \text{ mA}$ $V_{CE} = 10 \text{ V, } I_{C} = 5 \text{ mA}$	20 30 30 20 15 50	- 200 200 - 200	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 10 mA, I _B = 1 mA I _C = 20 mA, I _B = 2 mA I _C = 30 mA, I _B = 3 mA I _C = 50 mA, I _B = 5 mA	- - - -	0.3 0.35 0.5 1	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 10 mA, I _B = 1 mA I _C = 20 mA, I _B = 2 mA I _C = 30 mA, I _B = 3 mA	- - -	0.75 0.85 0.9	V
C _{ob}	Output Capacitance	V _{CB} = 20 V, I _E = 0, f = 1 MHz	-	6	pF
f _T	Current Gain Bandwidth Product*	$I_C = 10 \text{ mA}, V_{CE} = 20 \text{ V}, f = 20 \text{ MHz}$	40	200	MHz
V _{BE} (on)	Base-Emitter On Voltage	$I_C = 100 \text{ mA}, V_{CE} = 10 \text{ V},$	_	2	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. *Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%

2N6517

TYPICAL PERFORMANCE CHARACTERISTICS

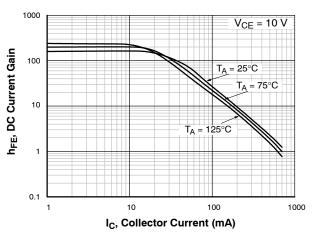


Figure 1. DC Current Gain

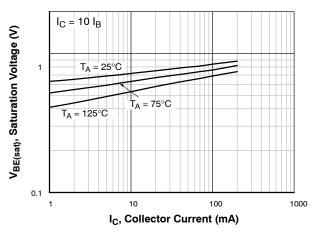


Figure 3. Saturation Voltage

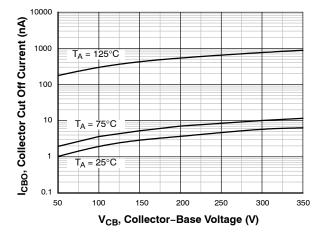


Figure 5. Collector Cut Off Current

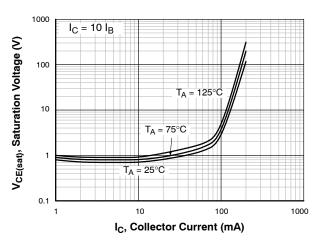


Figure 2. Saturation Voltage

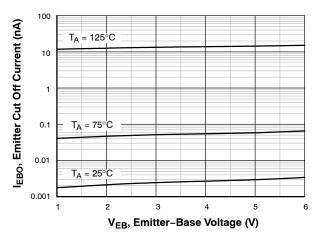


Figure 4. Emitter Cut Off Current

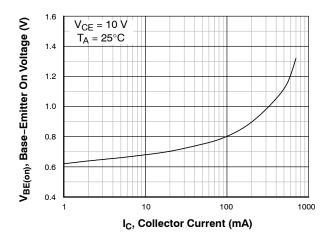


Figure 6. Base-Emitter On Voltage

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

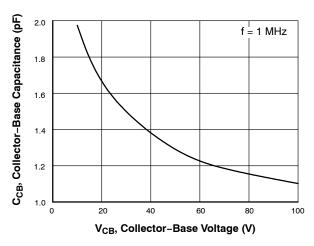


Figure 7. Output Capacitance

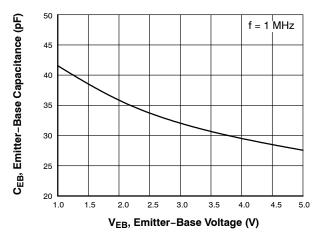


Figure 8. Input Capacitance

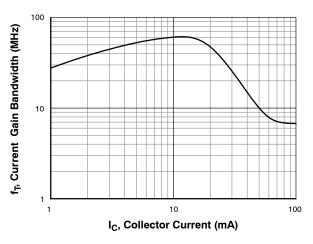


Figure 9. Current Gain Bandwidth Product

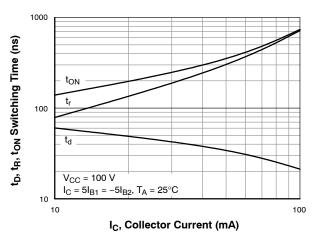


Figure 10. Resistive Load Switching

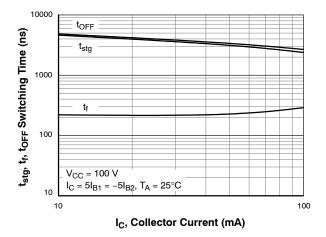
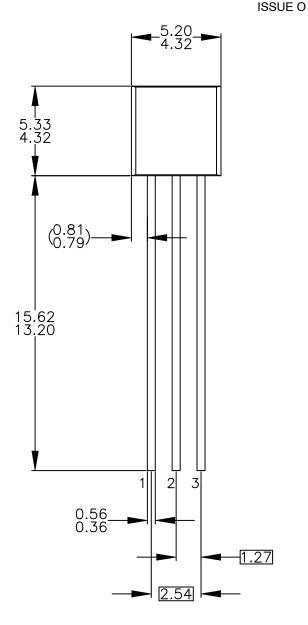
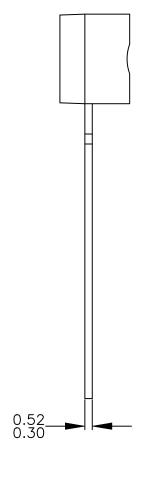


Figure 11. Resistive Load Switching

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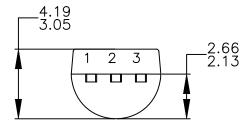
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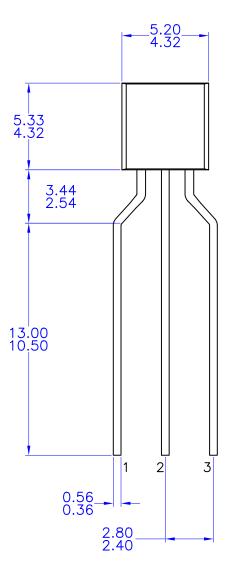
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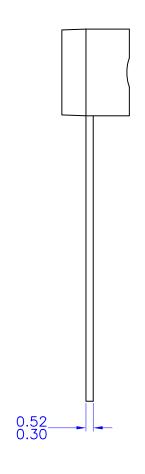
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TO-92 3 4.83x4.76 LEADFORMED

CASE 135AR ISSUE O

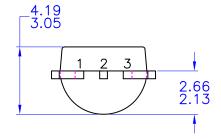
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