

Product Overview

NV34C02: EEPROM Serial 2-Kb SPD for DDR2 DIMM

For complete documentation, see the data sheet.

The NV34C02 is a EEPROM Serial 2-Kb SPD for DDR2 DIMM, internally organized as 16 pages of 16 bytes each, for a total of 256 bytes of 8 bits each.

It features a 16-byte page write buffer and supports both the Standard (100 kHz) as well as Fast (400 kHz) I²C protocol.

Write operations can be inhibited by taking the WP pin High (this protects the entire memory) or by setting an internal Write Protect flag via Software command (this protects the lower half of the memory).

In addition to Permanent Software Write Protection, the NV34C02 also features JEDEC compatible Reversible Software Write Protection for DDR2 Serial Presence Detect (SPD) applications operating over the 1.7 V to 3.6 V supply voltage range.

The CAT34C02 is fully backwards compatible with earlier DDR1 SPD applications operating over the 1.7 V to 5.5 V supply voltage range.

Features

- Supports Standard and Fast I²C Protocol
- 1.7 V to 5.5 V Supply Voltage Range
- 16-Byte Page Write Buffer
- Hardware Write Protection for Entire Memory
- Software Write Protection for Lower 128 Bytes
- Schmitt Triggers and Noise Suppression Filters on I²C Bus Inputs (SCL and SDA)
- Low Power CMOS Technology
- 1,000,000 Program/Erase Cycles
- 100 Year Data Retention
- Industrial Temperature Range

For more features, see the data sheet

Applications

- Computing and Peripherals

End Products

- Disk drives
- Modems

Part Electrical Specifications

Product	Compliance	Status	Type	Density	Organization	Data Transmission Standard	f _{cycle} Max (kHz)	t _{ACC} Max ns	V _{CC} Min (V)	V _{CC} Max (V)	I _{standby} Max (μA)	I _{act} Max (mA)	T Min (°C)	T Max (°C)	Package Type
NV34C02MUW3VTBG	AEC Qualified PPAP Capable Pb-free Halide free	Active	CMOS	2 kb	256 x 8	I2C	400	3500	1.7	5.5	5	2	-40	125	UDFN-8

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