

Product Overview

NL17SH126: Noninverting 3-State Buffer

For complete documentation, see the data sheet.

The NL17SH126 is an advanced high speed CMOS noninverting 3-state buffer fabricated with silicon gate CMOS technology. It achieves high speed operation similar to equivalent Bipolar Schottky TTL while maintaining CMOS low power dissipation. The internal circuit is composed of three stages, including a buffered 3-state output which provides high noise immunity and stable output. The NL17SH126 input structure provides protection when voltages up to 7 V are applied, regardless of the supply voltage. This allows the NL17SH126 to be used to interface 5 V circuits to 3 V circuits.

Features

- High Speed: $t_{PD} = 3.5$ ns (Typ) at $V_{CC} = 5$ V
- Low Power Dissipation: $I_{CC} = 1$ μ A (Max) at $T_A = 25^\circ$ C
- Power Down Protection Provided on Inputs
- Balanced Propagation Delays
- Pin and Function Compatible with Other Standard Logic Families
- These are PbFree Devices

Part Electrical Specifications

| Product | Compliance | Status | Channels | Output | V_{CC} Min (V) | V_{CC} Max (V) | t_{pd} Max (ns) | I_o Max (mA) | Package Type |
|----------------|------------------------|--------|----------|---------|------------------|------------------|-------------------|----------------|--------------|
| NL17SH126P5T5G | Pb-free Halide free | Active | 1 | 3-State | 5.5 | 2 | 3.5 | 8 | SOT-953 |

For more information please contact your local sales support at www.onsemi.com.

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