

## Product Overview

### NL17SH125: Non-Inverting 3-State Buffer

For complete documentation, see the data sheet.

The NL17SH125 MiniGate is an advanced high speed CMOS non inverting buffer in ultra small footprint. The NL17SH125 requires the 3 state control input (OE) to be set High to place the output in the high impedance state. The NL17SH125 input structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

### Features

- High Speed:  $t_{PD} = 3.8 \text{ ns}$  (Typ) at  $V_{CC} = 5.0 \text{ V}$
- Low Power Dissipation:  $I_{CC} = 1.0 \text{ uA}$  (Max) at  $T_A = 25\text{C}$
- Power Down Protection Provided on Inputs
- Balanced Propagation Delays

### Part Electrical Specifications

Product	Compliance	Status	Channels	Output	$V_{CC}$ Min (V)	$V_{CC}$ Max (V)	$t_{pd}$ Max (ns)	$I_O$ Max (mA)	Package Type
NL17SH125P5T5G	Pb-free Halide free	Active	1	3-State	1.65	5.5	11.5	8	SOT-953

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