



Product Overview

**NBVSPA018: Crystal Oscillator Module, Voltage Controlled, PureEdge™, 155.52 MHz LVDS, 2.5 V / 3.3 V**

For complete documentation, see the data sheet

**Product Description**

The NBVSPXXXX voltage controlled crystal oscillator (VCXO) devices are designed to meet today's requirements for 3.3 V LVDS clock generation applications. These devices use a high Q fundamental mode crystal and Phase Locked Loop (PLL) multiplier to provide a wide range of frequencies from 60 MHz to 700 MHz (factory configurable per user specifications) with a pullable range of  $\pm 100$  ppm and a frequency stability of  $\pm 50$  ppm. The silicon based PureEdge™ products design provides users with exceptional frequency stability and reliability. They produce an ultra low jitter and phase noise LVDS differential output. The NBVSPXXXX series devices are a member of ON Semiconductors PureEdge™ clock family that provides accurate and precision clock generation solutions.

| Features | Benefits |
|----------|----------|
|----------|----------|

- |   |   |
|---|---|
| <ul style="list-style-type: none"><li>• Ultra Low Jitter and Phase Noise 0.5 ps (12 kHz to 20 MHz)</li><li>• LVDS Differential Output</li><li>• Pullable Range Minimum of <math>\pm 100</math> ppm</li><li>• Control Voltage with Positive Slope</li><li>• Voltage Control Linearity of <math>\pm 10\%</math></li><li>• Frequency Stability of <math>\pm 50</math> ppm</li><li>• Operating Range: 3.3 V <math>\pm 10\%</math></li></ul> | <ul style="list-style-type: none"><li>• Improves system timing budget</li></ul> |
|---|---|

| Applications | End Products |
|--------------|--------------|
|--------------|--------------|

- |  |  |
|--|--|
| <ul style="list-style-type: none"><li>• Networking</li><li>• Wireless telecommunications</li></ul> | <ul style="list-style-type: none"><li>• 1/10G Ethernet</li><li>• Line Card Clock</li></ul> |
|--|--|

For more information please contact your local sales support at [www.onsemi.com](http://www.onsemi.com)

Created on: 7/11/2015