

## Product Overview

### NB7L585R: Input Mux - 2:1 Differential, 2.5 V / 3.3 V, Clock / Data Fanout Buffer - 1:6 RSECL, 7 GHz / 10 Gbps

For complete documentation, see the data sheet.



The NB7L585R is a differential 1:6 RSECL Clock/Data distribution chip featuring a 2:1 Clock/Data input multiplexer with an input select pin. The INx/INx inputs incorporate internal 50-ohm termination resistors and will accept LVPECL, CML, or LVDS logic levels. The NB7L585R produces six identical output copies of Clock or Data operating up to 7 GHz or 10 Gb/s, respectively. As such, NB7L585R is ideal for SONET, GigE, Fiber Channel, Backplane and other Clock/Data distribution applications. The NB7L585R is powered with either 2.5 V or 3.3 V supply. The NB7L585R is a member of the GigaComm™ family of high performance clock products.

## Features

- Maximum Input Data Rate > 10 Gb/s Typical
- Data Dependent Jitter < 10 ps/
- Maximum Input Clock Frequency > 7 GHz Typical
- Random Clock Jitter < 0.8 ps RMS
- Low Skew 1:6 RSECL Outputs, 20 ps max
- 2:1 Multi-Level Mux Inputs
- Differential RSECL Outputs, 400 mV peak-to-peak, typical
- Operating Range:  $V_{CC} = 2.375\text{ V to }3.6\text{ V}$  with  $GND = 0\text{ V}$
- $-40^{\circ}\text{C to }+85^{\circ}\text{C}$  Ambient Operating Temperature

## Applications

- Multiplex and Fanout Clock or Data in Routers, Servers

## End Products

- Routers, Servers

For more information please contact your local sales support at [www.onsemi.com](http://www.onsemi.com).

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