

Product Overview

NB7L585: Input Mux - 2:1 Differential, 2.5 V / 3.3 V, Clock / Data Fanout Buffer - 1:6 LVPECL

For complete documentation, see the data sheet.



The NB7L585 is a differential 1:6 LVPECL Clock/Data distribution chip featuring a 2:1 Clock/Data input multiplexer with an input select pin. The INx/INxb inputs incorporate internal 50-ohm termination resistors and will accept LVPECL, CML, or LVDS logic levels. The NB7L585 produces six identical output copies of Clock or Data operating up to 5 GHz or 8 Gbps, respectively. As such, NB7L585 is ideal for SONET, GigE, Fiber Channel, Backplane and other Clock/Data distribution applications. The NB7L585 is powered with either 2.5 V or 3.3 V supply. The NB7L585 is a member of the GigaComm™ family of high performance clock products.

Features

- Maximum Input Data Rate > 8 Gb/s
 - Data Dependent Jitter < 15 ps
 - Maximum Input Clock Frequency > 5 GHz
 - Random Clock Jitter < 0.8 ps RMS
 - Low Skew 1:6 LVPECL Outputs, 20 ps max
 - 2:1 MultiLevel Mux Inputs
 - 175 ps Typical Propagation Delay
 - 55 ps Typical Rise and Fall Times
 - Differential LVPECL Outputs, 800 mV peaktopeak, typical
 - Internal 50-ohm Input Termination Resistors
- For more features, see the data sheet

Applications

- Multiplex and Fanout

End Products

- Routers
- ATE, Instrumentation

Part Electrical Specifications

Product	Compliance	Status	Input/Output Ratio	Channels	Input Level	Output Level	V _{CC} Typ (V)	f _{Max} Typ (MHz)	t _{Jitter} Typ (ps)	t _{skew(OO) Max} (ps)	t _{pd} Typ (ns)	Package Type
NB7L585MNG	Pb-free	Active	2:6	1	ECL	ECL	3.3	7000	0.2	20	0.175	QFN-32
	Halide free				LVDS		2.5					
					CML							
NB7L585MNR4G	Pb-free	Active	2:6	1	ECL	ECL	2.5	7000	0.2	20	0.175	QFN-32
	Halide free				LVDS		3.3					
					CML							
NB7L585MNTWG	Pb-free	Active	2:6	1	LVDS	ECL	2.5	7000	0.2	20	0.175	QFN-32
	Halide free				CML		3.3					
					ECL							

For more information please contact your local sales support at www.onsemi.com.

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