

Product Overview

NB100ELT23L: Translator, Dual Differential LVPECL to LVTTTL

For complete documentation, see the data sheet.

The NB100ELT23L is a dual differential LVPECL/LVDS to LVTTTL translator. Because LVPECL (Positive ECL) levels are used, only +3.3 V and ground are required. The small outline 8-lead package and the dual gate design of the ELT23L makes it ideal for applications which require the translation of a clock and a data signal.

The ELT23L is available in only the ECL 100K standard. Since there are no LVPECL outputs or an external V_{BB} reference, the ELT23L does not require both ECL standard versions. The LVPECL inputs are differential. Therefore, the NB100ELT23L can accept any standard differential LVPECL input referenced from a V_{CC} of +3.3 V.

Features

- 2.1 ns Typical Propagation Delay
- Maximum Operating Frequency > 275 MHz
- 24 mA LVTTTL Outputs
- Operating Range: $V_{CC} = 3.0\text{ V to }3.6\text{ V}$ with $GND = 0\text{ V}$
- Open Input Default State
- Q Output Will Default LOW with Inputs Open or at GND

Benefits

- Precision Edge Placement

Applications

- Clock Signal Level Translations

End Products

- Logic Systems

Part Electrical Specifications

Product	Compliance	Status	Channels	Input Level	Output Level	V_{CC} Typ (V)	f_{Max} Typ (MHz)	t_{pd} Typ (ns)	t_r & t_f Max (ps)	Package Type
NB100ELT23LDR2G	Pb-free	Active	2	ECL	TTL	3.3	160	1.9	1700	SOIC-8
	Halide free								1250	
NB100ELT23LDTG	Pb-free	Active	2	ECL	TTL	3.3	160	1.9	1700	TSSOP-8
	Halide free								1250	
NB100ELT23LDTR2G	Pb-free	Active	2	ECL	TTL	3.3	160	1.9	1700	TSSOP-8
	Halide free								1250	

For more information please contact your local sales support at www.onsemi.com.

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