

## Product Overview

### N24S64: 64 Kb I2C CMOS Serial EEPROM with Software Write Protect and Programmable Device Address

For complete documentation, see the data sheet.

The N24S64 is a 64 Kb Serial CMOS EEPROM, internally organized as 8,192 words of 8 bits each. They feature a 32-byte page write buffer and support both the Standard (100 kHz), Fast (400 kHz) and Fast-Plus (1 MHz) I2C protocol.

The devices also feature a 128-bit factory-set read-only Unique ID, a 32-byte Secure Data Page that can be permanently locked against future changes, and Software Write Protection of the entire array.

A Device Configuration Register enables the user to specify the last 3 bits of the Device Address, allowing up to eight N24S64 devices to be addressed on the same bus.

### Features

- Supports Standard, Fast and Fast-Plus I2C Protocol
- 1.7 V to 5.5 V Supply Voltage Range
- 32-byte Page Write Buffer
- Lockable Secure Data Page
- User Programmable Write Protection
- User Programmable Device Address
- Schmitt Triggers and Noise Suppression Filters on I2C Bus Inputs (SCL and SDA)
- Low Power CMOS Technology
- 1,000,000 Program/Erase Cycles
- 40 Year Data Retention

For more features, see the data sheet

### Part Electrical Specifications

Product	Compliance	Status	Type	Density	Organization	Data Transmission Standard	f <sub>cycle</sub> Max (kHz)	t <sub>acc</sub> Max ns	V <sub>cc</sub> Min (V)	V <sub>cc</sub> Max (V)	I <sub>standby</sub> Max (μA)	I <sub>act</sub> Max (mA)	T Min (°C)	T Max (°C)	Package Type
N24S64C4DYT3G	Pb-free Halide free	NEW	CMOS	64 kb	64k x 8	Serial 8-bit	1000	3500	1.7	5.5	2	2	-40	85	WLCS P-4

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