

Product Overview

MC74HC4094A: 8-Bit Shift and Store Register

For complete documentation, see the data sheet.

The MC74HC4094A is a high speed CMOS 8-bit serial shift and Storage register. This device consists of an 8-bit shift register and latch with 3-stage output buffers. Data is shifted on positive clock (CP) transitions. The data in the shift register is transferred to the storage register when the Strobe (STR) input is high. The output buffers are enabled when the Output Enable (OE) input is set high. Two serial outputs (QS1, QS2) are available for cascading multiple devices.

Features

- Wide Operating Voltage Range: 2.0 to 6.0 V
- Low Power Dissipation: $I_{CC} = < 10 \text{ A}$
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- These are PbFree Devices

Applications

- SerialtoParallel Conversion
- Remote Control Storage Register

Part Electrical Specifications									
Product	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HC4094ADG	Pb-free	Active	Shift Register	1	2	6	26	5.2	SOIC-16
	Halide free								
MC74HC4094ADR2G	Pb-free	Active	Shift Register	1	2	6	26	5.2	SOIC-16
	Halide free								
MC74HC4094ADTG	Pb-free	Active	Shift Register	1	2	6	26	5.2	TSSOP-16
	Halide free								
MC74HC4094ADTR2G	Pb-free	Active	Shift Register	1	2	6	26	5.2	TSSOP-16
	Halide free								
NLV74HC4094BDR2G	AEC Qualified	Active	Shift Register	1	2	6	26	5.2	SOIC-16
	PPAP Capable								
	Pb-free								
	Halide free								
NLVHC4094BDTR2G	AEC Qualified	Active	Shift Register	1	2	6	26	5.2	TSSOP-16
	Pb-free								
	Halide free								

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