

Product Overview

MC74HC245A: Octal 3-State Non-Inverting Bus/Transceiver

For complete documentation, see the data sheet.

High-Performance Silicon-Gate CMOS

The MC74HC245A is identical in pinout to the LS245. The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs.

The HC245A is a 3-state noninverting transceiver that is used for 2-way asynchronous communication between data buses. The device has an active-low Output Enable pin, which is used to place the I/O ports into high-impedance states. The Direction control determines whether data flows from A to B or from B to A.

Features

- Output Drive Capability: 15 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 2 to 6 V
- Low Input Current: 1 μA
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 308 FETs or 77 Equivalent Gates
- Pb-Free Packages are Available

Part Electrical Specifications

Product	Compliance	Status	Channels	Output	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HC245ADTG	Pb-free	Active	8	3-State	2	6	15	6	TSSOP-20
	Halide free								
MC74HC245ADTR2G	Pb-free	Active	8	3-State	2	6	15	6	TSSOP-20
	Halide free								
MC74HC245ADWG	Pb-free	Active	8	3-State	2	6	15	6	SOIC-20W
	Halide free								
MC74HC245ADWR2G	Pb-free	Active	8	3-State	2	6	15	6	SOIC-20W
	Halide free								
NLV74HC245ADTG	AEC Qualified	Active	8	3-State	2	6	15	6	TSSOP-20
	PPAP Capable								
	Pb-free								
	Halide free								
NLV74HC245ADTR2G	AEC Qualified	Active	8	3-State	2	6	15	6	TSSOP-20
	PPAP Capable								
	Pb-free								
	Halide free								

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