

Product Overview

MC74ACT74: Dual D-Type Positive Edge-Triggered Flip-Flop

For complete documentation, see the data sheet.

The MC74ACT74/74ACT74 is a dual D-type flip-flop with Asynchronous Clear and Set inputs and complementary (Q,Q) outputs. Information at the input is transferred to the outputs on the positive edge of the clock pulse. Clock triggering occurs at a voltage level of the clock pulse and is not directly related to the transition time of the positive-going pulse. After the Clock Pulse input threshold voltage has been passed, the Data input is locked out and information present will not be transferred to the outputs until the next rising edge of the Clock Pulse input. Asynchronous Inputs:

LOW input to S_D (Set) sets Q to HIGH level LOW input to C_D (Clear) sets Q to LOW level Clear and Set are independent of clock Simultaneous LOW on C_D and S_D makes both Q and \bar{Q} HIGH

Features

- Outputs Source/Sink 24 mA
- ACT74 Has TTL Compatible Inputs $C_{D1}S_{D2} D_1Q_1CP_2Q_2$
- Pb-Free Packages Are Available

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
MC74ACT74DG	Pb-free	Active	D-Type	2	4.5	5.5	10	24	SOIC-14
	Halide free								
MC74ACT74DR2G	Pb-free	Active	D-Type	2	4.5	5.5	10	24	SOIC-14
	Halide free								
MC74ACT74DTR2G	Pb-free	Active	D-Type	2	4.5	5.5	10	24	TSSOP-14
	Halide free								

For more information please contact your local sales support at www.onsemi.com.

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