

Product Overview

MC74ACT373: Octal Transparent Latch with 3-State Outputs

For complete documentation, see the data sheet.

The MC74AC373/74ACT373 consists of eight latches with 3-state outputs for bus organized system applications. The flip-flops appear transparent to the data when Latch Enable (LE) is HIGH. When LE is LOW, the data that meets the setup time is latched. Data appears on the bus when the Output Enable (OE) is LOW. When OE is HIGH, the bus output is in the high impedance state.

Features

- Eight Latches in a Single Package
- 3-State Outputs for Bus Interfacing
- Outputs Source/Sink 24 mA
- ACT373 Has TTL Compatible Inputs
- Pb-Free Packages are Available

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74ACT373DTR2G	Pb-free	Active	Latch	8	4.5	5.5	10	24	TSSOP-20
	Halide free								
MC74ACT373DWG	Pb-free	Active	Latch	8	4.5	5.5	10	24	SOIC-20W
	Halide free								
MC74ACT373DWR2G	Pb-free	Active	Latch	8	4.5	5.5	10	24	SOIC-20W
	Halide free								

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