

## Product Overview

### MC100EPT22: Translator, Dual LVTTTL / LVCMOS to Differential LVPECL

For complete documentation, see the data sheet.

The MC100EPT22 is a dual LVTTTL/LVCMOS to differential LVPECL translator. Because LVPECL (Positive ECL) levels are used only +3.3V and ground are required. The small outline 8-lead SOIC package and the single gate of the EPT22 makes it ideal for those applications where space, performance, and low power are at a premium. Because the mature MOSAIC 5 process is used, low cost and high speed can be added to the list of features.

### Features

- 420 ps Typical Propagation Delay
- Maximum Frequency > 1.1 GHz
- Operating Range:  $V_{CC} = 3.0\text{ V to }3.6\text{ V}$  with  $GND = 0\text{ V}$
- PNP LVTTTL Inputs for Minimal Loading
- Q Output will default HIGH with inputs open
- The 100 Series contains temperature compensation.
- Pb-Free Packages are Available

Part Electrical Specifications										
Product	Compliance	Status	Channels	Input Level	Output Level	$V_{CC}$ Typ (V)	$f_{Max}$ Typ (MHz)	$t_{pd}$ Typ (ns)	$t_r$ & $t_f$ Max (ps)	Package Type
MC100EPT22DG	Pb-free	Active	2	CMOS	ECL	3.3	1100	0.42	200	SOIC-8
	Halide free			TTL						
MC100EPT22DR2G	Pb-free	Active	2	CMOS	ECL	3.3	1100	0.42	200	SOIC-8
	Halide free			TTL						
MC100EPT22DTG	Pb-free	Active	2	TTL	ECL	3.3	1100	0.42	200	TSSOP-8
	Halide free			CMOS						
MC100EPT22DTR2G	Pb-free	Active	2	TTL	ECL	3.3	1100	0.42	200	TSSOP-8
	Halide free			CMOS						
MC100EPT22MNR4G	Pb-free	Active	2	TTL	ECL	3.3	1100	0.42	200	DFN-8
	Halide free			CMOS						

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