

Product Overview

MC100ELT20: Translator, TTL to Differential PECL

For complete documentation, see the data sheet.

The MC10ELT/100ELT20 is a TTL to differential PECL translator. Because PECL (Positive ECL) levels are used only +5V and ground are required. The small outline 8-lead SOIC package and the single gate of the ELT20 makes it ideal for those applications where space, performance and low power are at a premium.

The 100 Series contains temperature compensation.

Features

- 1.5ns Typical Propagation Delay
- PNP TTL Inputs for Minimal Loading
- Flow Through Pinouts
- ESD Protection: >4 KV HBM, >200 V MM
- Operating Range: V_{CC} = 4.75 V to 5.25 V with GND = 0 V
- No Internal Input Pulldown Resistors
- Meets or Exceeds JEDEC Spec EIA/JESD78 IC Latchup Test
- Moisture Sensitivity Level 1
For Additional Information, see Application Note AND8003/D
- Flammability Rating: UL-94 code V-0 @ 1/8", Oxygen Index 28 to 34
- Transistor Count = 51 devices

For more features, see the data sheet

Part Electrical Specifications

Product	Compliance	Status	Channels	Input Level	Output Level	V_{CC} Typ (V)	f_{Max} Typ (MHz)	t_{pd} Typ (ns)	t_r & t_f Max (ps)	Package Type
MC100ELT20DG	Pb-free Halide free	Active	1	TTL	ECL	5	100	0.82	1500	SOIC-8

For more information please contact your local sales support at www.onsemi.com.

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