

Product Overview

FDMS3602AS: Asymmetric Dual N-Channel PowerTrench® Power Stage MOSFET 25V

For complete documentation, see the data sheet.

This device includes two specialized N-Channel MOSFETs in a dual PQFN package. The switch node has been internally connected to enable easy placement and routing of synchronous buck converters. The control MOSFET (Q1) and synchronous SyncFET (Q2) have been designed to provide optimal power efficiency.

Features

- Q1: N-Channel
Max $r_{DS(on)}$ = 5.6 m Ω at $V_{GS} = 10\text{ V}$, $I_D = 15\text{ A}$
Max $r_{DS(on)}$ = 8.5 m Ω at $V_{GS} = 4.5\text{ V}$, $I_D = 14\text{ A}$
- Q2: N-Channel
Max $r_{DS(on)}$ = 2.2 m Ω at $V_{GS} = 10\text{ V}$, $I_D = 26\text{ A}$
Max $r_{DS(on)}$ = 3.4 m Ω at $V_{GS} = 4.5\text{ V}$, $I_D = 22\text{ A}$
- Low inductance packaging shortens rise/fall times, resulting in lower switching losses
- MOSFET integration enables optimum layout for lower circuit inductance and reduced switch node ringing
- RoHS Compliant

Applications

- Server

Part Electrical Specifications

Product	Compliance	Status	Channel Polarity	Configuration	$V_{SS}^{(BRD)}$ Min (V)	V_{GS}^{Max} (V)	$V_{GS}^{(th)Max}$ (V)	I_D^{Max} (A)	P_D^{Max} (W)	$R_{DS(on)Max}$ @ $V_{GS} = 2.5\text{ V}$ (m Ω)	$R_{DS(on)Max}$ @ $V_{GS} = 4.5\text{ V}$ (m Ω)	$R_{DS(on)Max}$ @ $V_{GS} = 10\text{ V}$ (m Ω)	Q_g^{Typ} @ $V_{GS} = 4.5\text{ V}$ (nC)	Q_g^{Typ} @ $V_{GS} = 10\text{ V}$ (nC)	C_{iss}^{Typ} (pF)	Package Type
FDMS3602AS	Pb-free Halide free	Active	N-Channel	Dual	25	20	3	Q1: 15.0, Q2: 26.0	Q1: 2, Q2: 2.5	-	Q1: 8.5, Q2: 3.4	Q1: 5.6, Q2: 2.2	22	21	3260	PQFN-8

For more information please contact your local sales support at www.onsemi.com.

Created on: 5/19/2019