



Product Overview

AP0100CS: 1 MP Co-Processor

For complete documentation, see the [data sheet](#)

Product Description

AP0100CS is a dedicated image co-processor that enables flexible camera platforms using high performance ON Semiconductor megapixel high dynamic range (HDR) sensors. The two-chip solution of sensor and co-processor allows for multiple camera price and performance points with re-use of circuit board design, fast time to market and design flexibility. Sensor performance is enhanced by the reduction in heat resulting from a separate co-processor chip while at the same time enabling high performance features. The AP0100CS supports 185-degree fisheye lens distortion correction, perspective correction and multiple view options such as split side view, triptych and trailer hitch.

Features

- High Dynamic Range

Applications

- Security

Part Electrical Specifications

Product	Compliance	Status	Type	Megapixels	Frame Rate (fps)	Optical Format	Shutter Type	Pixel Size (µm)	Color	Package Type
AP0100CS2L00SUGA 0-DR	Pb-free Halide free	NEW	CMOS	1.2	45				RGB and Mono	VFBGA-100
AP0100CS2L00SUGA 0-DR-E	Pb-free Halide free	NEW	CMOS	1.2	45				RGB and Mono	VFBGA-100

For more information please contact your local sales support at www.onsemi.com

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