

Product Overview

74VHCT240A: Octal Buffer/Line Driver with 3-STATE Outputs

For complete documentation, see the data sheet.

The VHCT240A is an advanced high speed CMOS octal bus transceiver fabricated with silicon gate CMOS technology. It achieves high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation. The VHCT240A is an inverting 3-STATE buffer having two active-LOW output enables. This device is designed to be used as 3-STATE memory address drivers, clock drivers, and bus oriented transmitter/receivers. Protection circuits ensure that 0V to 7V can be applied to the input and output (Note 1) pins without regard to the supply voltage. These circuits prevent device destruction due to mismatched supply and input/output voltages. This device can be used to interface 5V to 3V systems and two supply systems such as battery back up.

Features

- High Speed: $t_{PD} = 5.6$ ns (typ) at $V_{CC} = 5V$
- Power down protection is provided on inputs and outputs
- Low power dissipation: $I_{CC} = 4$ μA (max) @ $T_A = 25^\circ C$
- Pin and function compatible with 74HCT240

Applications

- This product is general usage and suitable for many different applications.

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
74VHCT240AMTCX	Pb-free	Active	Buffer	8	4.5	5.5	6.1	8	TSSOP-20
74VHCT240ASJX	Pb-free	Active	Buffer	8	4.5	5.5	6.1	8	SOP-20

For more information please contact your local sales support at www.onsemi.com.

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