Product Overview

74AUP1T97: Two-Input, Low Power, Configurable Logic Gate with Translator

For complete documentation, see the data sheet.

The 74AUP1T97 is a universal configurable 2-input logic gate that provides single supply voltage level translation. This device is designed for applications with inputs switching levels that accept 1.8V low voltage CMOS signals while operating from either a single 2.5V or 3.3V supply voltage. The 74AUP1T97 is an ideal low power solution for mixed voltage signal applications especially for battery-powered portable applications. This product guarantees very low static and dynamic power consumption across entire voltage range. All inputs are implemented with hysteresis to allow for slower transition input signals and better switching noise immunity.
The 74AUP1T97 provides for multiple functions as determined by various configurations of the three inputs. The potential logic functions provided are MUX, AND, NAND, OR, and NOR, inverter and buffer.

Features

- Single Supply Voltage Translator
- -1.8V to 3.3V Input at V_{CC}=3.3V
- -1.8V to 2.5V Input at V_{CC}=2.5V
- 2.3V to 3.6V V_{CC} Supply Voltage Operation
- 3.6V Over-Voltage Tolerant I/O's at V_{CC} from 2.3V to 3.6V
- Power-Off High-Impedance Inputs and Outputs
- Low Static Power Consumption
- - I_{CC}=0.9µA Maximum
- Low Dynamic Power Consumption
- - C_{PD}=2.7pF Typical at 3.3V

  For more features, see the data sheet

Applications

- This product is general usage and suitable for many different applications.

Part Electrical Specifications

Product	Compliance	Status	Type	Channels	V_{CC} Min (V)	V_{CC} Max (V)	t_{pd} Max (ns)	I_O Max (mA)	Package Type
74AUP1T97FHX	Pb-free Halide free	Active	SmartGate	1	2.3	3.6	2.7	4	UDFN-6
74AUP1T97L6X	Pb-free Halide free	Active	SmartGate	1	2.3	3.6	2.7	4	SIP-6

For more information please contact your local sales support at www.onsemi.com.

Created on: 1/23/2019