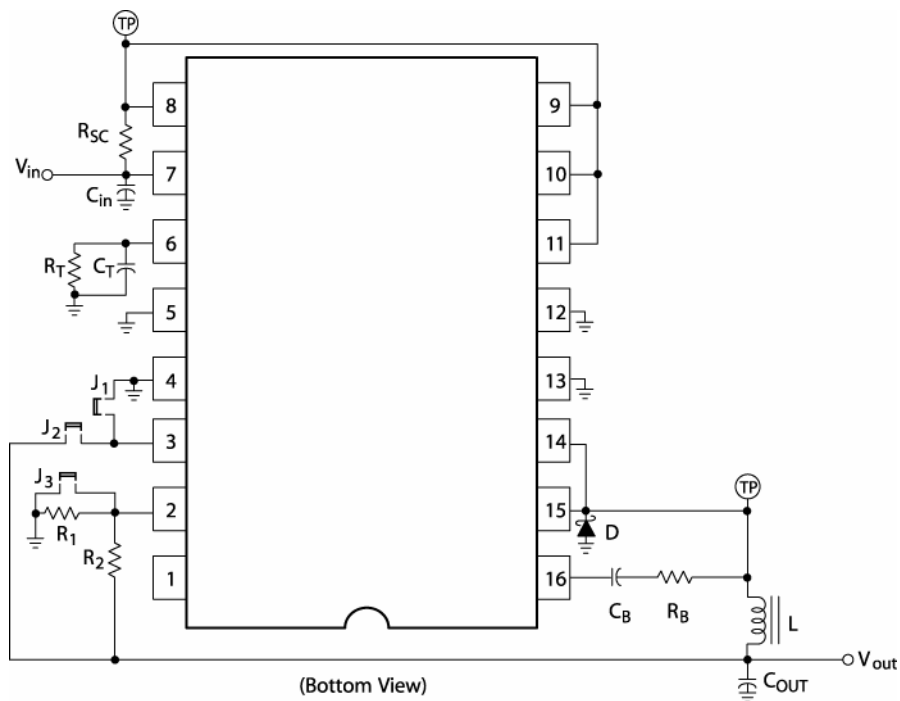


## Schematic for the NCP3163 Buck Evaluation Board

ON Semiconductor®



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For  $V_{out} = 5.0\text{ V}$ : J1=open, J2=closed, and J3=closed

For  $V_{out} = 3.3\text{ V}$ : J1=closed, J2=open, and J3=open