

Test Procedure for the NCP3063BSTEXG Evaluation Board

ON Semiconductor®



12.15.2006

Table 1: Suggested Equipment for Test

Equipment	Quantity	Voltage (V)	Current (A)	Power (W)
DC source	1	12	8	96
Volt Meter	2	14V	NA	NA
Shunt	2	NA	10A	NA
Power Cables	14	NA	10A	NA
Load	1	25V	3A	75W
Oscilloscope	1	25V	NA	NA

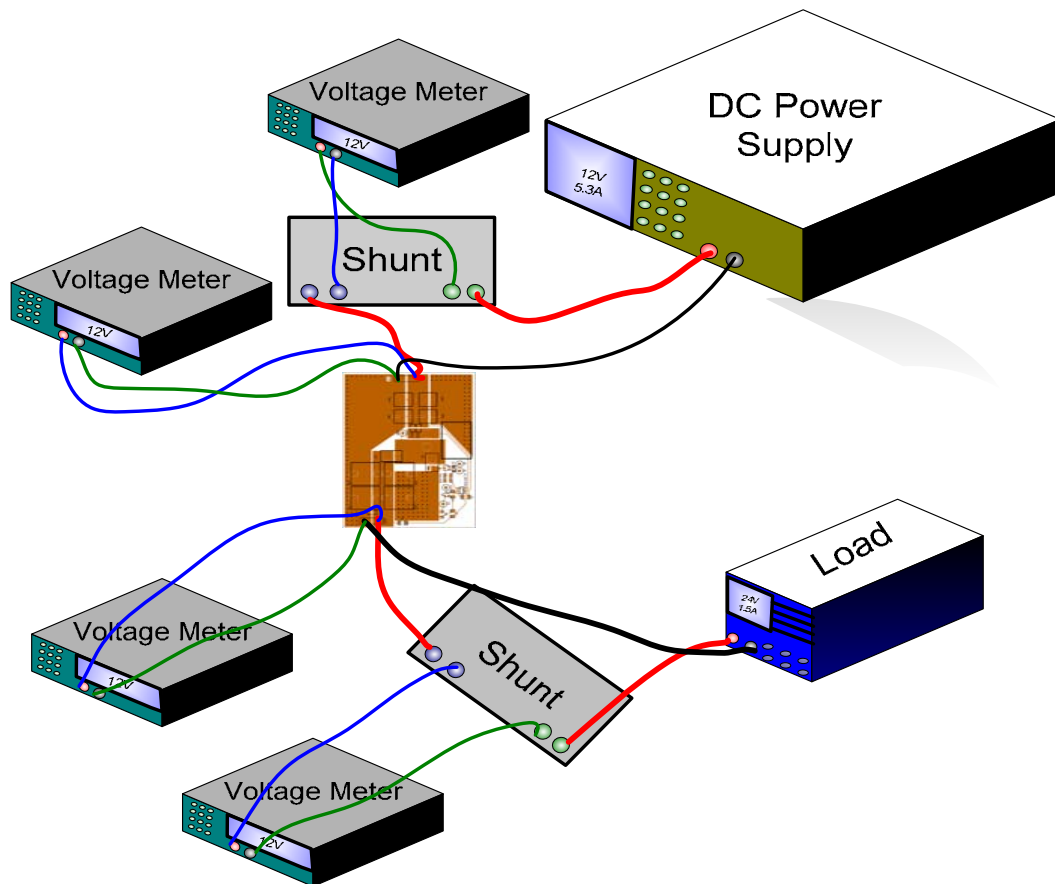


Figure 6: Test Procedure Set up

Test Procedure for Boost Demonstration Board

1. Ensure that the equipment listed in Table 1 is connected using the input and output clips as shown in Figure 6. (The input and output clips are configured in such a way that the user can take voltage measurements at the PCB as shown in Figure 6.)
2. Apply a 12 V input to the part with no load attached to the demonstration board. The user should observe an output voltage of approximately 24 V.
3. Apply any load up to the nominal 2 A load and observe the load regulation and point efficiencies.
4. A 2.5 A load should be applied for short durations, but should be limited in a naturally cooled environment. However, in a forced air or fan cooled environment, loads greater than 2 A can be applied continuously.

Test pins labeled CTP, EN, GD, SE, and SWN should be observed for frequency control signal, enable signal, ground signal, emitter of the Darlington, and switch node of the boost circuit, respectively. The user should ensure good grounding techniques when measuring critical signals.