

MBRS410L, NRVBS410L

Surface Mount Schottky Power Rectifier

This device employs the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes, in surface mount applications where compact size and weight are critical to the system. Typical applications are AC-DC and DC-DC converters, reverse battery protection, and “ORing” of multiple supply voltages and any other application where performance and size are critical.

Features

- Ultra Low V_F
- 1st in the Market Place with a 10 V_R Schottky Rectifier
- Small Compact Surface Mountable Package with J-Bend Leads
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- Very Low Forward Voltage Drop
- Excellent Ability to Withstand Reverse Avalanche Energy Transients
- Guard-Ring for Stress Protection
- NRVBS Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 217 mg (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Polarity: Notch in Plastic Body Indicates Cathode Lead
- ESD Ratings: Machine Model = C
Human Body Model = 3B

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	10	V
Average Rectified Forward Current (@ $T_L = 110^\circ\text{C}$)	I_O	4.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I_{FSM}	150	A
Operating Junction Temperature	T_J	-65 to +125	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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SCHOTTKY BARRIER RECTIFIERS 4.0 AMPERES, 10 VOLTS



SMC
CASE 403

MARKING DIAGRAM



B4L1 = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
MBRS410LT3G	SMB (Pb-Free)	2500 / Tape & Reel
NRVBS410LT3G	SMB (Pb-Free)	2500 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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THERMAL CHARACTERISTICS

Characteristic	Symbol	Min Pad (Note 2)	1 Inch Pad	Unit
Thermal Resistance, Junction-to-Lead	$R_{\theta JL}$	12	7.0	$^{\circ}\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	109	59	

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (Note 1)	V_F	$T_J = 25^{\circ}\text{C}$	$T_J = 100^{\circ}\text{C}$	V
($I_F = 2.0\text{ A}$)		0.31	0.200	
($I_F = 4.0\text{ A}$)		0.33	0.225	
($I_F = 8.0\text{ A}$)		0.35	0.250	
Maximum Instantaneous Reverse Current (Note 1)	I_R	$T_J = 25^{\circ}\text{C}$	$T_J = 100^{\circ}\text{C}$	mA
(Rated dc Voltage, $V_R = 5.0\text{ V}$)		2.0	100	
(Rated dc Voltage, $V_R = 10\text{ V}$)		5.0	200	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2\%$.
2. Mounted with Minimum Recommended Pad Size, PC Board FR4.

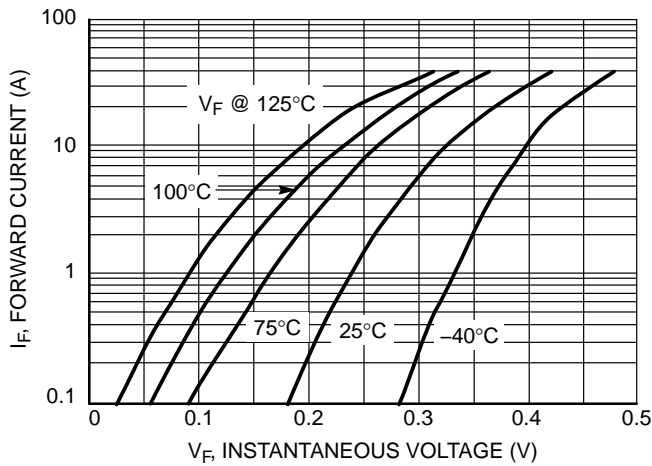


Figure 1. Typical Forward Voltage

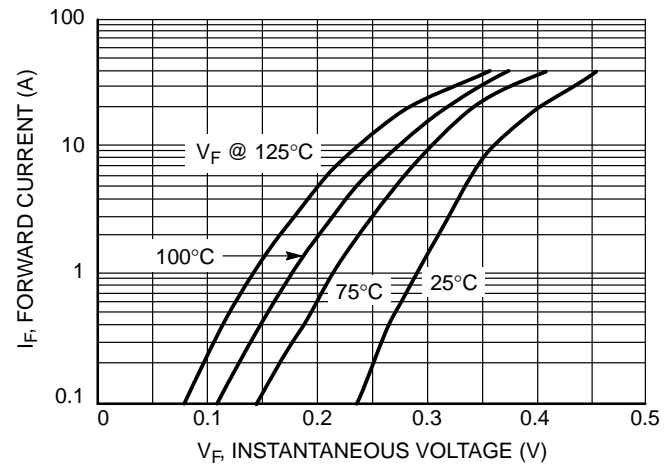


Figure 2. Maximum Forward Voltage

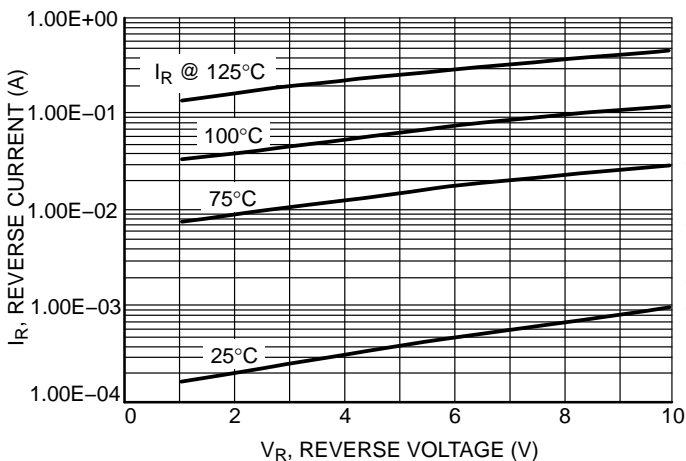


Figure 3. Typical Reverse Current

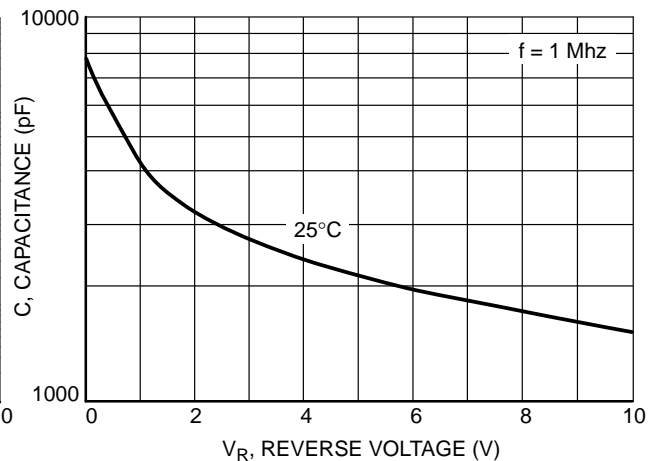


Figure 4. Typical Capacitance

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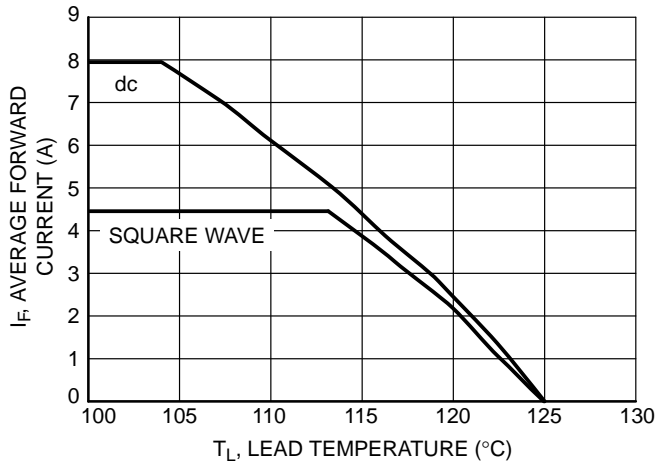


Figure 5. Current Derating (Junction-to-Lead)

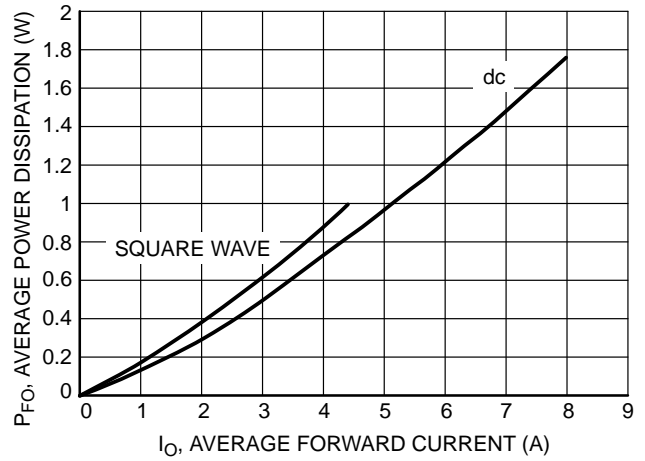


Figure 6. Forward Power Dissipation

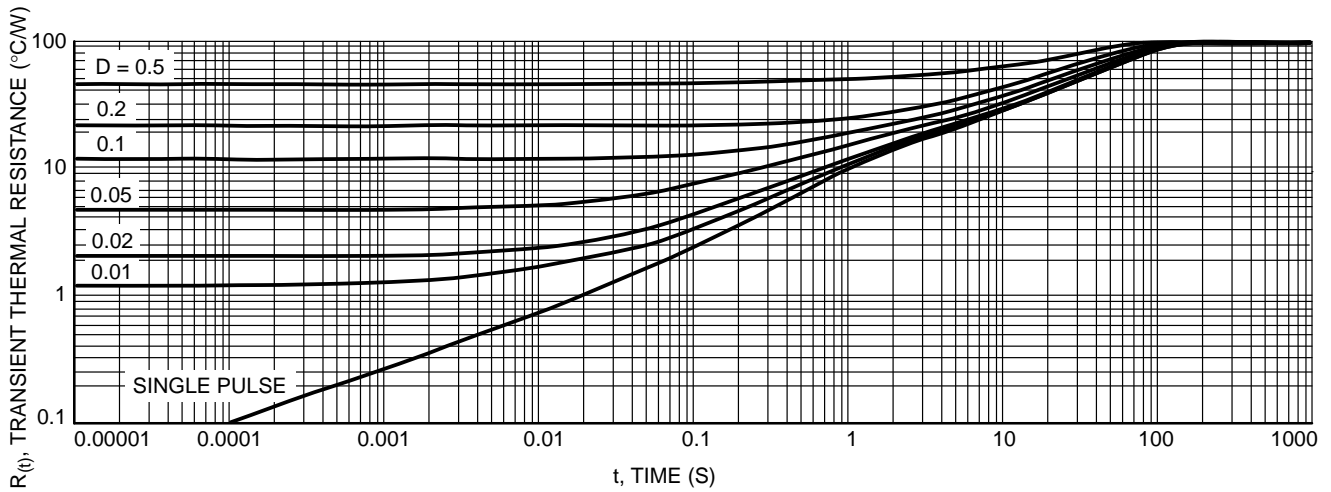


Figure 7. Thermal Response, Junction-to-Ambient (min pad)

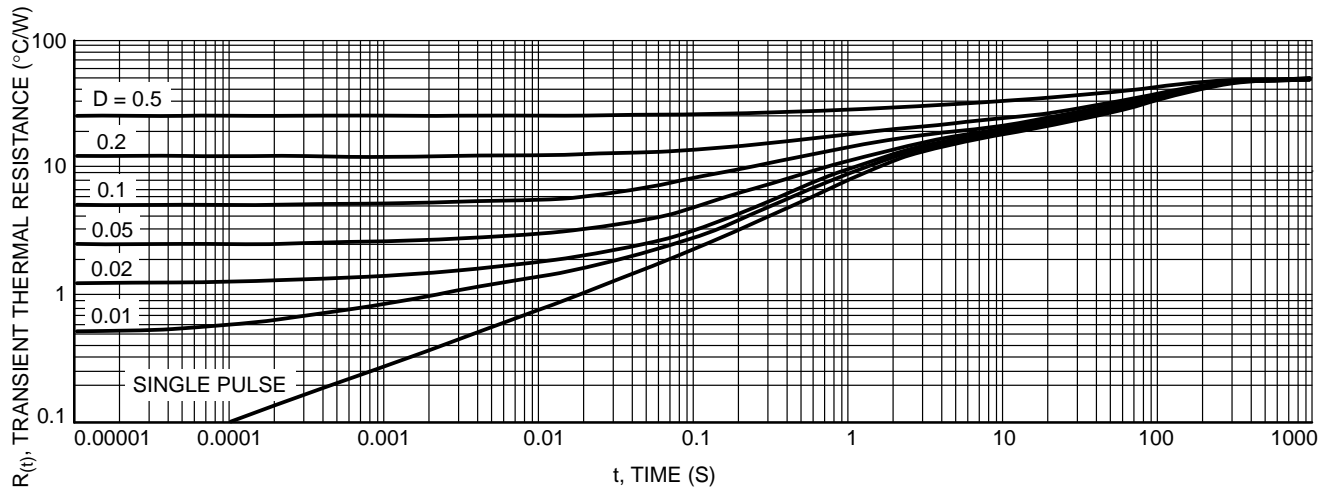
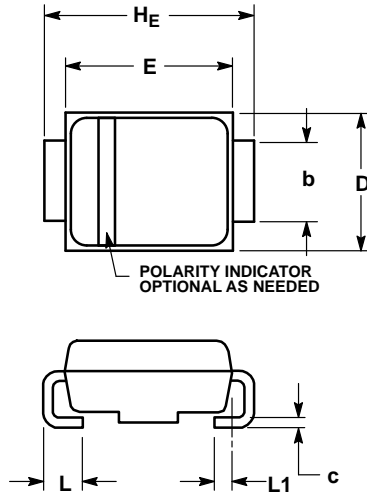


Figure 8. Thermal Response, Junction-to-Ambient (1 inch pad)

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PACKAGE DIMENSIONS

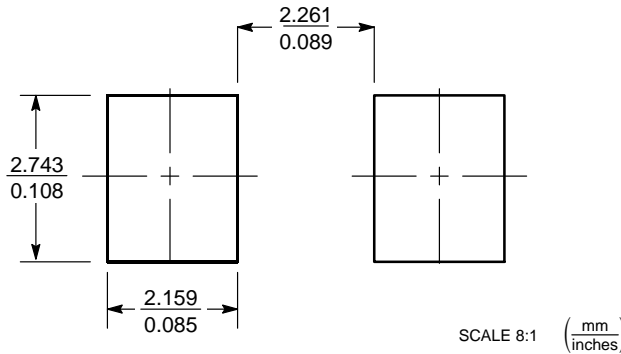
SMB
CASE 403A-03
ISSUE J




- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION b SHALL BE MEASURED WITHIN DIMENSION L1.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.95	2.30	2.47	0.077	0.091	0.097
A1	0.05	0.10	0.20	0.002	0.004	0.008
b	1.96	2.03	2.20	0.077	0.080	0.087
c	0.15	0.23	0.31	0.006	0.009	0.012
D	3.30	3.56	3.95	0.130	0.140	0.156
E	4.06	4.32	4.60	0.160	0.170	0.181
HE	5.21	5.44	5.60	0.205	0.214	0.220
L	0.76	1.02	1.60	0.030	0.040	0.063
L1	0.51 REF			0.020 REF		

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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