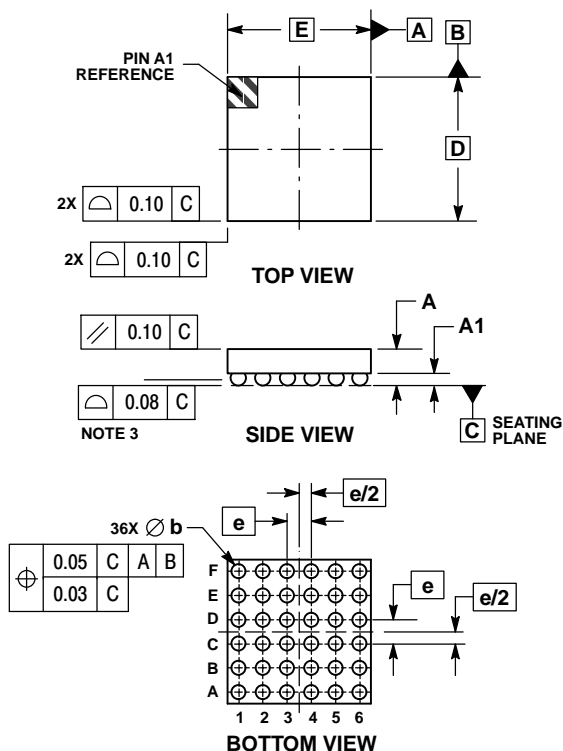




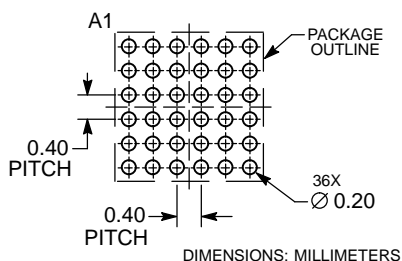
**SCALE 2:1**

**WLCSP36 2.375x2.375**  
CASE 567GW  
ISSUE A

DATE 29 APR 2014



**RECOMMENDED  
SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual. SOLDERRM/D.

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

DIM	MILLIMETERS	
	MIN	MAX
A	—	0.60
A1	0.17	0.23
b	0.24	0.29
D	2.375 BSC	
E	2.375 BSC	
e	0.40 BSC	

### GENERIC MARKING DIAGRAM\*



A = Assembly Location  
WL = Wafer Lot  
Y = Year  
WW = Work Week  
▪ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "■", may or may not be present.

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<b>NEW STANDARD:</b>		
<b>DESCRIPTION:</b>	<b>WLCSP36 2.375X2.375</b>	<b>PAGE 1 OF 2</b>

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