




---

**PRODUCT BULLETIN**  
Generic Copy

---

**09-JUL-2005**

**SUBJECT: ON Semiconductor Product Bulletin #14189**

**TITLE: Incorrect Datasheet Limits for I<sub>ee</sub> on MC100LVEP11**

**EFFECTIVE DATE: 29-Jun-2005**

**AFFECTED PRODUCT DIVISION: Analog Products**

**FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:**

Contact your local ON Semiconductor sales office or Greg Hooker<FFMGNR@onsemi.com>

**NOTIFICATION TYPE:**

ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing. To do so, contact your local ON Semiconductor sales office.

**DESCRIPTION AND PURPOSE:**

ON Semiconductor has identified and will be correcting the I<sub>ee</sub> limits in its datasheet for the MC100LVEP11 device. The listed limits are approximately 5 mA lower than they should be for this device and not matching similar devices within the family.

1.) In Table 4, Table 5, Table 6,

10LVEP11 Current Datasheet Limits for I<sub>ee</sub>

	-40 deg C			25 deg C			85 deg C		
Symbol	min	typ	max	min	typ	max	min	typ	max
I <sub>ee</sub> (mA)	24	30	36	24	30	36	25	31	38

10LVEP11 Corrected Datasheet Limits for I<sub>ee</sub>

	-40 deg C			25 deg C			85 deg C		
Symbol	min	typ	max	min	typ	max	min	typ	max
I <sub>ee</sub> (mA)	25	33	40	29	33	40	32	34	42

2.) In Table 7, Table 8, Table 9,

100LVEP11 Current Datasheet Limits for I<sub>ee</sub>

	-40 deg C			25 deg C			85 deg C		
Symbol	min	typ	max	min	typ	max	min	typ	max
I <sub>ee</sub> (mA)	25	31	37	29	35	41	32	38	45

100LVEP11 Corrected Datasheet Limits for I<sub>ee</sub>

	-40 deg C			25 deg C			85 deg C		
Symbol	min	typ	max	min	typ	max	min	typ	max
I <sub>ee</sub> (mA)	25	35	42	29	38	46	32	41	50

**AFFECTED DEVICE LIST (WITHOUT SPECIALS)****PART**

MC100LVEP11D  
MC100LVEP11DG  
MC100LVEP11DR2  
MC100LVEP11DR2G  
MC100LVEP11DT  
MC100LVEP11DTG  
MC100LVEP11DTR2  
MC100LVEP11DTR2G  
MC10LVEP11D  
MC10LVEP11DG  
MC10LVEP11DR2  
MC10LVEP11DR2G  
MC10LVEP11DT  
MC10LVEP11DTG  
MC10LVEP11DTR2  
MC10LVEP11DTR2G