

**ON Semiconductor**



Subject: ON Semiconductor PRODUCT/PROCESS CHANGE NOTIFICATION 10180

TITLE: 48LD TSSOP 3-LAYER PRE-PLATED LEAD FRAME

EFFECTIVE DATE: 05-AUG-00

AFFECTED CHANGE CATEGORIES

Package Change

On Semiconductor Assy Site

AFFECTED PRODUCT DIVISIONS

LOGIC PRODUCTS DIV

ADDITIONAL RELIABILITY DATA: Available

Ref: RJJ930

Contact your local ON Semiconductor Sales Office.

SAMPLES: Contact Below

Ref: R12237

Contact your local ON Semiconductor Sales Office.

For any questions concerning this notification:

REFERENCE:WON KANG

PHONE: 602-244-4387

DISCLAIMER:

ON SEMICONDUCTOR WILL CONSIDER THIS CHANGE APPROVED UNLESS SPECIFIC  
CONDITIONS OF ACCEPTANCE ARE PROVIDED IN WRITING WITHIN 30 DAYS OF  
RECEIPT OF THIS NOTICE. TO DO SO, CONTACT YOUR LOCAL ON  
SEMICONDUCTOR SALES OFFICE.

GPCN FORMAT: CUSTOMER

DO NOT REPLY TO THIS MESSAGE.

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PRODUCT/PROCESS CHANGE NOTIFICATION

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ISSUE DATE: 27-Apr-2000

NOTIFICATION #:10180

EFFECTIVE DATE: 05-Aug-2000

ISSUING DIVISION:ON SEMICONDUCTOR

DESCRIPTION AND PURPOSE

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Since November 1999, ON Semiconductor has been building 14/16/20LD TSSOP using 3-layer (Ni-Pd-Au) pre-plated lead frames in Carmona Philippines. Carmona has recently qualified the 48LD TSSOP package using the 3-layer (Ni-Pd-Au) pre-plated frames. This change is being implemented to expand assembly capacity to provide enhanced customer support. Upon expiration of this PCN, customers may receive 48ld TSSOP products with either of these 2 processes: Sn-Pb finish or 3-layer Ni-Pd-Au.

QUALIFICATION PLAN

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See Rel Data

RELIABILITY DATA SUMMARY

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Solderability (no Steam age) - 3 lots x 0/15

Solderability (1 Hr Steam age) - 3 lots x 0/15

Wettability - 3 lots x 0/10

AC w/ PC IR Reflow @ 220C 96 Hrs - 3 lots x 0/77

AC w/ PC IR Reflow @ 245C 96 Hrs - 3 lots x 0/77

AC w/ PC Oil Immersion @ 260C 96 Hrs- 3 lots x 0/77

TC w/ PC IR Reflow @ 220C 500 cyc - 3 lots x 0/77

TC w/ PC IR Reflow @ 245C 500 cyc - 3 lots x 0/77

TC w/ PC Oil Immersion @ 260C 500 cyc-3 lots x 0/77

AC 240 Hrs- 3 lots x 0/77

TC 1000 cyc - 3 lots x 0/77

High Temp Storage @175C - 3 lots x 0/77

ELECTRICAL CHARACTERISTIC SUMMARY

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Final Test Yield mean exceeds 98% - 3 lots

CHANGED PART IDENTIFICATION

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Assembly location marking ID

FILE FORMAT: ASCII TEXT

FONT - Courier

SIZE - 12 Point

LINE - 70 characters/line

PAGE - 55 lines/page

PAGEBREAK CHARACTER - ^L (Control L)

AFFECTED DEVICE LIST (WITHOUT SPECIALS)

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MC74LCX16240DT , MC74LCX16240DTR2, MC74LCX16244DT , MC74LCX16244DTR2

MC74LCX16245DT , MC74LCX16373DT , MC74LCX16373DTR2, MC74LCX16374DT

MC74LCX16374DTR2