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<u>MOSFET</u> – Power, Single N-Channel

40 V, 1.3 mΩ, 235 A NTMFS5C426N

Features

- Small Footprint (5x6 mm) for Compact Design
- Low R_{DS(on)} to Minimize Conduction Losses
- Low Q_G and Capacitance to Minimize Driver Losses
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V _{DSS}	40	V
Gate-to-Source Voltage			V _{GS}	±20	V
Continuous Drain	Steady State	$T_{C} = 25^{\circ}C$	۱ _D	235	А
Current R _{θJC} (Notes 1, 3)		T _C = 100°C		166	
Power Dissipation		$T_{C} = 25^{\circ}C$	PD	128	W
$R_{\theta JC}$ (Note 1)		$T_{C} = 100^{\circ}C$		64	
Continuous Drain Current R _{θJA} (Notes 1, 2, 3)	Steady State	T _A = 25°C	۱ _D	41	А
		T _A = 100°C		29	
Power Dissipation		T _A = 25°C	PD	3.8	W
$R_{\theta JA}$ (Notes 1, 2)		T _A = 100°C		1.9	
Pulsed Drain Current	$T_A = 25^{\circ}C$, $t_p = 10 \ \mu s$		I _{DM}	900	А
Operating Junction and Storage Temperature Range			T _J , T _{stg}	–55 to +175	°C
Source Current (Body Diode)			۱ _S	122	А
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 19 A)			E _{AS}	739	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			ΤL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE MAXIMUM RATINGS

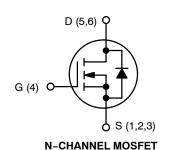
Parameter	Symbol	Value	Unit
Junction-to-Case - Steady State	$R_{\theta JC}$	1.2	°C/W
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	39	

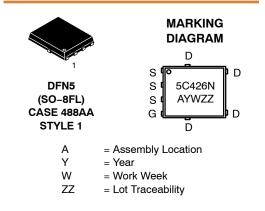
 The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
Surface, mounted on EP4 heard using a 650 mm², 0 are Current.

2. Surface-mounted on FR4 board using a 650 mm², 2 oz. Cu pad.

3. Maximum current for pulses as long as 1 second is higher but is dependent on pulse duration and duty cycle.

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
40 V	1.3 m Ω @ 10 V	235 A





ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet. NOTE: Some of the device on this data sheet have been

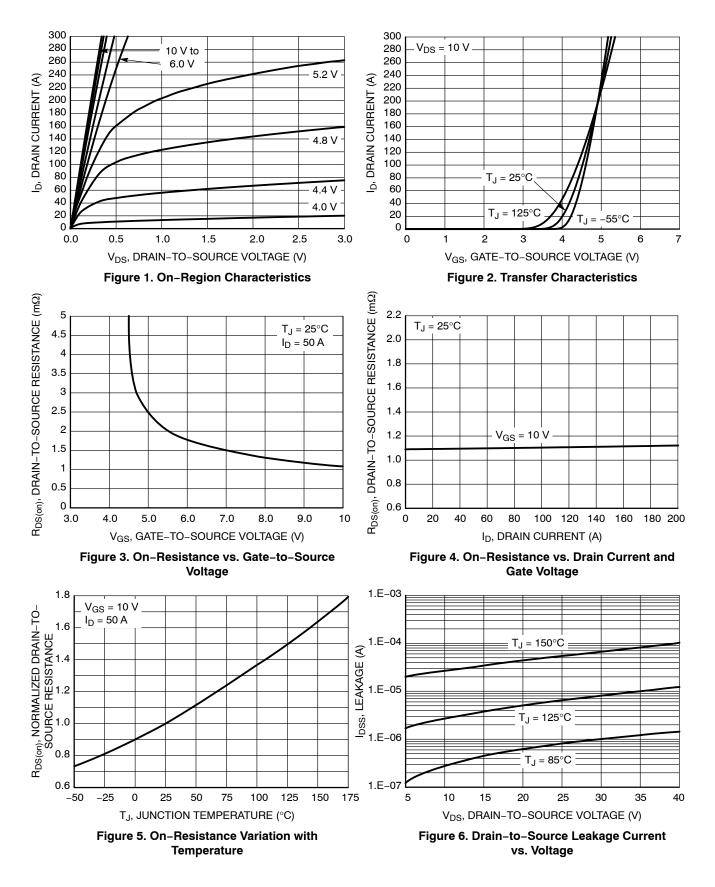
DISCONTINUED. Please refer to the table on page 5.

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise specified)

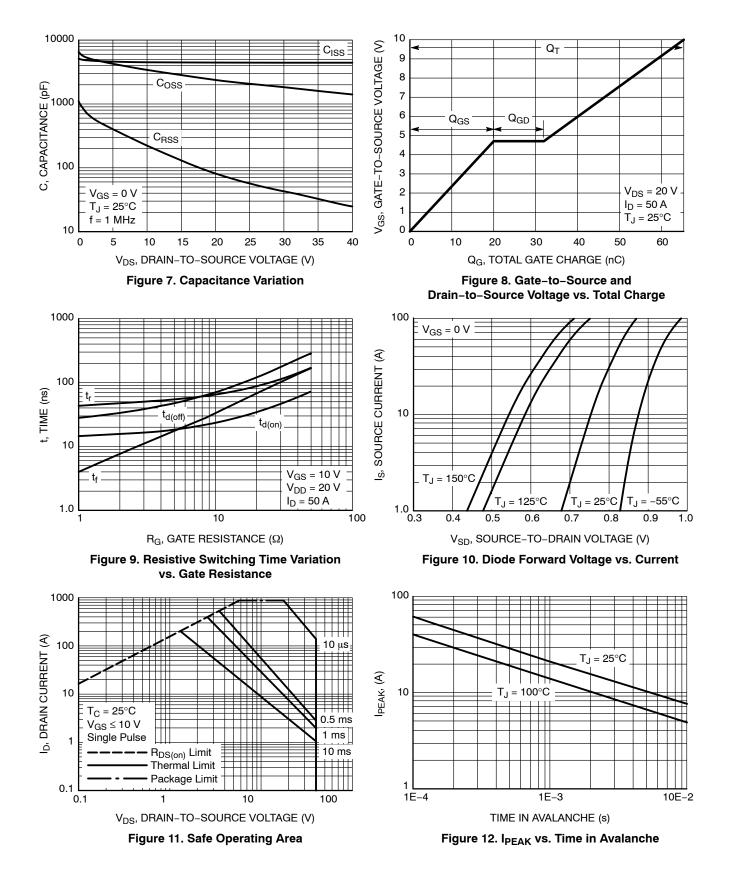
Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 250 μ A		40			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} / T _J				9.6		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V, T_J = 25^{\circ}C$				10	
		V _{DS} = 40 V T _J =	T _J = 125°C			100	μΑ
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 V, V_{GS}$	_S = 20 V			100	nA
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D$	= 170 μA	2.5		3.5	V
Threshold Temperature Coefficient	V _{GS(TH)} /T _J				-8.6		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 50 A		1.1	1.3	mΩ
Forward Transconductance	9 _{FS}	V _{DS} =15 V, I _D	= 50 A		145		S
CHARGES, CAPACITANCES & GATE RE	SISTANCE						
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 25 V			4300		
Output Capacitance	C _{OSS}				2100		pF
Reverse Transfer Capacitance	C _{RSS}				59		
Total Gate Charge	Q _{G(TOT)}	V_{GS} = 10 V, V_{DS} = 20 V; I_{D} = 50 A			65		
Threshold Gate Charge	Q _{G(TH)}	V _{GS} = 10 V, V _{DS} = 20 V; I _D = 50 A			13		
Gate-to-Source Charge	Q _{GS}				20		nC
Gate-to-Drain Charge	Q _{GD}				12		
Plateau Voltage	V _{GP}				4.7		V
SWITCHING CHARACTERISTICS (Note 5	5)						
Turn-On Delay Time	t _{d(ON)}				15		
Rise Time	tr	V_{GS} = 10 V, V_{DS} = 20 V, I_{D} = 50 A, R_{G} = 2.5 Ω			47		ns
Turn-Off Delay Time	t _{d(OFF)}				36		
Fall Time	t _f				9.0		
DRAIN-SOURCE DIODE CHARACTERIS	TICS						
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V,	$T_J = 25^{\circ}C$		0.82	1.2	
		$I_{\rm S} = 50 \rm A$	T _J = 125°C		0.68		V
Reverse Recovery Time	t _{RR}	V _{GS} = 0 V, dIS/dt = 100 A/µs, I _S = 50 A			63		
Charge Time	t _a				34		ns
Discharge Time	t _b				29		1
Reverse Recovery Charge	Q _{RR}				92		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 4. Pulse Test: pulse width $\leq 300 \ \mu$ s, duty cycle $\leq 2\%$. 5. Switching characteristics are independent of operating junction temperatures.

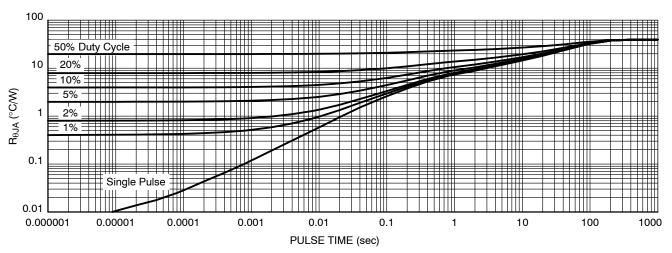
TYPICAL CHARACTERISTICS

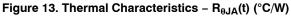


TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS





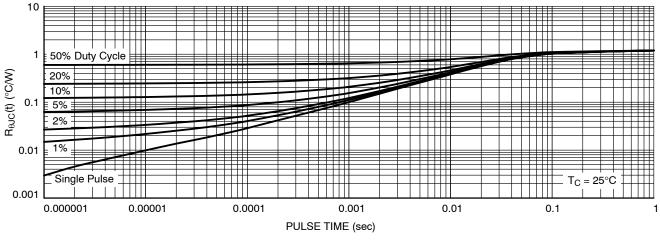


Figure 14. Thermal Characteristics – $R_{\theta JC}(t)$ (°C/W)

ORDERING INFORMATION

Device	Marking	Package	Shipping [†]
NTMFS5C426NT1G	5C426N	DFN5 (Pb-Free)	1500 / Tape & Reel

DISCONTINUED (Note 6)

NTMFS5C426NT3G	5C426N	DFN5	5000 / Tape & Reel
		(Pb–Free)	

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

6. **DISCONTINUED:** This device is not recommended for new design. Please contact your **onsemi** representative for information. The most current information on this device may be available on <u>www.onsemi.com</u>.

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