

# **N-Channel JFET, Dual**

## -15 V, 10 to 32 mA, 35 ms

## NSVJ5908DSG5

Automotive JFET designed for compact and efficient designs and including high gain performance. AEC-Q101 qualified JFET and PPAP capable suitable for automotive applications.

#### **Features**

- Large | yfs |
- Small Ciss
- This Small Package Enables Sets to be Smaller and Thinner
- Ultralow Noise Figure
- MCPH5 Package is Pin-compatible with SC-88AFL
- Composite Type with 2 JFET Contained in a MCPH5 Package Currently in Use, Improving the Mounting Efficiency Greatly
- The NSVJ5908DSG5 is Formed with Two Chips, Being Equivalent to the NSVJ3557SA3, Placed in One Package
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements;
  AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

## **Typical Applications**

- AM Tuner RF Amplification
- Low Noise Amplifier

#### SPECIFICATIONS ABSOLUTE MAXIMUM RATINGS TA = 25°C

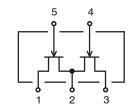
Symbol	Parameter	Value	Unit
$V_{DSX}$	Drain-to-Source Voltage	15	V
$V_{GDS}$	Gate-to-Drain Voltage	-15	V
I <sub>G</sub>	Gate Current	10	mA
I <sub>D</sub>	Drain Current	50	mA
P <sub>D</sub>	Allowable Power Dissipation - 1 unit	200	mW
P <sub>T</sub>	Total Power Dissipation	300	mW
T <sub>J</sub> , T <sub>Stg</sub>	Operating Junction and Storage Temperature	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



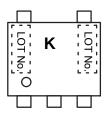
#### **ELECTRICAL CONNECTION**

#### N-Channel



- 1: Drain1
- 2: Source1/Source2
- 3: Drain 2
- 4: Gate2
- 5: Gate1

#### **MARKING DIAGRAM**



K = Specific Device Code

#### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

## NSVJ5908DSG5

Table 1. ELECTRICAL CHARACTERISTICS  $T_A = 25$ °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Gate-to-Drain Breakdown Voltage	V <sub>(BR)GDS</sub>	$I_G = -10 \mu\text{A},  V_{DS} = 0  \text{V}$	-15	-	-	V
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = -10 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	-1.0	nA
Cutoff Voltage	V <sub>GS(off)</sub>	$V_{DS} = 5 \text{ V}, I_D = 100 \mu\text{A}$	-0.3	-0.7	-1.5	V
Zero-Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 0 V	10	-	32	mA
Forward Transfer Admittance	yfs	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 0 V, f = 1 kHz	24	35	-	mS
Input Capacitance	Ciss	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	10.5	-	pF
Reverse Transfer Capacitance	Crss	1	-	3.5	_	pF
Noise Figure	NF	$V_{DS} = 5 \text{ V}, \text{ Rg} = 1 \text{ k}\Omega, I_D = 1 \text{ mA}, f = 1 \text{ kHz}$	-	1.0	-	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

NOTE: The specifications shown above are for each individual JFET.

## **ORDERING INFORMATION**

Device	Marking	Package Type	Shipping <sup>†</sup>
NSVJ5908DSG5T1G	К	SC-88AFL / MCPH5 (Pb-Free / Halogen Free)	3,000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, <u>BRD8011/D</u>.

## NSVJ5908DSG5

#### **TYPICAL CHARACTERISTICS**

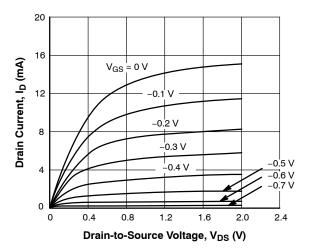


Figure 1. I<sub>D</sub> vs. V<sub>DS</sub>

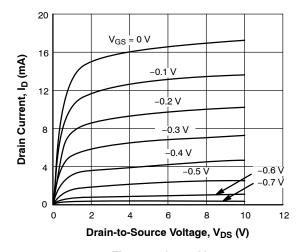


Figure 2.  $I_D$  vs.  $V_{DS}$ 

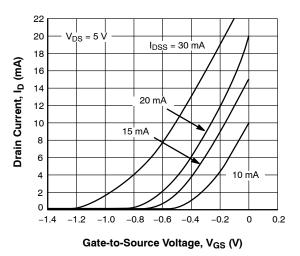


Figure 3. I<sub>D</sub> vs. V<sub>GS</sub>

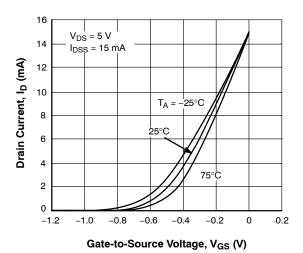


Figure 4.  $I_D$  vs.  $V_{GS}$ 

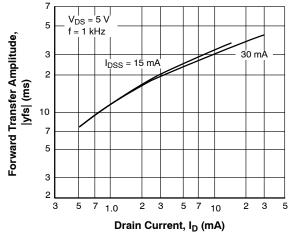


Figure 5. |yfs| vs. I<sub>D</sub>

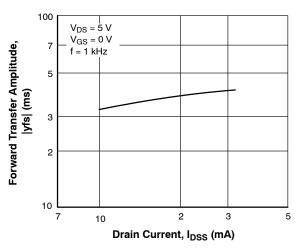


Figure 6. |yfs| vs. I<sub>DSS</sub>

## NSVJ5908DSG5

## TYPICAL CHARACTERISTICS (continued)

3

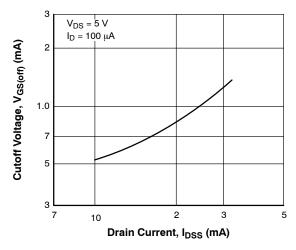


Figure 7. V<sub>GS(Off)</sub> vs. I<sub>DSS</sub>

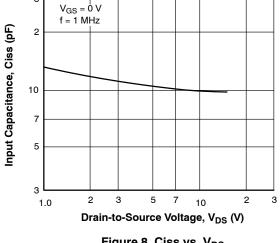


Figure 8. Ciss vs.  $V_{DS}$ 

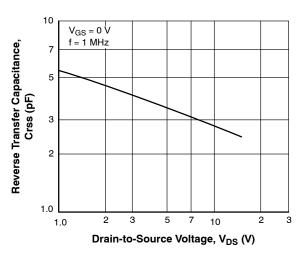


Figure 9. Crss vs. V<sub>DS</sub>

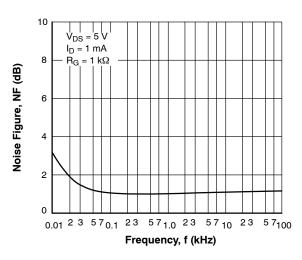


Figure 10. NF vs. f

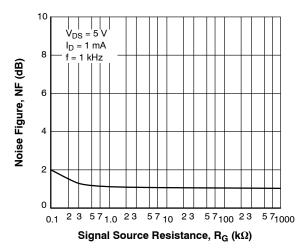


Figure 11. NF vs. R<sub>G</sub>

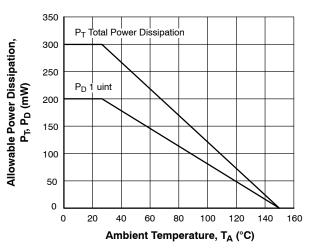
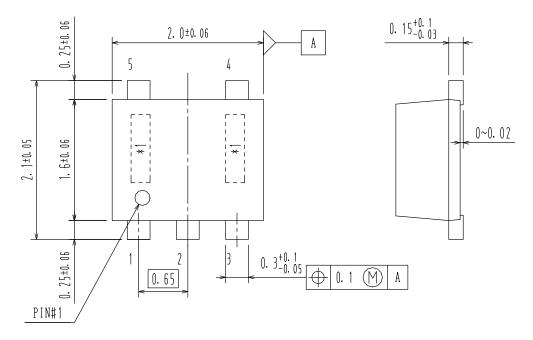


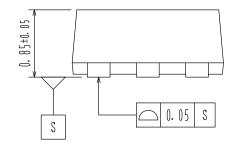
Figure 12. P<sub>T</sub>, P<sub>D</sub> vs. T<sub>A</sub>

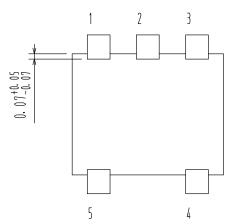


### SC-88AFL/ MCPH5 CASE 419AP ISSUE O

**DATE 30 NOV 2011** 







DOCUMENT NUMBER:	98AON65479E	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	SC-88AFL / MCPH5		PAGE 1 OF 1	

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

#### ADDITIONAL INFORMATION

**TECHNICAL PUBLICATIONS:** 

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$ 

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales