onsemi

N-Channel JFET, -25 V, 20 to 40 mA, 40 mS

Automotive JFET designed for compact and efficient designs and

including high gain performance. AEC-Q101 qualified JFET and

NSVJ3910SB3

• High Forward Transfer Admittance

• High Breakdown Voltage

Low Input CapacitanceLow Noise Figure



CPH3 CASE 318BA

MARKING DIAGRAM



J2 = Specific Device Code

ELECTRICAL CONNECTION

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Typical Applications

• Low Noise Amplifier for Automotive AM Radio

PPAP capable suitable for automotive applications.

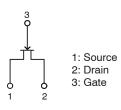
Specifications

Features

ABSOLUTE MAXIMUM RATINGS (at T_A = 25° C)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V _{DSX}	25	V
Gate-to-Drain Voltage	V _{GDS}	-25	V
Gate Current	l _G	10	mA
Drain Current	Ι _D	50	mA
Allowable Power Dissipation	PD	400	mW
Operating Junction and Storage Temperature	T _J , T _{STG}	–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



N-Channel

ORDERING INFORMATION

Device	Package	Shipping [†]
NSVJ3910SB3T1G	CPH3 (Pb-Free)	3000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NSVJ3910SB3

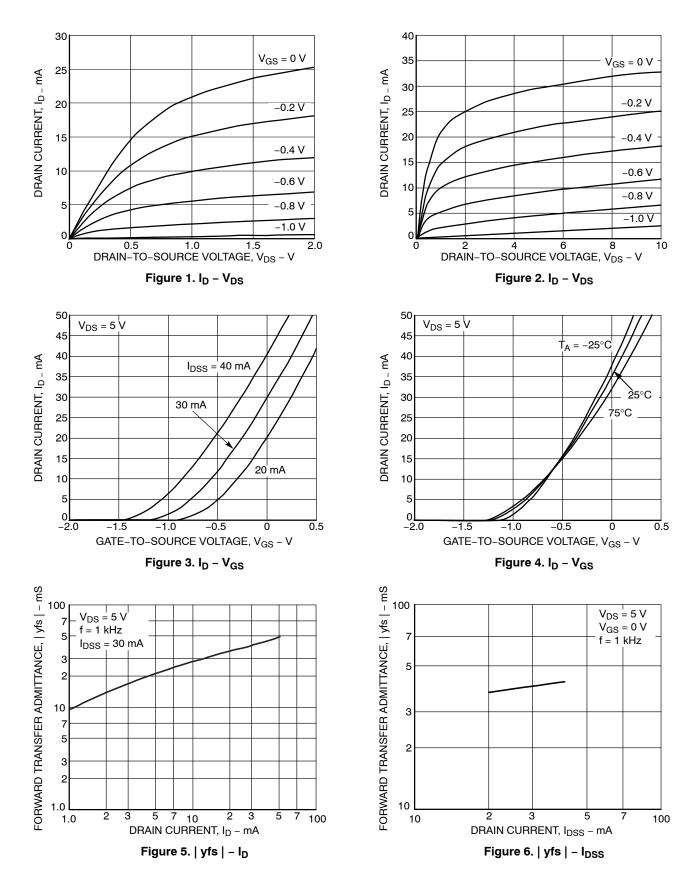
ELECTRICAL CHARACTERISTICS (at $T_A = 25^{\circ}C$)

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Gate-to-Drain Breakdown Voltage	V _{(BR)GDS}	$I_G = -10 \ \mu A$, $V_{DS} = 0 \ V$	-25	-	-	V
Gate Cutoff Current	I _{GSS}	$V_{GS} = -10 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$	-	-	-1.0	nA
Cutoff Voltage	V _{GS(off)}	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 100 \mu\text{A}$	-0.6	-1.2	-1.8	V
Drain Current	I _{DSS}	$V_{DS} = 5 \text{ V}, V_{GS} = 0 \text{ V}$	20	-	40	mA
Forward Transfer Admittance	yfs	$V_{DS} = 5 V, V_{GS} = 0 V, f = 1 kHz$	30	40	-	mS
Input Capacitance	Ciss	V_{DS} = 5 V, V_{GS} = 0 V, f = 1 MHz	-	6.0	-	pF
Reverse Transfer Capacitance	Crss]	-	2.3	-	pF
Noise Figure	NF	V_{DS} = 5 V, V_{GS} = 0 V, f = 100 MHz	-	2.1	2.8	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

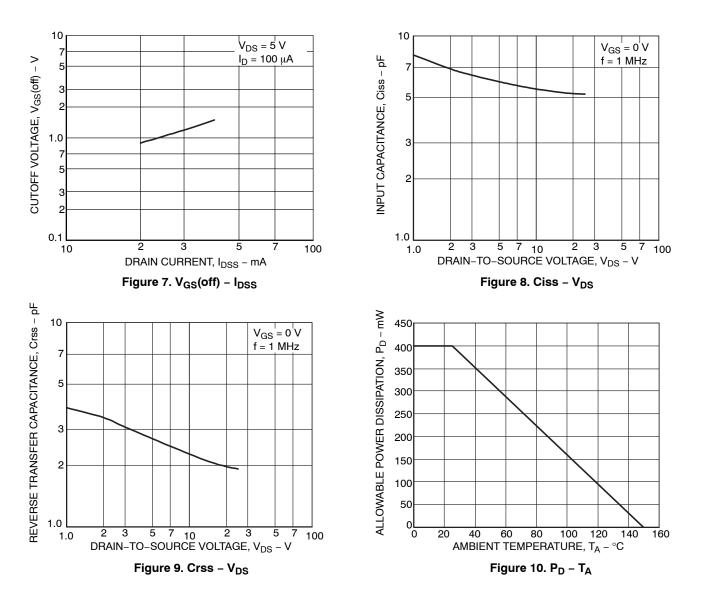
NSVJ3910SB3

TYPICAL CHARACTERISTICS



NSVJ3910SB3

TYPICAL CHARACTERISTICS (CONTINUED)



RECOMMENDED SOLDERING FOOTPRINT

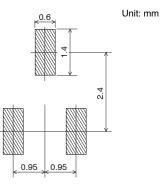
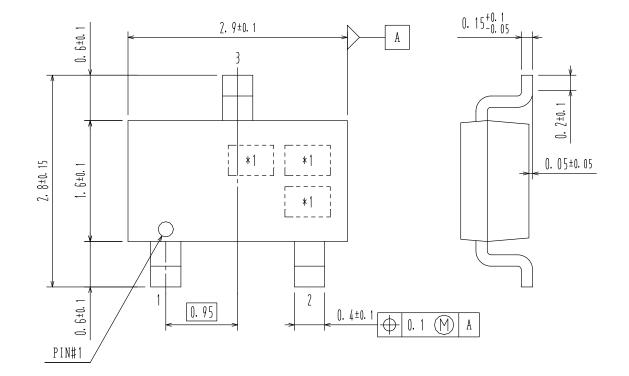


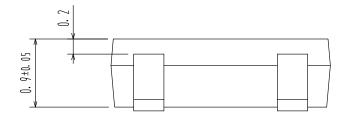
Figure 11. Recommended Soldering Footprint



CPH3 CASE 318BA ISSUE O

DATE 30 NOV 2011





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