IGBT - Inverter Welding

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss. The IGBT is well suited for welding applications. Incorporated into the device is a soft and fast co–packaged free wheeling diode with a low forward voltage.

Features

- $T_{Jmax} = 175^{\circ}C$
- Soft Fast Reverse Recovery Diode
- Optimized for High Speed Switching
- 5 µs Short–Circuit Capability
- This is a Pb-Free Device

Typical Applications

Welding

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V_{CES}	600	V
Collector current @ Tc = 25°C @ Tc = 100°C	I _C	100 50	A
Diode Forward Current @ Tc = 25°C @ Tc = 100°C	l _F	100 50	A
Diode Pulsed Current T _{PULSE} Limited by T _J Max	I _{FM}	200	Α
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	200	Α
Short–circuit withstand time $V_{GE} = 15 \text{ V}, V_{CE} = 400 \text{ V},$ $T_{J} \le +150 ^{\circ}\text{C}$	t _{SC}	5	μS
Gate-emitter voltage	$V_{\sf GE}$	±20	V
Transient gate-emitter voltage (T _{PULSE} = 5 μ s, D < 0.10)		±30	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P _D	417 208	W
Operating junction temperature range	TJ	-55 to +175	°C
Storage temperature range	T _{stg}	-55 to +175	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T _{SLD}	260	°C

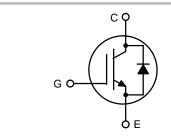
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

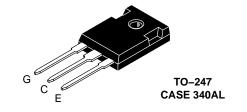


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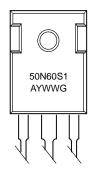
www.onsemi.com

50 A, 600 V V_{CEsat} = 1.80 V E_{OFF} = 0.46 mJ





MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
NGTB50N60S1WG	TO-247 (Pb-Free)	30 Units / Rail

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ heta JC}$	0.36	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ heta JC}$	0.60	°C/W
Thermal resistance junction-to-ambient	$R_{ heta JA}$	40	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC						
Collector–emitter breakdown voltage, gate–emitter short–circuited	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu\text{A}$	V _{(BR)CES}	600	_	-	V
Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 50 A V _{GE} = 15 V, I _C = 50 A, T _J = 175°C	V _{CEsat}	1.50 -	1.80 2.19	2.00	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 350 \mu A$	V _{GE(th)}	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	$V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}$ $V_{GE} = 0 \text{ V}, V_{CE} = 600 \text{ V}, T_{J=150^{\circ}\text{C}}$	I _{CES}	_ _	- -	0.5 4.0	mA
Gate leakage current, collector-emitter short-circuited	V _{GE} = 20 V , V _{CE} = 0 V	I _{GES}	_	-	200	nA
DYNAMIC CHARACTERISTIC						
Input capacitance		C _{ies}	_	5328	_	pF
Output capacitance	$V_{CE} = 20 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	C _{oes}	-	252	-	1
Reverse transfer capacitance	1	C _{res}	-	148	-	1
Gate charge total		Qg	-	220	-	nC
Gate to emitter charge	$V_{CE} = 480 \text{ V}, I_{C} = 50 \text{ A}, V_{GE} = 15 \text{ V}$	Q _{ge}	_	52	-	1
Gate to collector charge	1	Q_{gc}	_	116	-	1
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-on delay time		t _{d(on)}	_	100	-	ns
Rise time	1	t _r	_	47	-	1
Turn-off delay time	T _J = 25°C	t _{d(off)}	-	237	-	1
Fall time	$V_{CC} = 400 \text{ V}, I_{C} = 50 \text{ A}$ $R_{g} = 10 \Omega$	t _f	-	67	-	1
Turn-on switching loss	V _{GE} = 0 V/ 15 V	E _{on}	_	1.50	-	mJ
Turn-off switching loss	1	E _{off}	-	0.46	-	1
Total switching loss	1	E _{ts}	_	1.96	-	1
Turn-on delay time		t _{d(on)}	_	90	-	ns
Rise time	1	t _r	_	49	-	
Turn-off delay time	T _J = 150°C	t _{d(off)}	_	245	-	
Fall time	$V_{CC} = 400 \text{ V}, I_{C} = 50 \text{ A}$ $R_{c} = 10 \Omega$	t _f	_	96	-	
Turn-on switching loss	$R_g = 10 \Omega$ $V_{GE} = 0 V/ 15 V$	E _{on}	_	1.90	-	mJ
Turn-off switching loss		E _{off}	-	0.83	-]
Total switching loss		E _{ts}	-	2.73	-	
DIODE CHARACTERISTIC						
Forward voltage	$V_{GE} = 0 \text{ V, } I_F = 50 \text{ A}$ $V_{GE} = 0 \text{ V, } I_F = 50 \text{ A, } T_J = 175^{\circ}\text{C}$	V _F	- -	2.10 2.20	2.90 -	V
Reverse recovery time	T _J = 25°C	t _{rr}	_	94	-	ns
Reverse recovery charge	$I_F = 50 \text{ A}, V_R = 400 \text{ V}$	Q _{rr}	_	0.45	-	μС
Reverse recovery current	di _F /dt = 200 A/μs	I _{rrm}	-	8	_	Α

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

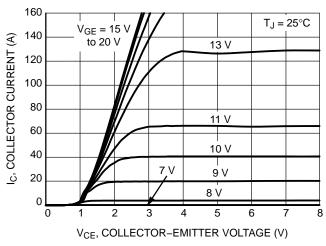


Figure 1. Output Characteristics

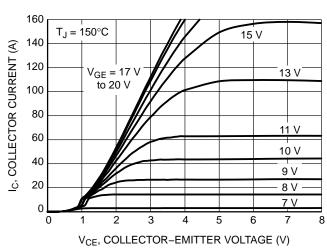


Figure 2. Output Characteristics

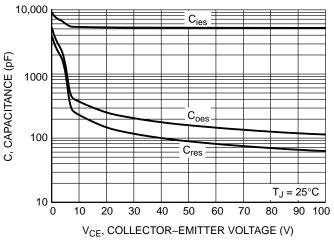


Figure 3. Typical Capacitance

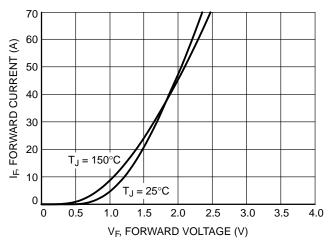


Figure 4. Diode Forward Characteristics

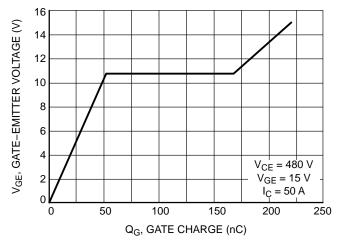


Figure 5. Typical Gate Charge

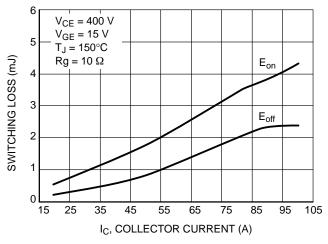


Figure 6. Switching Loss vs. I_C

TYPICAL CHARACTERISTICS

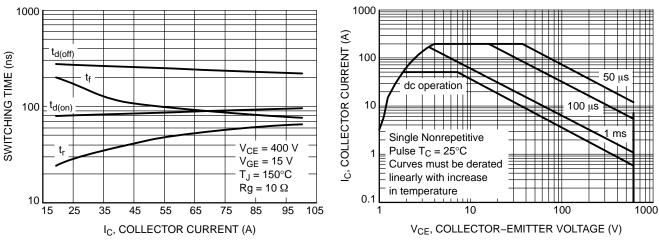


Figure 7. Switching Time vs. I_{C}

Figure 8. Safe Operating Area

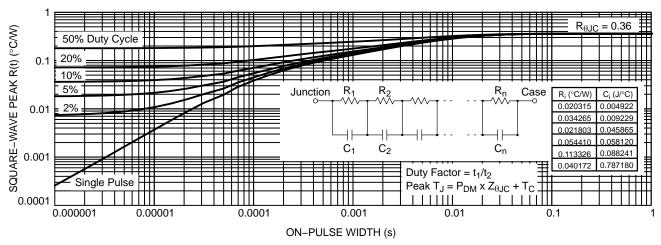


Figure 9. IGBT Transient Thermal Impedance

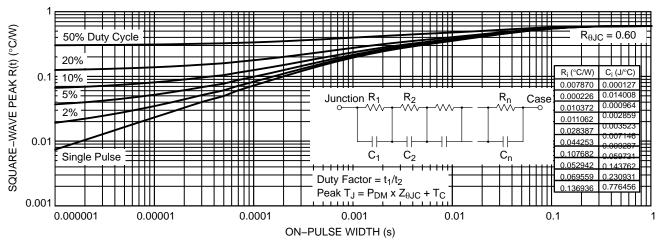
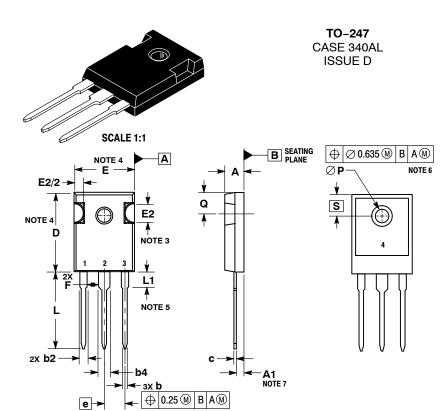


Figure 10. Diode Transient Thermal Impedance





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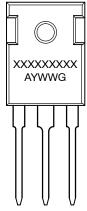
NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 SLOT REQUIRED, NOTCH MAY BE ROUNDED.

- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH.
 MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.
- LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY
- ØP SHALL HAVE A MAXIMUM DRAFT ANGLE OF 1.5° TO THE TOP OF THE PART WITH A MAXIMUM DIAMETER OF 3.91.
- DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.70	5.30	
A1	2.20	2.60	
b	1.07	1.33	
b2	1.65	2.35	
b4	2.60	3.40	
С	0.45	0.68	
D	20.80	21.34	
E	15.50	16.25	
E2	4.32	5.49	
е	5.45 BSC		
F	2.655		
L	19.80	20.80	
L1	3.81	4.32	
P	3.55	3.65	
Q	5.40	6.20	
S	6.15 BSC		

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code = Assembly Location Α

Υ = Year WW = Work Week = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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