Switch-mode Power Rectifiers

TO-220/D²PAK Surface Mount Power Package

MBRB2060CTG, MBR2060CTG, NRVBB2060CTT4G

These state-of-the-art devices employ the use of the Schottky Barrier principle with a platinum barrier metal.

Features

- Package Designed for Power Surface Mount Applications (D²PAK)
- Center-Tap Configuration (D²PAK)
- Guardring for Stress Protection
- Low Forward Voltage
- 175°C Operating Junction Temperature
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Short Heat Sink Tab Manufactured Not Sheared (D²PAK)
- Similar in Size to Industry Standard TO-220 Package
- NRVBB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These are Pb–Free Devices

Mechanical Characteristics:

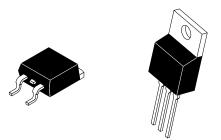
- Case: Epoxy, Molded, Epoxy Meets UL 94 V-0
- Weight: 1.7 Grams (Approximately) D²PAK, 1.9 Grams (Approximately) TO–220
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds (D²PAK)
- Device Meets MSL1 Requirements (D²PAK)
- ESD Ratings:
 - Machine Model = C (> 400 V)
 - Human Body Model = 3B (> 8000 V)



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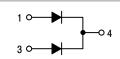
www.onsemi.com

SCHOTTKY BARRIER RECTIFIERS 20 AMPERES, 60 VOLTS

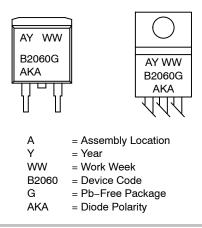


D²PAK-3 CASE 418B STYLE 3

TO-220 CASE 221A STYLE 6



MARKING DIAGRAMS



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

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MAXIMUM RATINGS (Per Leg)

Rating	Symbol	Value	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	60	V	
Average Rectified Forward Current (Rated V_R , T_C = 110°C) Total Device	I _{F(AV)}	10 20	А	
Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz, T _C = 100°C)	I _{FRM}	20	A	
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	150	А	
Peak Repetitive Reverse Surge Current (2.0 μs, 1.0 kHz)	I _{RRM}	0.5	А	
Storage Temperature Range	T _{stg}	-65 to +175	°C	
Operating Junction Temperature (Note 1)	TJ	-65 to +175	°C	
Voltage Rate of Change (Rated V _R)	dv/dt	10,000	V/µs	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. The heat generated must be less than the thermal conductivity from Junction-to-Ambient: $dP_D/dT_J < 1/R_{\theta,JA}$.

THERMAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case Junction-to-Ambient (Note 2) MBRB2060CTG Junction-to-Ambient (Note 2) MBR2060CTG	R _θ jc R _{θJA} R _{θJA}	2.0 50 60	°C/W

2. When mounted using minimum recommended pad size on FR-4 board.

ELECTRICAL CHARACTERISTICS (Per Leg)

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 3) ($i_F = 20$ Amps, T _J = 125°C) ($i_F = 20$ Amps, T _J = 25°C)	v _F	0.85 0.95	V
Maximum Instantaneous Reverse Current (Note 3) (Rated dc Voltage, $T_J = 125^{\circ}C$) (Rated dc Voltage, $T_J = 25^{\circ}C$)	İR	35 0.15	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

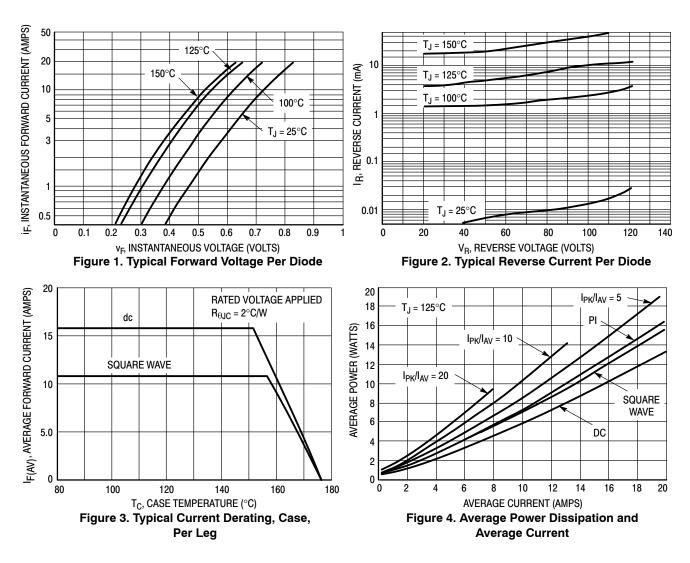
ORDERING INFORMATION

Device	Package	Shipping [†]	
MBRB2060CTG	D ² PAK (Pb–Free)	50 Units / Rail	
MBRB2060CTT4G	D ² PAK (Pb–Free)	800 Units / Tape & Reel	
NRVBB2060CTT4G*	D ² PAK (Pb–Free)	800 Units / Tape & Reel	
MBR2060CTG	TO-220 (Pb-Free)	50 Units / Rail	

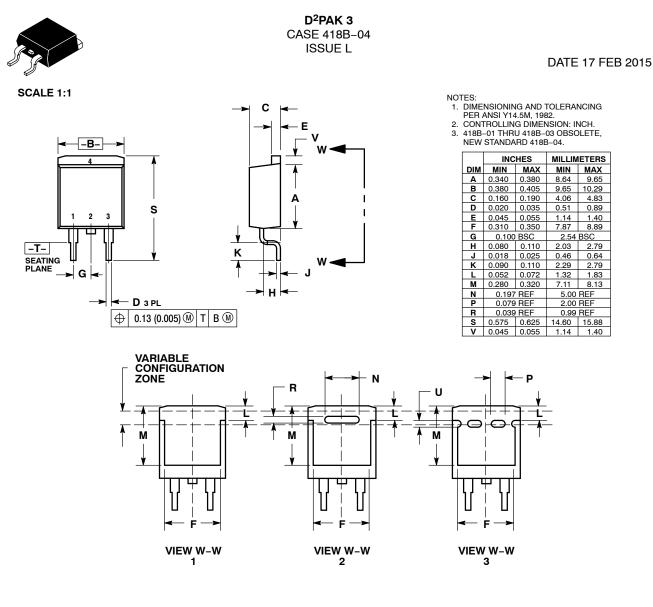
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*NRVBB Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable

MBRB2060CTG, MBR2060CTG, NRVBB2060CTT4G



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STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:	STYLE 6:
PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. GATE	PIN 1. CATHODE	PIN 1. NO CONNECT
2. COLLECTOR	2. DRAIN	2. CATHODE	2. COLLECTOR	2. ANODE	2. CATHODE
3. EMITTER	SOURCE	ANODE	3. EMITTER	CATHODE	3. ANODE
4. COLLECTOR	4. DRAIN	CATHODE	4. COLLECTOR	4. ANODE	4. CATHODE

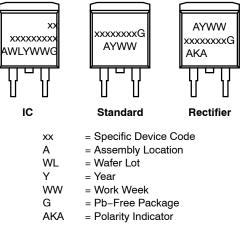
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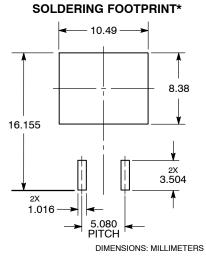
D²PAK 3 CASE 418B-04 ISSUE L

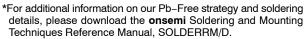
DATE 17 FEB 2015

GENERIC MARKING DIAGRAM*



*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.





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