

MOSFET – P-Channel, QFET

-60 V, -30 A, 26 mΩ

FQPF47P06, FQPF47P06YDTU

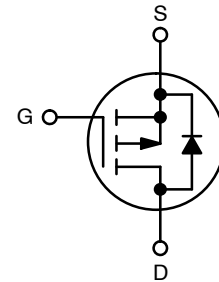
Description

This P-Channel enhancement mode power MOSFET is produced using onsemi's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

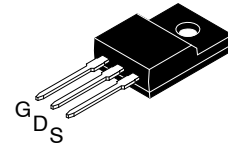
Features

- -30 A, -60 V, $R_{DS(on)} = 26 \text{ m}\Omega$ (Max.) @ $V_{GS} = -10 \text{ V}$, $I_D = -15 \text{ A}$
- Low Gate Charge (Typ. 84 nC)
- Low C_{rss} (Typ. 320 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating

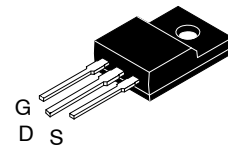
V_{DS}	$R_{DS(on)}$ MAX	I_D MAX
-60 V	26 mΩ @ -10 V	-30 A



P-Channel MOSFET

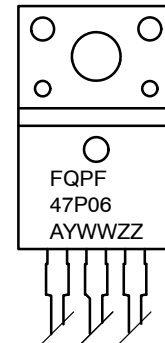


TO-220 Fullpack, 3-Lead / TO-220F-3SG
CASE 221AT



TO-220-3LD LF
CASE 340BJ

MARKING DIAGRAM



FQPF47P06 = Specific Device Code
A = Assembly Location
YWW = Date Code (Year & Week)
ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FQPF47P06	TO-220-3 Fullpack (Pb-Free)	1000 Units / Tube
FQPF47P06YDTU	TO-220-3 (Pb-Free)	800 Units / Tube

FQPF47P06, FQPF47P06YDTU

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise specified)

Symbol	Parameter		FQPF47P06 / FQPF47P06YDTU	Unit
V _{DSS}	Drain–Source Voltage		–60	V
I _D	Drain Current	– Continuous (T _C = 25°C)	–30	A
		– Continuous (T _C = 100°C)	–21.2	A
I _{DM}	Drain Current (Note 1)	– Pulsed	–120	A
V _{GSS}	Gate–Source Voltage		+ 25	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		820	mJ
I _{AR}	Avalanche Current (Note 1)		–30	A
E _{AR}	Repetitive Avalanche Energy (Note 1)		6.2	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		–7.0	V/ns
P _D	Power Dissipation (T _C = 25°C)		62	W
		– Derate above 25°C	0.41	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		–55 to +175	°C
T _L	Maximum Lead Temperature for Soldering Purposes, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. L = 1.06 mH, I_{AS} = –30 A, V_{DD} = –25 V, R_G = 25 Ω, Starting T_J = 25°C
3. I_{SD} ≤ –47 A, di/dt ≤ 300A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C

THERMAL CHARACTERISTICS

Symbol	Characteristic	Typ	Max	Unit
R _{θJC}	Thermal Resistance, Junction–to–Case	–	2.42	°C/W
R _{θJA}	Thermal Resistance, Junction–to–Ambient	–	62.5	°C/W

FQPF47P06, FQPF47P06YDTU

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = -250 μA	-60	--	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = -250 μA, Referenced to 25°C	-	-0.06	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -60 V, V _{GS} = 0 V	-	-	-1	μA
		V _{DS} = -48 V, T _C = 150°C	-	-	-10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = -25 V, V _{DS} = 0 V	-	-	-100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = 25 V, V _{DS} = 0 V	-	-	100	nA

ON CHARACTERISTICS

V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250 μA	-2.0	-	-4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = -10 V, I _D = -15 A	-	0.021	0.026	Ω
g _{FS}	Forward Transconductance	V _{DS} = -30 V, I _D = -15 A (Note 4)	-	19	-	S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V _{DS} = -25 V, V _{GS} = 0 V, f = 1.0 MHz	-	2800	3600	pF
C _{oss}	Output Capacitance		-	1300	1700	pF
C _{rss}	Reverse Transfer Capacitance		-	320	420	pF

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	V _{DD} = -30 V, I _D = -23.5 A, R _G = 25 Ω (Note 4, 5)	-	50	110	ns
t _r	Turn-On Rise Time		-	450	910	ns
t _{d(off)}	Turn-Off Delay Time		-	100	210	ns
t _f	Turn-Off Fall Time		-	195	400	ns
Q _g	Total Gate Charge	V _{DS} = -48 V, I _D = -47 A, V _{GS} = -10 V (Note 4, 5)	-	84	110	nC
Q _{gs}	Gate-Source Charge		-	18	-	nC
Q _{gd}	Gate-Drain Charge		-	44	-	nC

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATING

I _S	Maximum Continuous Drain-Source Diode Forward Current		-	-	-30	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		-	-	-120	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = -30 A	-	-	-4.0	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = -47 A, dI _F / dt = 100 A/μs (Note 4)	-	130	-	ns
Q _{rr}	Reverse Recovery Charge		-	0.55	-	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse Test: Pulse width ≤ 300 μs, Duty cycle ≤ 2%

5. Essentially independent of operating temperature

TYPICAL CHARACTERISTICS

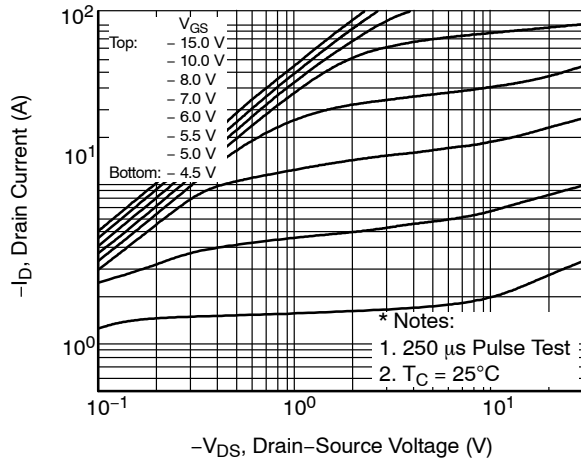


Figure 1. On-Region Characteristics

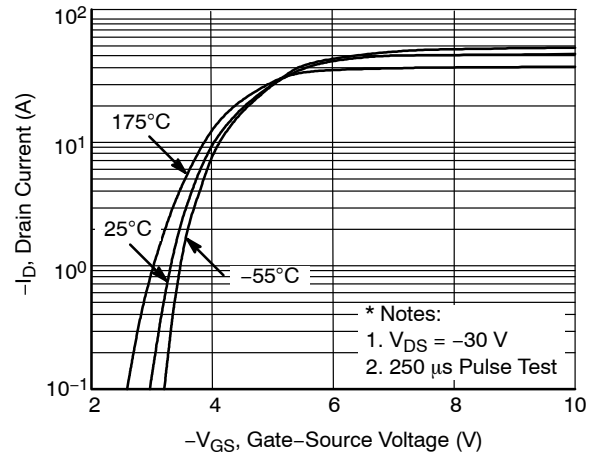


Figure 2. Transfer Characteristics

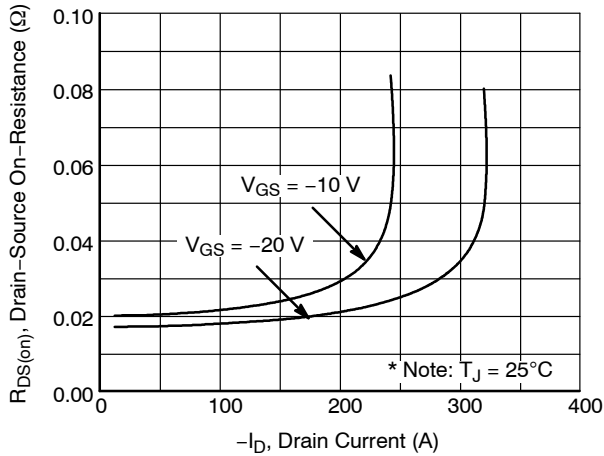


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

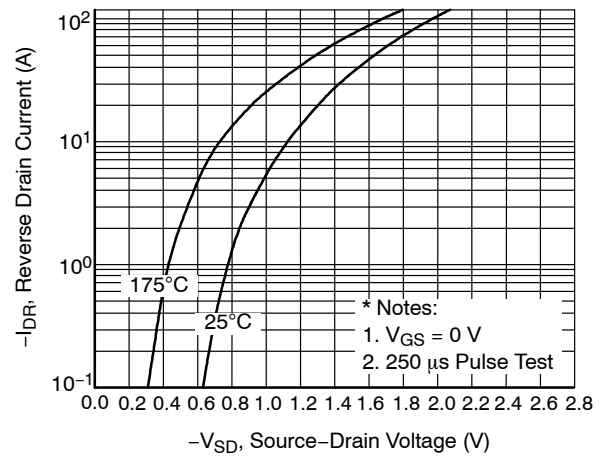


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

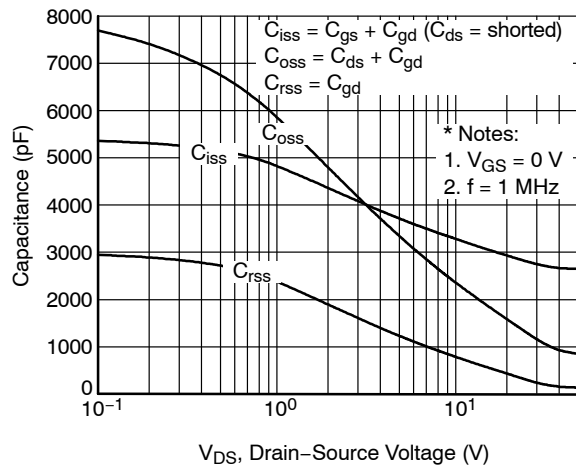


Figure 5. Capacitance Characteristics

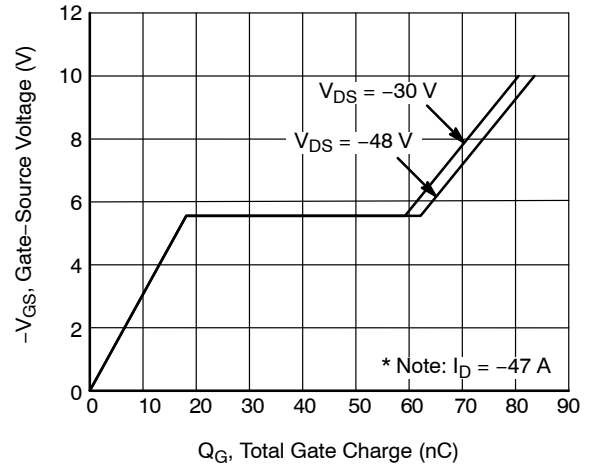


Figure 6. Gate Charge Characteristics

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TYPICAL CHARACTERISTICS (CONTINUED)

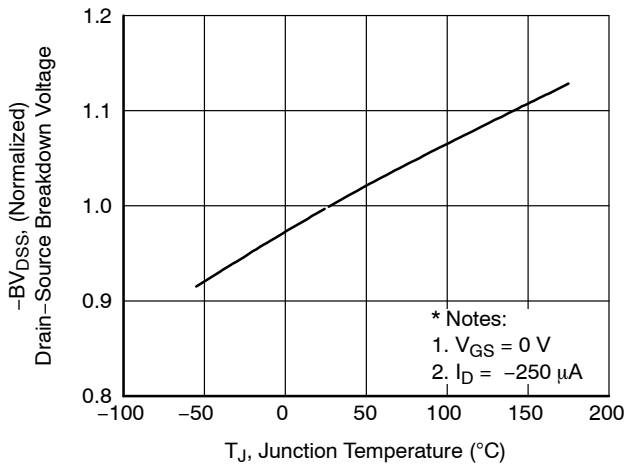


Figure 7. Breakdown Voltage Variation vs. Temperature

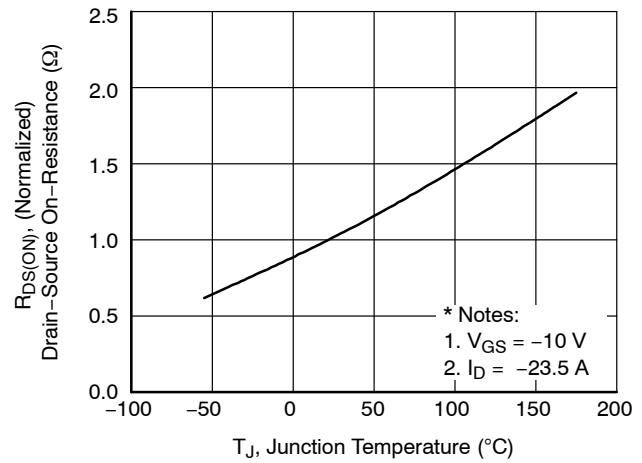


Figure 8. On-Resistance Variation vs. Temperature

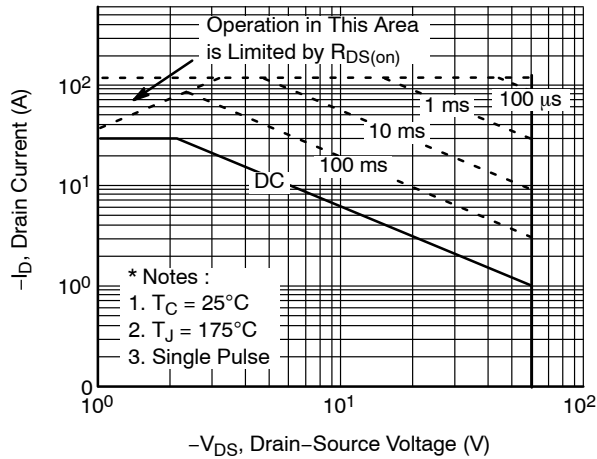


Figure 9. Maximum Safe Operating Area

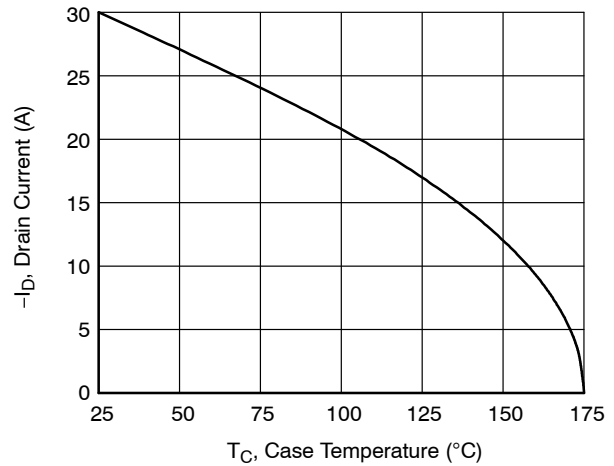


Figure 10. Maximum Drain Current vs. Case Temperature

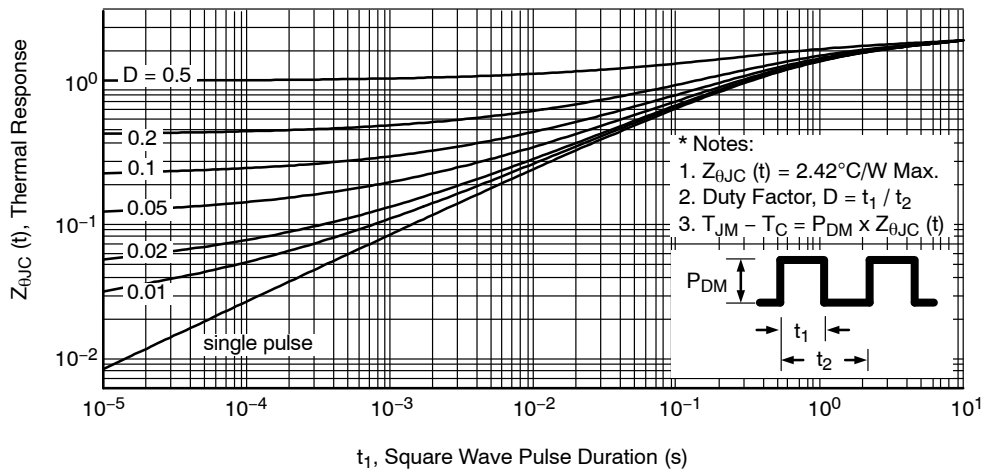


Figure 11. Transient Thermal Response Curve

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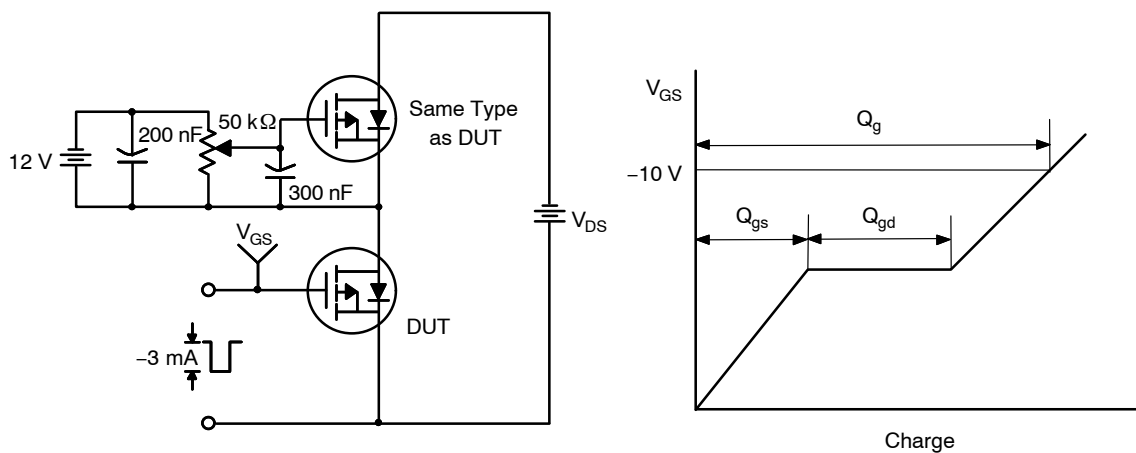


Figure 12. Gate Charge Test Circuit & Waveform

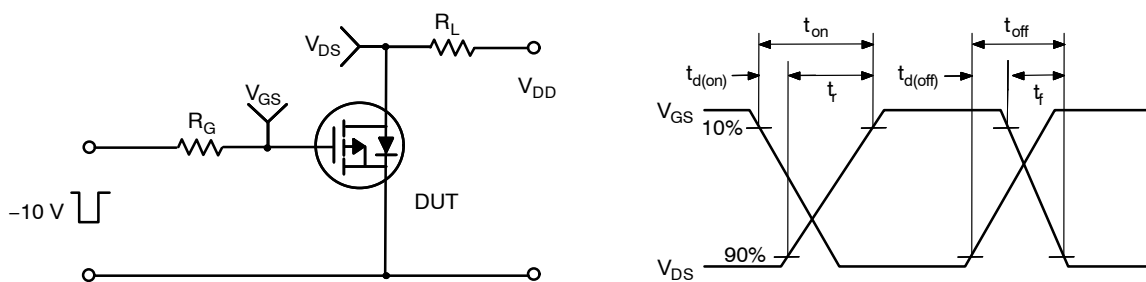


Figure 13. Resistive Switching Test Circuit & Waveforms

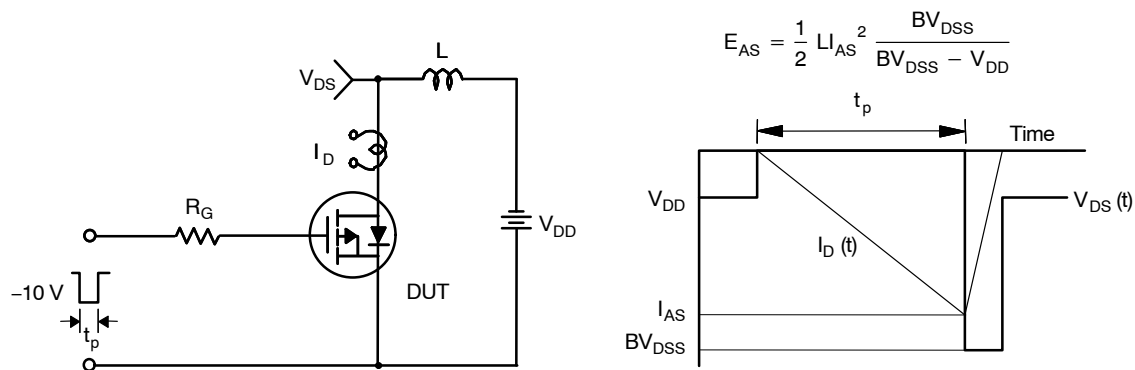


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

FQPF47P06, FQPF47P06YDTU

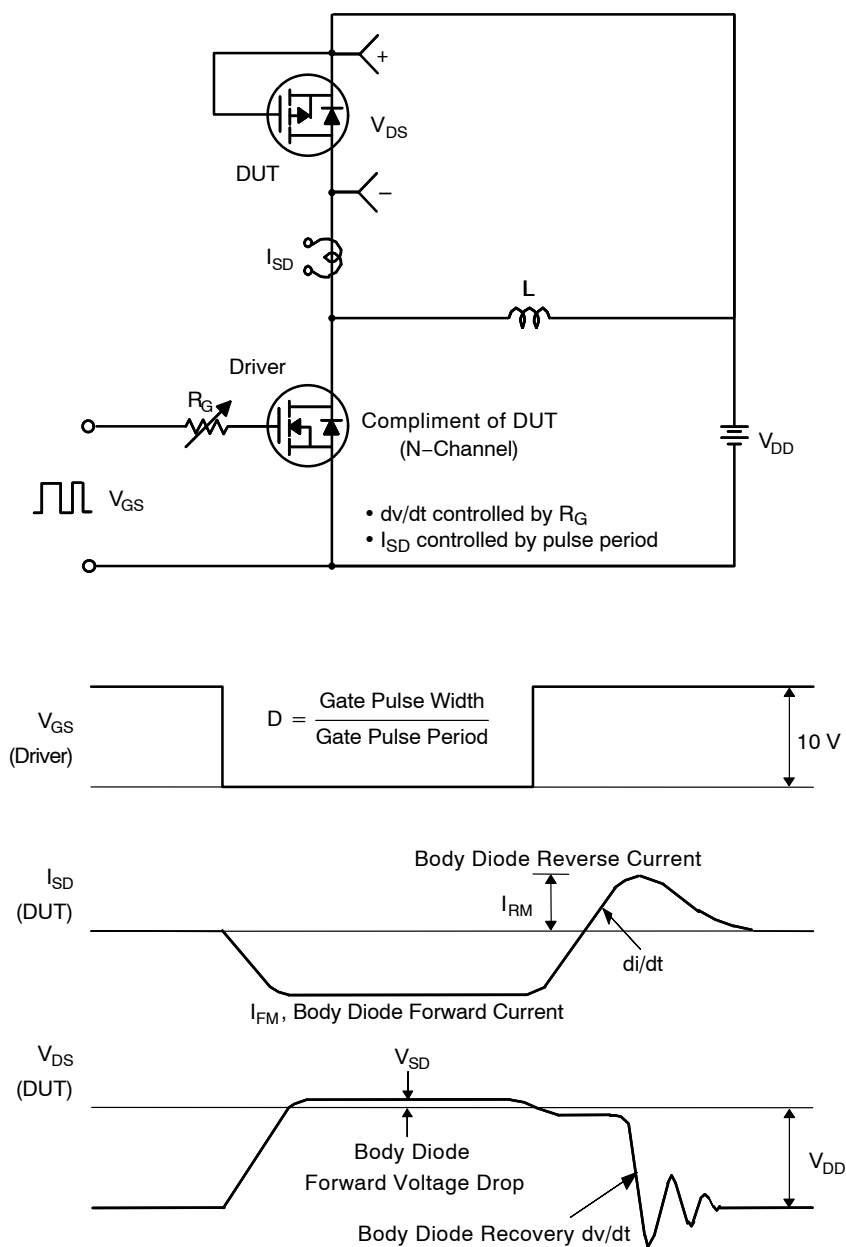


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

ON Semiconductor®

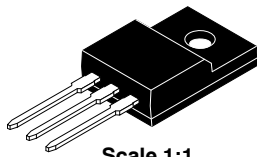
ON

TO-220 Fullpack, 3-Lead / TO-220F-3SG

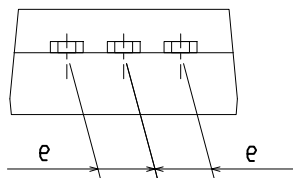
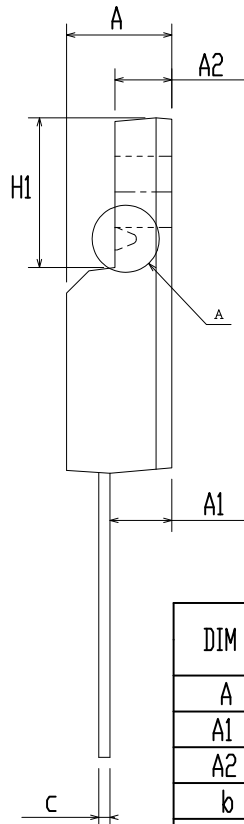
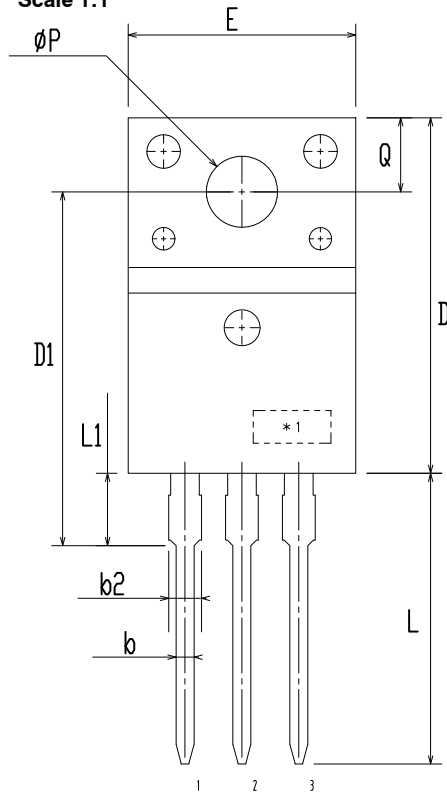
CASE 221AT

ISSUE B

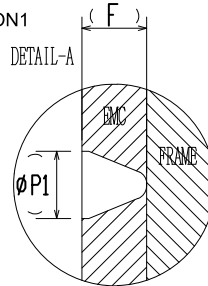
DATE 19 JAN 2021



Scale 1:1



OPTION1



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	~	~	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9.96	10.16	10.36
e	2.34	2.54	2.74
F	~	0.84	~
H1	6.48	6.68	6.88
L	12.78	12.98	13.18
L1	3.03	3.23	3.43
Ø P	2.98	3.18	3.38
Ø P1	~	1.00	~
Q	3.20	3.30	3.40

NOTES:

A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009

B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCTIONS.

C. OPTION 1 - WITH SUPPORT PIN HOLE

OPTION 2 - NO SUPPORT PIN HOLE

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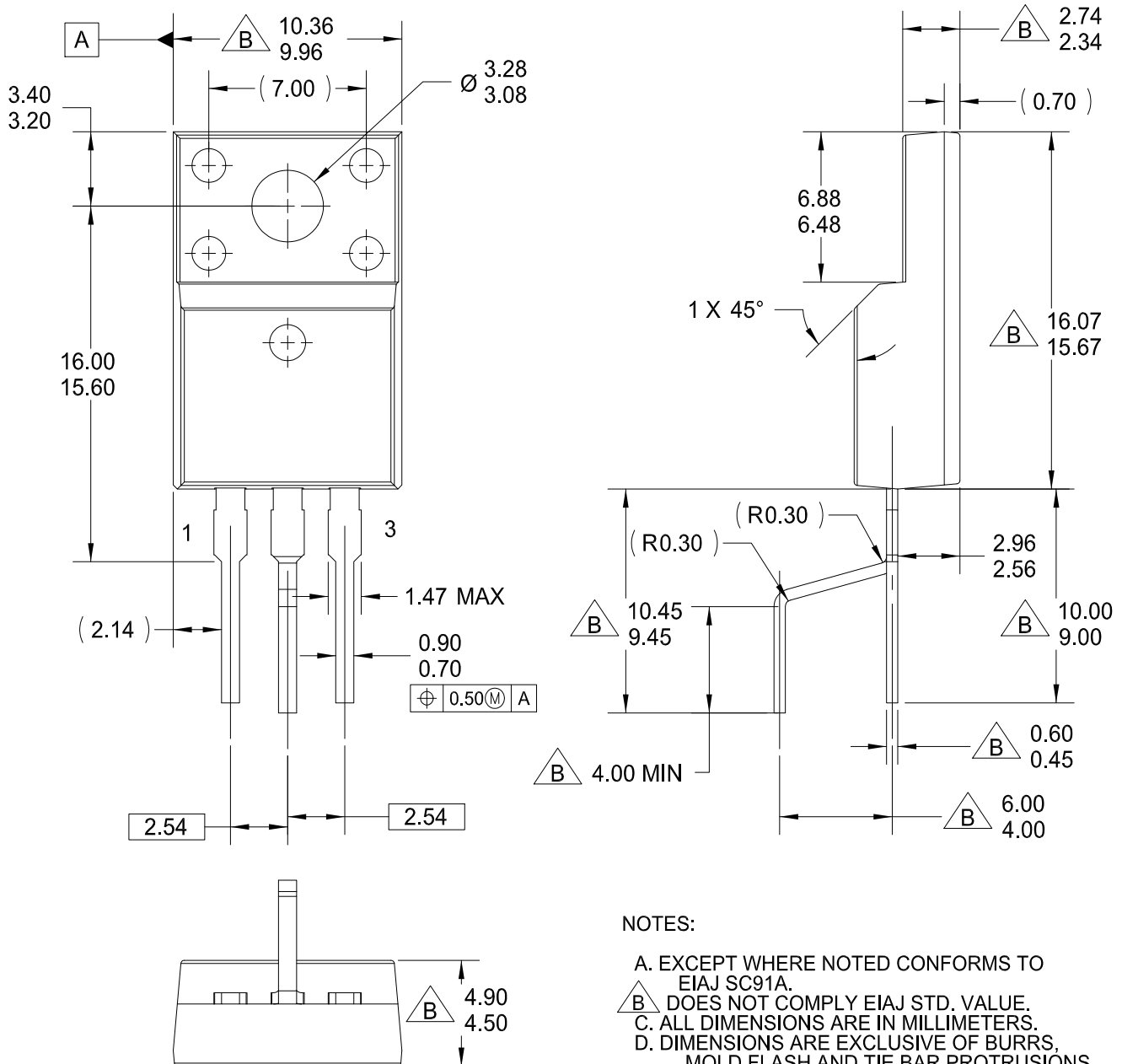
PACKAGE DIMENSIONS

ON Semiconductor®

ON

TO-220-3LD LF
CASE 340BJ
ISSUE O

DATE 31 AUG 2016



NOTES:

- A. EXCEPT WHERE NOTED CONFORMS TO EIAJ SC91A.
- B. DOES NOT COMPLY EIAJ STD. VALUE.
- C. ALL DIMENSIONS ARE IN MILLIMETERS.
- D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- E. DIMENSION AND TOLERANCE AS PER ASME Y14.5-1994.

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