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SEMICONDUCTOR®

FQP14N30 N-Channel QFET[®] MOSFET

300 V, 14.4 A, 290 mΩ

Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

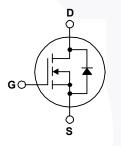
• 14.4 A, 300 V, R_{DS(on)} = 290 m Ω (Max.) @ V_{GS} = 10 V, I_D = 7.2 A

November 2013

- Low Gate Charge (Typ. 30 nC)
- Low Crss (Typ. 23 pF)
- 100% Avalanche Tested







Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

Symbol	Parameter		FQP14N30	Unit
V _{DSS}	Drain-Source Voltage		300	V
I _D	Drain Current - Continuous (T _C = 25°C)	14.4	A
	- Continuous (T _C = 100°	C)	9.1	A
I _{DM}	Drain Current - Pulsed	(Note 1)	57.6	A
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	600	mJ
I _{AR}	Avalanche Current	(Note 1)	14.4	A
E _{AR}	Repetitive Avalanche Energy	(Note 1)	14.7	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	4.5	V/ns
P _D	Power Dissipation ($T_C = 25^{\circ}C$)		147	W
	- Derate above 25°C		1.18	W/°C
T _J , T _{STG}	Operating and Storage Temperature Rang	je	-55 to +150	°C
Τ _L	Maximum Lead Temperature for Soldering 1/8" from Case for 5 seconds] ,	300	°C

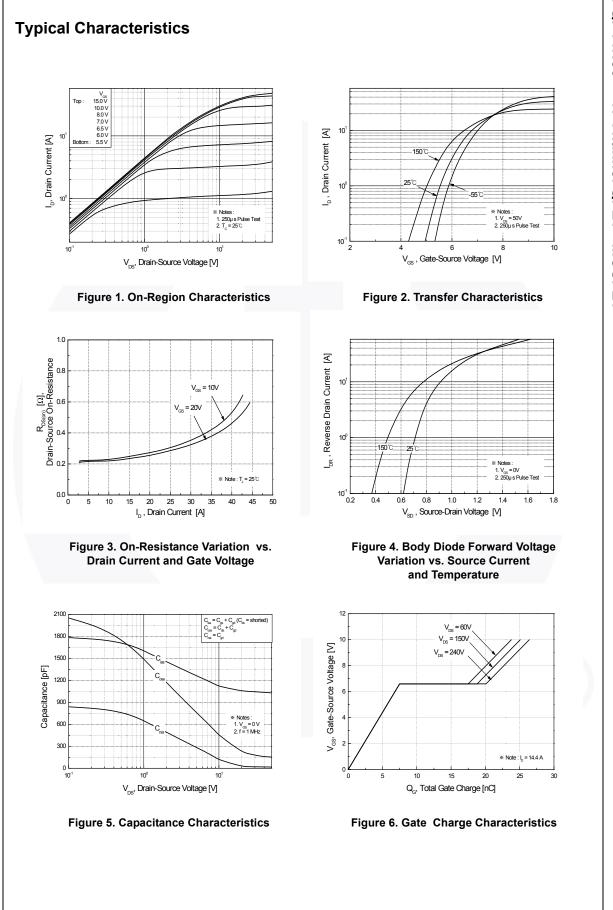
Thermal Characteristics

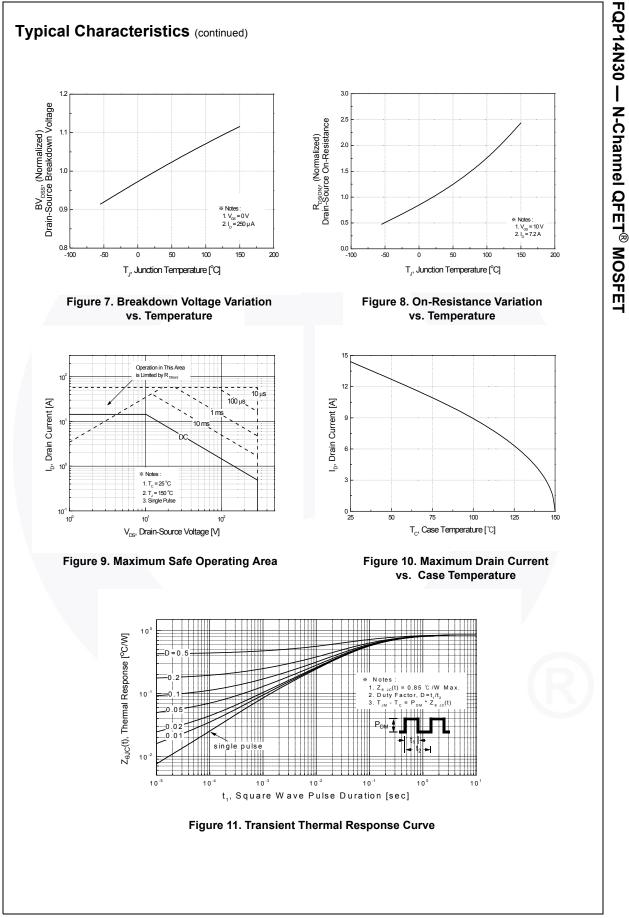
Symbol	Parameter	FQP14N30	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	0.85	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W

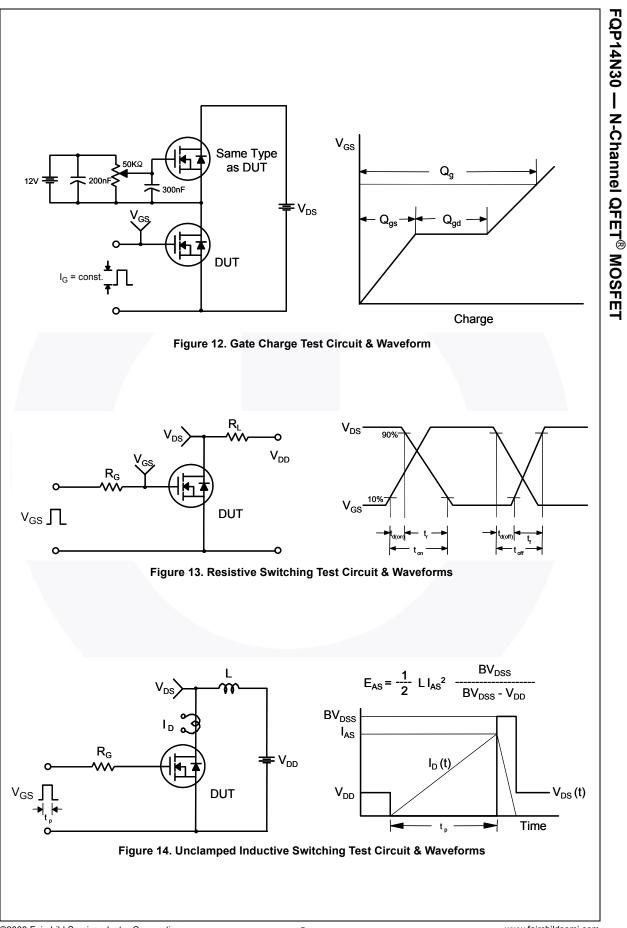
Part NumberTop MarkPackageFQP14N30FQP14N30TO-220		e Packing Method R	Reel Size	Та	ape Widt	h Q	Quantity		
		Tube N/A		N/A		5	50 units		
	cal C	haracteristics	T _C = 25°C	unless otherwise noted.					1
Symbol		Parameter		Test Condit	ions	Min	Тур	Мах	Unit
Off Cha	aracte	ristics							
3V _{DSS}	1	Source Breakdown V	oltage	V _{GS} = 0 V, I _D = 250 μA		300			V
ΔBV _{DSS}	Breakdown Voltage Temperature Coefficient		$I_D = 250 \ \mu\text{A}$, Referenced to 25°C			0.34		V/°C	
DSS				V _{DS} = 300 V, V _{GS} = 0 V				1	μA
200	Zero Gate Voltage Drain Current		$V_{\rm DS} = 240 \text{ V}, \text{ T}_{\rm C} = 125^{\circ}\text{C}$				10	μA	
GSSF	Gate-I	Gate-Body Leakage Current, Forward		$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$				100	nA
GSSR		Body Leakage Currer		$V_{GS} = -30 \text{ V}, \text{ V}_{DS} = 0$				-100	nA
On Cha	aracter	ristics				I			
GS(th)	Gate ⁻	Threshold Voltage		$V_{DS} = V_{GS}, I_{D} = 250$	μA	3.0		5.0	V
R _{DS(on)}		Drain-Source esistance		$V_{\rm GS}$ = 10 V, I _D = 7.2	A		0.23	0.29	Ω
FS	Forwa	rd Transconductance		V _{DS} = 50 V, I _D = 7.2	A		9.5		S
Dvnam	ic Cha	racteristics							
C _{iss}	1	Capacitance		V _{DS} = 25 V, V _{GS} = 0	V		1050	1360	pF
Coss		t Capacitance		$v_{DS} = 25 v, v_{GS} = 0$ f = 1.0 MHz	ν,		200	260	pF
Crss		se Transfer Capacitar	nce				23	30	pF
	1	aracteristics							
d(on)		On Delay Time					22	55	ns
r		On Rise Time		V _{DD} = 150 V, I _D = 14	.4 A,		145	300	ns
d(off)		Off Delay Time		R _G = 25 Ω			45	100	ns
f		Off Fall Time		(Note 4)			70	150	ns
, למ		Gate Charge		$V_{} = 240 V_{} = 14$	1 4		30	40	nC
λ _{gs}		Source Charge		V _{DS} = 240 V, I _D = 14.4 A, V _{GS} = 10 V			7.5		nC
λ _{gd}	Gate-I	Drain Charge		.63	(Note 4)		13		nC
		Diada Characta	viation on	d Movimum Doti					
s	1	um Continuous Drair		d Maximum Rati	iiyə			14.4	A
S SM		num Pulsed Drain-Sou					/	57.6	A
SM / _{SD}		Source Diode Forwar		$V_{GS} = 0 V, I_S = 14.4$	A		-	1.5	V
r SD		se Recovery Time	. voltago	$V_{GS} = 0.7, r_S = 14.4 \text{ A}$ $V_{GS} = 0.7, r_S = 14.4 \text{ A},$ $dr_F / dt = 100 \text{ A}/\mu \text{s}$			200		ns
<u>ת</u> מיי		se Recovery Charge					1.5		μC
-11	1.000	ce . coorery onlarge		1					μΟ

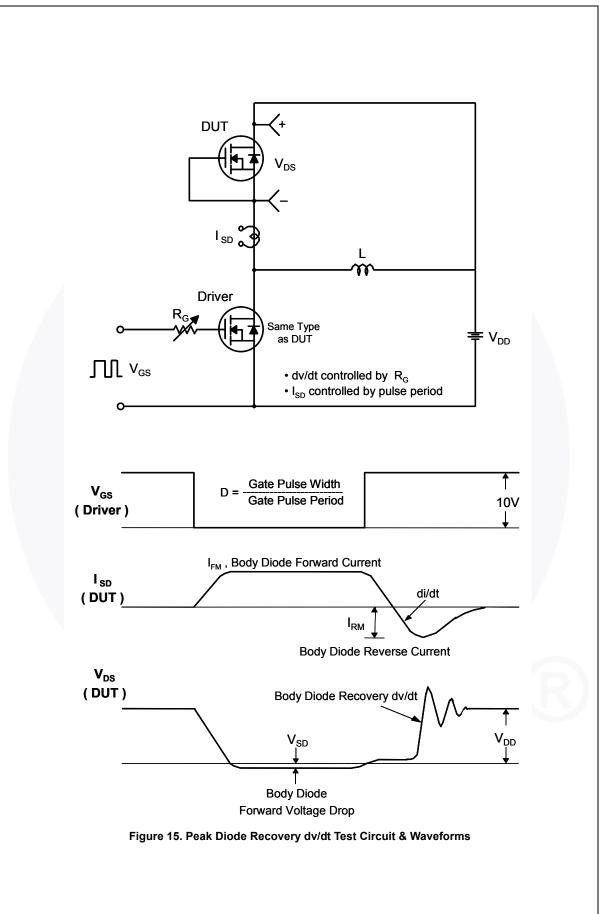
4. Essentially independent of operating temperature.

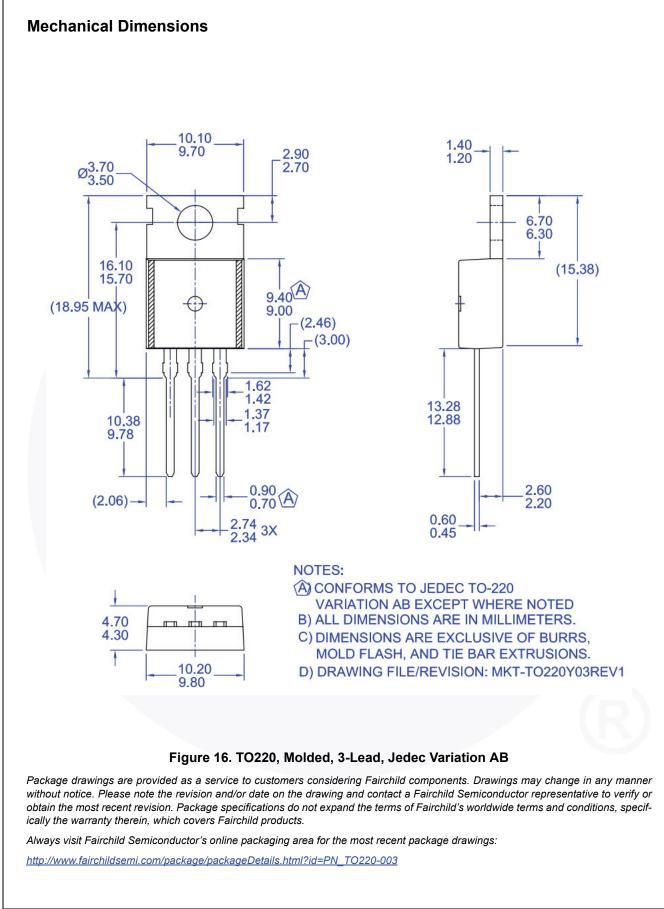
FQP14N30 — N-Channel QFET[®] MOSFET













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