# **LCD and Camera EMI Filter Array with ESD Protection**

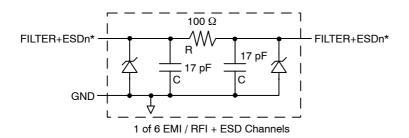
## Features

- Six Channels of EMI Filtering with Integrated ESD Protection
- Pi-Style EMI Filters in a Capacitor-Resistor-Capacitor (C-R-C) Network
- ±15 kV ESD Protection on Each Channel (IEC 61000-4-2 Level 4, Contact Discharge)
- ±30 kV ESD Protection on Each Channel (HBM)
- Greater than 40 dB Attenuation (Typical) at 1 GHz
- uDFN Package with 0.40 mm Lead Pitch:
  - 12-Lead: 2.50 mm x 1.20 mm x 0.50 mm
- These Devices are Pb-Free and are RoHS Compliant

## Applications

- LCD and Camera Data Lines in Mobile Handsets
- I/O Port Protection for Mobile Handsets, Notebook Computers, PDAs. etc.
- EMI Filtering for Data Ports in Cell Phones, PDAs or Notebook Computers
- Wireless Handsets
- Handheld PCs/PDAs

## **BLOCK DIAGRAM**



\*See Package/Pinout Diagrams for expanded pin information.



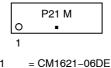
# **ON Semiconductor®**

http://onsemi.com



**DE SUFFIX** CASE 517AE

## **MARKING DIAGRAM**



P21 Μ

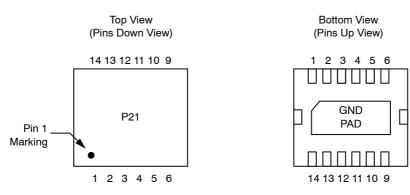
- = Month Code
- = Pb-Free Package

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>		
CM1621-06DE	uDFN-12 (Pb-Free)	3000/Tape & Reel		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

# PACKAGE / PINOUT DIAGRAMS



12-Lead UDFN Package

### Table 1. PIN DESCRIPTIONS

Device Pin(s)	Name	Description	Device Pin(s)	Name	Description
1	FILTER1	Filter + ESD Channel 1	12	FILTER1	Filter + ESD Channel 1
2	FILTER2	Filter + ESD Channel 2	11	FILTER2	Filter + ESD Channel 2
3	FILTER3	Filter + ESD Channel 3	10	FILTER3	Filter + ESD Channel 3
4	FILTER4	Filter + ESD Channel 4	9	FILTER4	Filter + ESD Channel 4
5	FILTER5	Filter + ESD Channel 5	8	FILTER5	Filter + ESD Channel 5
6	FILTER6	Filter + ESD Channel 6	7	FILTER6	Filter + ESD Channel 6
gnd pad	GND	Device Ground			

## SPECIFICATIONS

#### Table 2. ABSOLUTE MAXIMUM RATINGS

Parameter	Rating	Units	
Storage Temperature Range	-65 to +150	°C	
DC Power per Resistor	100	mW	
DC Package Power Rating	500	mW	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### **Table 3. STANDARD OPERATING CONDITIONS**

Parameter	Rating	Units
Operating Temperature Range	-40 to +85	°C

Symbol	Parameter	Conditions	Min	Тур	Max	Units
R	Resistance		85	100	115	Ω
C <sub>TOTAL</sub>	AL Total Channel Capacitance At 2.5 VDC Reverse Bias, 1 MHz, 30 mVAC		27	34	41	pF
С	C Capacitance C At 2.5 VDC Reverse Bias, 1 MHz, 30 mVAC			17		pF
V <sub>DIODE</sub>	Standoff Voltage	I <sub>DIODE</sub> = 10 μA		6.0		V
I <sub>LEAK</sub>	Diode Leakage Current (reverse bias)	V <sub>DIODE</sub> = +3.3 V			100	nA
V <sub>SIG</sub>	Signal Clamp Voltage	I <sub>LOAD</sub> = 1.0 mA	6.0	7.0	8.0	V
V <sub>ESD</sub>	In-system ESD Withstand Voltage a) Human Body Model (HBM), MIL-STD-883, Method 3015 b) Contact Discharge per IEC 61000-4-2 Level 4	(Note 2)	±30 ±15			kV
R <sub>DYN</sub>	Dynamic Resistance Positive Negative			2.3 0.9		Ω
$f_{C}$	Cut–off Frequency $Z_{SOURCE} = 50 \ \Omega$ , $Z_{LOAD} = 50 \ \Omega$	Channel R = 100 Ω, Channel C = 15 pF		90	135 (Note 3)	MHz
A <sub>1GHz</sub>	Absolute Attenuation at 1 GHz from 0 dB Level	$Z_{\text{SOURCE}} = 50 \ \Omega, \ Z_{\text{LOAD}} = 50 \ \Omega, \ DC \ Bias = 0 \ V; \ (Notes 1 \ and 3)$		-40		dB
A <sub>800MHz</sub> – 3 GHz	Absolute Attenuation at 800 MHz to 3 Ghz from 0 dB Level	$Z_{\text{SOURCE}} = 50 \ \Omega, \ Z_{\text{LOAD}} = 50 \ \Omega, \ DC \ Bias = 0 \ V; \ (Notes 1 \ and 3)$		-35		dB

Table 4. ELECTRICAL OPERATING CHARACTERISTICS (Note 1)

T<sub>A</sub> = 25°C unless otherwise specified.
 ESD applied to input and output pins with respect to GND, one at a time.
 Attenuation / RF curves characterized by a network analyzer using microprobes.

### **PERFORMANCE INFORMATION**

Typical Filter Performance ( $T_A = 25^{\circ}C$ , DC Bias = 0 V, 50  $\Omega$  Environment)

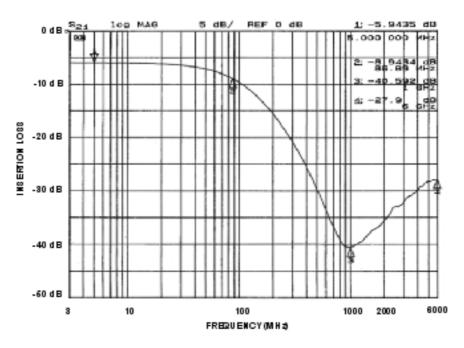


Figure 1. Insertion Loss vs. Frequency (FILTER1 Input to GND, CM1621–06DE) Typical Diode Capacitance vs. Input Voltage

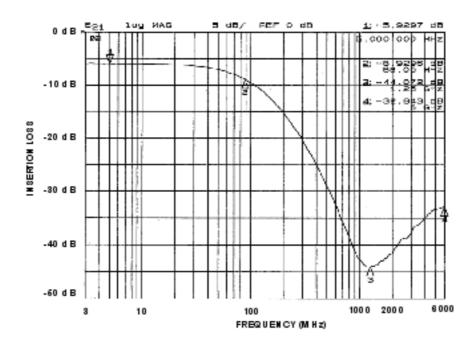


Figure 2. Insertion Loss vs. Frequency (FILTER2 Input to GND, CM1621–06DE) Typical Diode Capacitance vs. Input Voltage

## PERFORMANCE INFORMATION (Cont'd)

Typical Filter Performance (T<sub>A</sub> = 25°C, DC Bias = 0 V, 50  $\Omega$  Environment)

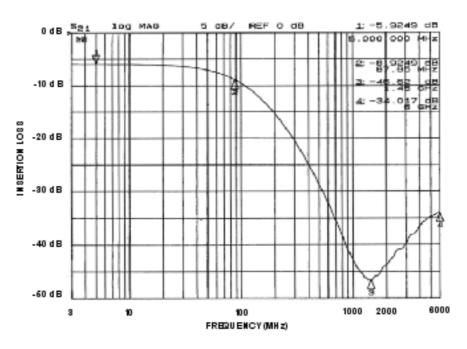


Figure 3. Insertion Loss vs. Frequency (FILTER3 Input to GND, CM1621–06DE) Typical Diode Capacitance vs. Input Voltage

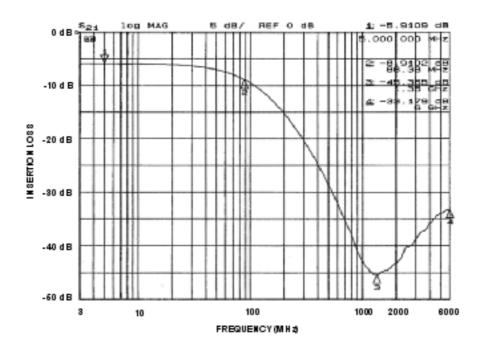
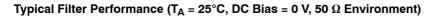


Figure 4. Insertion Loss vs. Frequency (FILTER4 Input to GND, CM1621–06DE) Typical Diode Capacitance vs. Input Voltage

## PERFORMANCE INFORMATION (Cont'd)



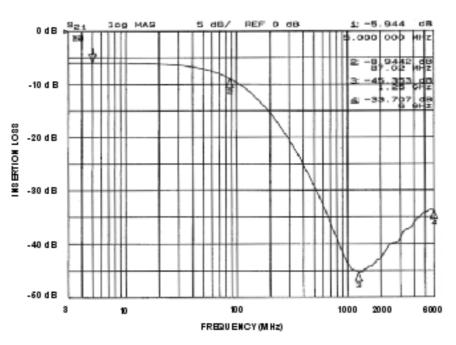


Figure 5. Insertion Loss vs. Frequency (FILTER5 Input to GND, CM1621–06DE) Typical Diode Capacitance vs. Input Voltage

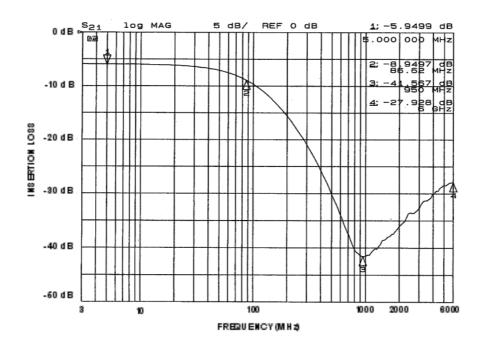
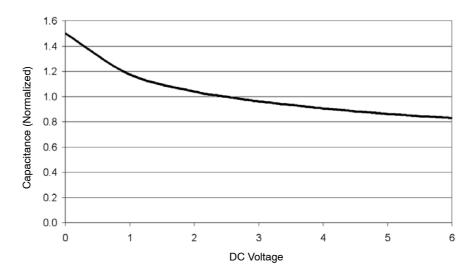
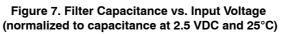
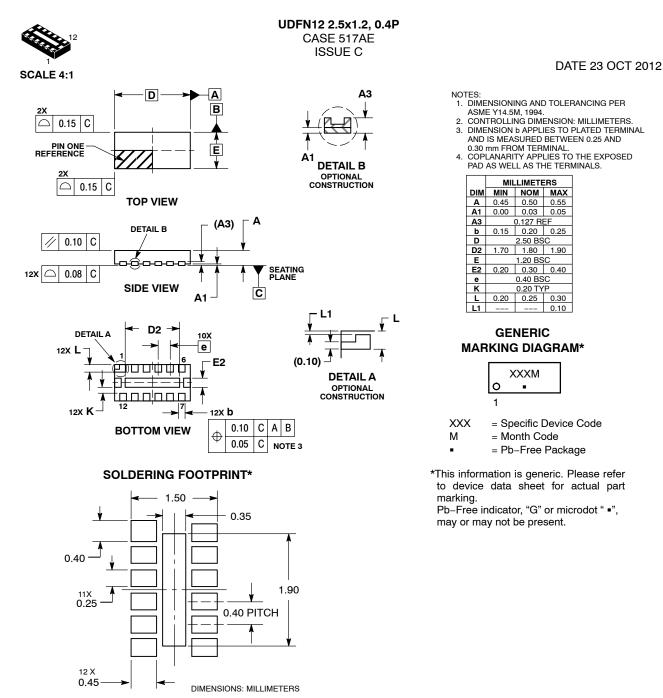


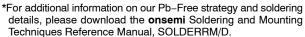
Figure 6. Insertion Loss vs. Frequency (FILTER6 Input to GND, CM1621–06DE) Typical Diode Capacitance vs. Input Voltage

# **PERFORMANCE INFORMATION (Cont'd)**









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