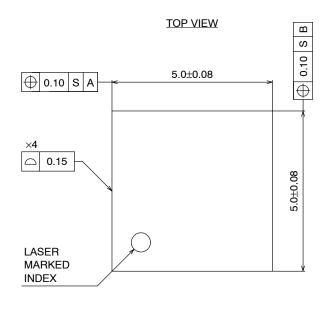


# LFLGA64 5x5 / FLGA64 CASE 569AD **ISSUE A**

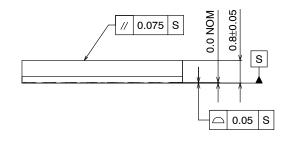
**DATE 17 JAN 2014** 

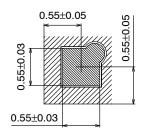


# 0.75 0.75 ⋖ 000,000 0000000 0.5 0000000 В 9 0000000 0000000 00000000 က Q000|000Q 0000000G' D C B 60-Ø0.3±0.03

**BOTTOM VIEW** 

#### SIDE VIEW





DETAIL\_a

0.5

# **GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code

Y = Year M = Month

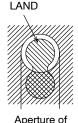
DDD = Additional Traceability Data

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

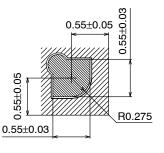


0.03 (M)

SAB



Aperture of Solder Resist



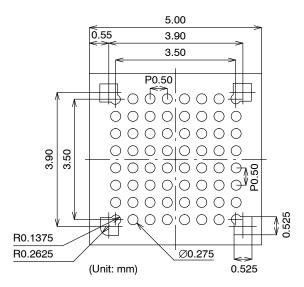
DETAIL\_b

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DESCRIPTION:	LFLGA64 5X5 / FLGA64		PAGE 1 OF 2

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**DATE 17 JAN 2014** 

### **SOLDERING FOOTPRINT\***



NOTE: The measurements are not to guarantee but for reference only.

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DESCRIPTION:	LFLGA64 5X5 / FLGA64		PAGE 2 OF 2

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<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.