



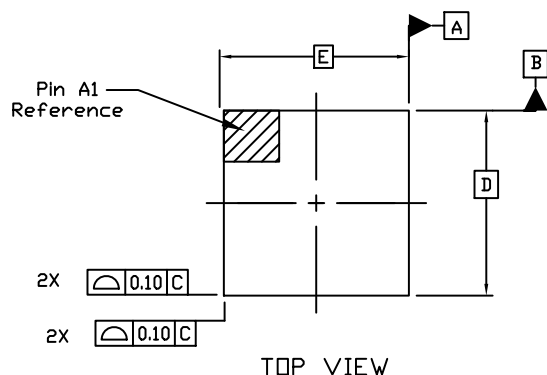
SCALE 2:1

### WLCSP36 2.34x2.34, 0.4P

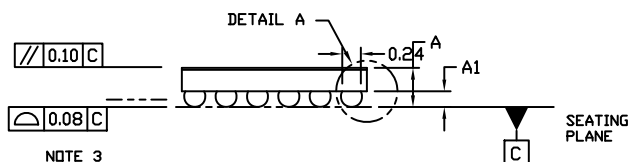
#### CASE 567LR

#### ISSUE D

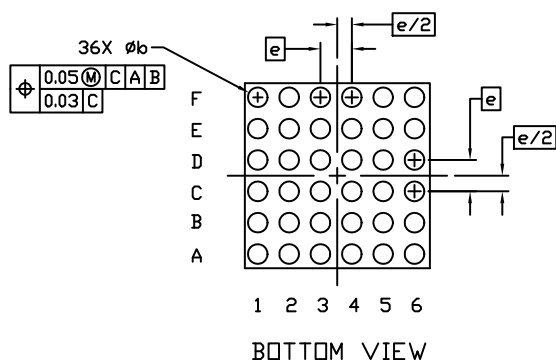
DATE 27 JUN 2019



TOP VIEW



SIDE VIEW

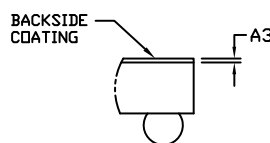


BOTTOM VIEW

#### NOTES:

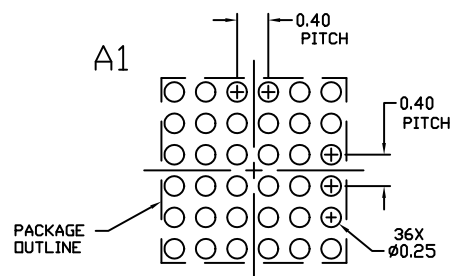
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. COPLANARITY APPLIES TO THE SPHERICAL CROWNS OF THE SOLDER BALLS.
4. BACKSIDE COATING IS OPTIONAL.

#### NOTE 4



DETAIL A

DIM	MILLIMETERS		
	MIN.	NDM.	MAX.
A	---	---	0.54
A1	0.17	0.20	0.23
A3	0.00	0.018	0.027
b	0.27 REF		
D	2.34 BSC		
E	2.34 BSC		
e	0.40 BSC		



RECOMMENDED MOUNTING FOOTPRINT

#### GENERIC MARKING DIAGRAM\*



- A = Assembly Location
- WL = Wafer Lot
- Y = Year
- WW = Work Week
- = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking.  
Pb-Free indicator, "G" or microdot "▪", may or may not be present.

\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	WLCSP36 2.34x2.34, 0.4P	PAGE 1 OF 1

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