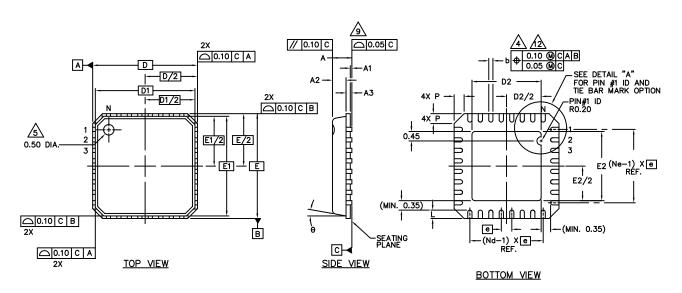
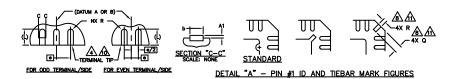
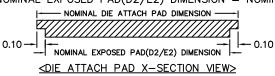


NQFP 20, 5x5 CASE 560AK-01 ISSUE O

DATE 29 JUL 2008







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SYMBOL	PITCH VARIA	I MAX.	No _{TE}	SYMBOL	MIN.	VARIAT	MAX.	No _{TE}	S>3BOL	PITCH MIN.	NOM.	MAX.	No _{TE}	S-MBOL	PITCH MIN.	VARIAT	MAX.	No _{TE}
e	0.80 BS	С		e		0.65 BSC			回		0.50 BSC			Ō		0.50 BSC		
N	16		3	N		20		3	Z		28		3	N		32		3
Nd	4		3	Nd		5		3	PZ		7		3	ЬИ		8		3
Ne	4		3	Ne		5		3	Ne		7		3	Ne		8		3
L	0.50 0.60	0.75		L	0.50	0.60	0.75		L	0.50	0.60	0.75		L	0.30	0.40	0.50	
ь	0.28 0.33	0.40	4	ь	0.23	0.28	0.35	4	Ь	0.18	0.23	0.30	4	Ь	0.18	0.23	0.30	4
D2s	SEE EXPOSED PAD VA	RIATION: A,B		D2	SEE EXPOSE	D PAD VAR	IATION: A,B		D2	SEE EXPOSE	D PAD VARI	ATION: A,B		D2	SEE EXPOS	ED PAD VAR	ATION: B,C,D	
E2S	SEE EXPOSED PAD VA	RIATION: A,B		E2	SEE EXPOSE	D PAD VAR	IATION: A,B		E2	SEE EXPOSE	D PAD VARI	ATION: A,B		E2	SEE EXPOS	ED PAD VAR	ATION: B,C,D	

* NOT DESIGNED YET ** DESIGNED BUT NOT TOOLED UP

, 4 th	PITCH	N.									
₩	1 11011	MIN I NOM I MAX									
Ľ	MIN.	I NOM.	MAX.	Ė							
е		0.40 BSC									
N		36		3							
Nd	9										
Ne	9										
Г	0.30										
Ь	0.15	4,12									
D2	SEE EXPOSED PAD VARIATION: *										
E2	SEE EXPOS	ED PAD VAI	RIATION: *								

STANDARD>

SYMBOLS		D2			E2		NOTE	
		MIN	NOM	MAX	MIN	NOM	MAX	
EXPOSED PAD	Α	2.60	2.70	2.80	2.60	2.70	2.80	
VARIATIONS	В	3.00	3.10	3.20	3.00	3.10	3.20	
	C	3.20	3.30	3.40	3.20	3.30	3.40	
	D	3.40	3.50	3.60	3.40	3.50	3.60	

S Y M B O L	COMMON								
MB	DIMENSIONS								
૧	MIN.	NOM.	MAX.	NO _{TE}					
Α	0.80	0.85	0.90						
A1	0.00	0.01	0.05	10					
A2	0.60	0.65	0.70						
Α3		0.20 REF.							
D	5.00 BSC								
D1	4.75 BSC								
E	5.00 BSC								
E1		4.75 BSC							
Φ	0 – 12°								
P	0.24	0.42	0.60						
Q	0.30	0.40	0.65	8,11					
R	0.13	0.17	0.23	8.11					

- NOTES: 1. DIE THICKNESS ALLOWABLE IS 0.305mm MAXIMUM(.012 INCHES MAXIMUM)
 - 2. DIMENSIONING & TOLERANCES CONFORM TO ASME Y14.5M. 1994.
 - 3. N IS THE NUMBER OF TERMINALS. Nd IS THE NUMBER OF TERMINALS IN X-DIRECTION &
 - Ne IS THE NUMBER OF TERMINALS IN Y-DIRECTION. /4\ DIMENSION f b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30mm FROM TERMINAL TIP.
 - /5.\ THE PIN #1 IDENTIFIER MUST BE EXISTED ON THE TOP SURFACE OF THE
 - PACKAGE BY USING INDENTATION MARK OR OTHER FEATURE OF PACKAGE BODY.
 - /6\ EXACT SHAPE AND SIZE OF THIS FEATURE IS OPTIONAL.
 - 7. ALL DIMENSIONS ARE IN MILLIMETERS.
 - $\sqrt{8}$ the shape shown on four corners are not actual i/o.
 - BILATERAL COPLANARITY ZONE APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
 - APPLIED ONLY FOR TERMINALS.
 - Q AND R APPLIES ONLY FOR STRAGHT TIEBAR SHAPES.
 - FOR 0.40mm LEAD PITCH, THE LEAD POSITION TOLERANCE MUST BE 0.07mm AT THE ACTUAL MEAN VALUE OF BODY SIZE.

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