



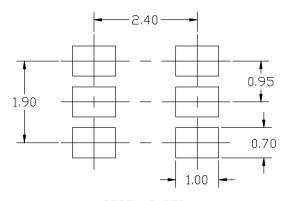
## SC74-6 3.00x1.50x0.90, 0.95P CASE 318AA ISSUE C

**DATE 22 AUG 2023** 

## NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETERS
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MILLIMETERS		
DIM	MIN.	N□M.	MAX.
Α	0.90	1.00	1.10
A1	0.01	0.06	0.10
A2	0.80	0.90	1.00
b	0.25	0.37	0.50
С	0.10	0.18	0.26
D	2.90	3.00	3.10
Е	1.30	1.50	1.70
е	0.85	0.95	1.05
L	0.20	0.40	0.60
HE	2.50	2.75	3.00
М	0°	_	10°



## RECOMMENDED MOUNTING FOOTPRINT\*

For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

STYLE 20:	STYLE 21:
PIN 1. COLLECTOR 1	PIN 1. COLI
2. BASE 2	2. EMIT
<ol><li>EMITTER 2</li></ol>	<ol><li>BASE</li></ol>
<ol><li>COLLECTOR 2</li></ol>	4. COLI
5. BASE 1	5. EMIT
6. EMITTER 1	6. BASE

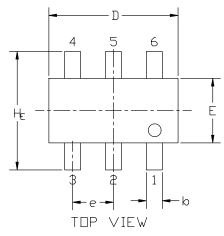
LECTOR 1 TTER 2 EE 2 LECTOR 2

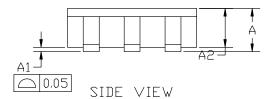
6. BASE 1

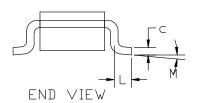
device data sheet for actual part marking.
Pb-Free indicator, "G" or microdot "■", may
or may not be present. Some products may
not follow the Generic Marking.
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DESCRIPTION:	SC74-6 3.00x1.50x0.90, 0.95P		PAGE 1 OF 1	

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## **GENERIC MARKING DIAGRAM\***



= Specific Device Code XXX = Date Code М

= Pb-Free Package \*This information is generic. Please refer to