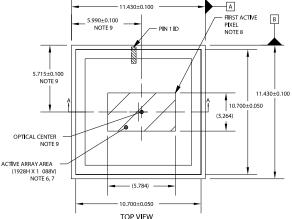


## PLCC48 11.43x11.43 (Parallel) CASE 776AS ISSUE D

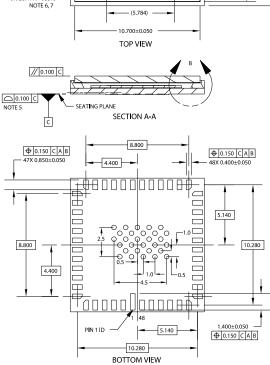
**DATE 18 DEC 2019** 

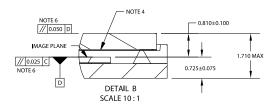


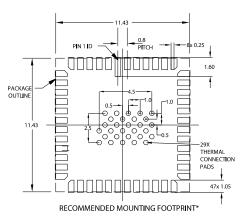
## NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994
- CONTROLLING DIMENSION: MILLIMETERS [mm].
- GLASS: 0.550 THICKNESS; REFRACTIVE INDEX = 1.52
- AIR GAP BETWEEN GLASS AND PIXEL ARRAY: 0.260 THICKNESS COPLANARITY APPLIES TO THE PLATED LAND PADS.
- PARALLELISM APPLIES ONLY TO THE ACTIVE ARRAY
- MAXIMUM ROTATION OF ACTIVE ARRAY RELATIVE TO DATUMS A AND B IS ± 0.5°.
- REFER TO THE DEVICE DATA SHEET FOR TOTAL PIXEL ARRAY DEFINITION S.
- OPTICAL CENTER RELATIVE TO PACKAGE CENTER (X, Y) = (0.275, 0.00 0).

  PACKAGE CENTER (X, Y) = (0.000, 0.000).

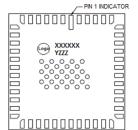






\*FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD
THE ON SEMICONDUCTOR SOLDERING AND MOUNTING
TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

## **GENERIC MARKING DIAGRAM\***



XXXX = Specific Device Code

= Year

ZZZ = Lot Traceability

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON12147G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	PLCC48 11.43x11.43 (Parallel)		PAGE 1 OF 1

ON Semiconductor and un are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.