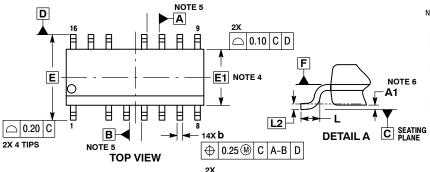
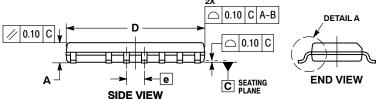


## SOIC-16 NB MISSING PINS 2 AND 13

CASE 751DU **ISSUE O** 

**DATE 18 OCT 2013** 





## NOTES:

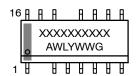
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.

  DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION.

  ALLOWABLE PROTRUSION SHALL BE 0.10 mm IN EXCESS OF
- MAXIMUM MATERIAL CONDITION.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
  PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS
  OR GATE BURRS SHALL NOT EXCEED 0.25 mm PER SIDE. DIMEN-SIONS D AND E ARE DETERMINED AT DATUM F.
  DIMENSIONS A AND B ARE TO BE DETERMINED AT DATUM F.
- A1 IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.

	MILLIMETERS		
DIM	MIN	MAX	
Α	1.35	1.75	
A1	0.10	0.25	
b	0.35	0.49	
C	0.17	0.25	
D	9.80	10.00	
Е	6.00 BSC		
E1	3.90 BSC		
е	1.27 BSC		
L	0.40	1.27	
L2	0.203 BSC		

## **GENERIC MARKING DIAGRAM\***



XXXXX = Specific Device Code = Assembly Location

WL = Wafer Lot = Year WW = Work Week G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

SOLDERIN	14X	
<u> </u>		1.52
		7.00
1.27 →	8	×

**PITCH** 

→ 0.60

DIMENSIONS: MILLIMETERS

**RECOMMENDED** SOI DEDING ECOTODINT

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Γ	DESCRIPTION:			PAGE 1 OF 1	

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