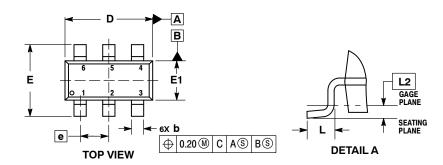
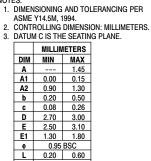
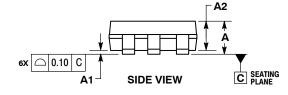


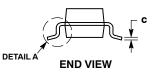


**DATE 29 FEB 2012** 

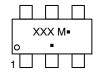








## GENERIC MARKING DIAGRAM\*



XXX = Specific Device Code

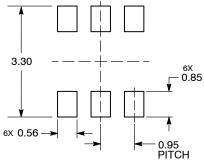
M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

## RECOMMENDED SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

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DESCRIPTION:	SOT-23, 6 LEAD		PAGE 1 OF 1

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<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.