UDFN2 2.0x1.25, 1.3P
CASE 517DF
ISSUE A
DATE 06 JUL 2016


BOTTOM VIEW
RECOMMENDED SOLDERING FOOTPRINT*


DIMENSIONS: MILLIMETERS
*For additional information on our $\mathrm{Pb}-$ Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

1. Dimensioning and tolerancing per

ASME Y14.5M, 1994
2. CONTROLLING DIMENSION: MILLIMETERS.

|  | MILLIMETERS |  |  |
| :---: | :---: | :---: | :---: |
| DIM | MIN | MAX |  |
| A | 0.45 | 0.55 |  |
| A1 | 0.00 | 0.05 |  |
| b | 0.95 | 1.05 |  |
| D | 2.00 | BSC |  |
| E | 1.25 | BSC |  |
| e | 1.30 | BSC |  |
| L | 0.45 | 0.55 |  |

GENERIC MARKING DIAGRAMS*

XX = Specific Device Code
$\mathrm{M}=$ Date Code
STYLE 1:
PIN 1. CATHODE
2. ANODE
*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

| DOCUMENT NUMBER: | 98AON04628G | Electronic versions are uncontrolled except when accessed directly from the Document Repository. <br> Printed versions are uncontroled except when stamped "CONTROLLED COPY" in red. |
| ---: | :--- | :--- | :--- |
| DESCRIPTION: | UDFN2 2.0X1.25,1.3P | PAGE 1 OF 1 |

ON Semiconductor and ON are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

