PIN 1

**IDENTIFIER** 

// 0.10 C



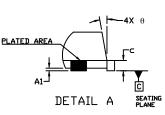


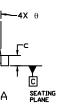
CASE 507BA **ISSUE A** 





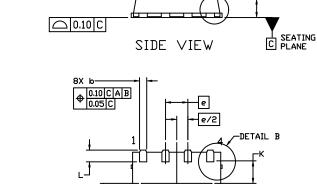
DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
CONTROLLING DIMENSION: MILLIMETERS
DIMENSIONS DI AND EI DO NOT INCLUDE MOLD FLASH,
PROTRUSIONS, OR GATE BURRS.
THIS PACKAGE CONTAINS WETTABLE FLANK DESIGN
FEATURES TO AID IN FILLET FORMATION ON THE LEADS DURING MOUNTING.





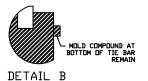
**MILLIMETERS** DIM MIN. NDM. MAX. 0.90 1.00 1.10 Α 0.05 A1 0.00 b 0.33 0.41 0.51 0.28 0.33 C 0.23 D 5.00 5.15 5.30 D1 4.70 4.90 5.10 D2 3.80 4.00 4.20 Ε 6.00 6.30 6.15 E1 5.70 5.90 6.10 E2 3.45 3.85 3.65 e 1.27 BSC G 0.575 0.71 0.51 Κ 1.50 1.20 1.35 0.575 0.51 0.71

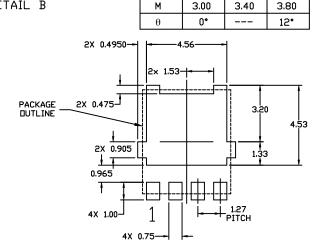
0.150 REF



TOP VIEW

DETAIL A





L1

М

## **GENERIC** MARKING DIAGRAM\*

PIN 5 (EXPOSED PAD)



= Assembly Location Α Υ = Year

W = Work Week 77 = Lot Traceability

XXXXXX = Specific Device Code \*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present. Some products

may not follow the Generic Marking.

RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DOCUMENT	MIIMPED.
DOCUMENT	NUMBER:

98AON26450H

BOTTOM VIEW

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**DESCRIPTION:** 

DFNW5 5x6 (FULL-CUT SO8FL WF)

**PAGE 1 OF 1** 

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