

### RECOMMENDED **SOLDERING FOOTPRINT\***

**BOTTOM VIEW** 

e/2

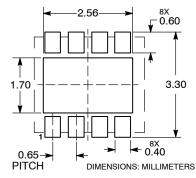
е

8X **b** 

Ф

0.10 | C | A | B

0.05 C NOTE 3



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

# DFN8, 3x3, 0.65P CASE 506DG **ISSUE A**



DFTΔII Δ ALTERNATE TERMINAL CONSTRUCTION

### **DATE 28 APR 2016**

#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND
- 0.30mm FROM THE TERMINAL TIP.
  COPLANARITY APPLIES TO THE EXPOSED
  PAD AS WELL AS THE TERMINALS.

	MILLIM	ETERS
DIM	MIN	MAX
Α	0.80	1.00
A1	0.00	0.05
A3	0.20	REF
b	0.25	0.35
D	3.00 BSC	
D2	2.30	2.50
E	3.00 BSC	
E2	1.50	1.70
е	0.65 BSC	
K	0.30 TYP	
L	0.35	0.45

# **GENERIC** MARKING DIAGRAM\*



XXXXXX= Specific Device Code

= Assembly Location

= Wafer Lot L Υ = Year = Work Week W

= Pb-Free Package (Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " ■", may or may not be present.

DOCUMENT NUMBER: 98AON10527G Electronic versions are uncontrolled except when stamped "CONTROLLED COPY" in	
Electronic versions are uncontrolled except when accessed directly from the Docur	cument Repository.

ON Semiconductor and un are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.