9 PIN FLIP-CHIP 1.45x1.45x0.596 CASE 499AL **ISSUE A** DATE 21 JUN 2022 NDTES: А DIMENSIONING AND TOLERANCING PER 1. В 4X 0.10 C ASME Y14.5M, 2009. 2. CONTROLLING DIMENSION: MILLIMETERS PIN 1 DIMENSION & IS MEASURED AT THE З. REFERENCE MAXIMUM SOLDER BALL DIAMETER PARALLEL TO DATUM C. 4. COPLANARITY APPLIES TO THE ΤΠΡ VIFW SPHERICAL CROWNS OF THE SOLDER BALLS DETAIL A 5. DATUM C, THE SEATING PLANE, IS DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS. 0.10 C MILLIMETERS DIM SEATING ○ 0.05 C MIN. NDM. MAX. PLANE С А 0.541 0.596 0.651 SIDE VIEW Α1 0.206 0.236 0.266 0.385 Aa Δ2 0.335 0.360 -D1 0.289 0.319 0.349 h e D 1.450 BSC с e R D1 Ŧ 1.000 BSC (Ŧ (+ E 1.450 BSC DETAIL A 9X E1 1.000 BSC SCALE 1:3 0.05 M C A B \oplus 0.50 BSC е 0.03 M C BOTTOM VIEW - 1.00 0.50 PTTCH Α1 -0.50 PITCH Ð Φ Ð GENERIC **MARKING DIAGRAM*** 1.00 AЗ С A1 PACKAGE AYWW XXX 9X Ø0.26 **DUTI INF** RECOMMENDED MOUNTING FOOTPRINT* C1 For additional information on our Pb-Free XXXX = Specific Device Code strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D. A = Assembly Location Y – Vear ww = Work Week = Pb-Free Package G or • *This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking. Electronic versions are uncontrolled except when accessed directly from the Document Repository. DOCUMENT NUMBER: 98AON19548D Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. **DESCRIPTION:** 9 PIN FLIP-CHIP 1.45x1.45x0.596 PAGE 1 OF 1

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