



INDICATOR

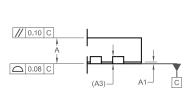
△ 0.10 C

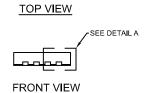
8

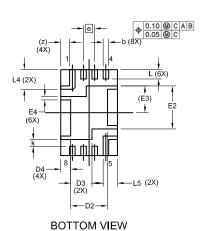
5

## PQFN8 3.3X5, 0.65P CASE 483AU **ISSUE A**

△ 0.10 C







SEATING PLANE DETAIL A SCALE: 2X -1.95 0.97 0.65 TYP KEEP OUT AREA 0.42 (8X) -0.30 0.70 (6X 1.61 1.30 (2X) (3.40)

LAND PATTERN RECOMMENDATION

\*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

## **DATE 06 MAY 2021**

- NOTES: UNLESS OTHERWISE SPECIFIED A) DOES NOT FULLY CONFORM TO JEDEC REGISTRATION, MO229 DATED 8/2012.
- B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH. MOLD FLASH OR BURRS DOES NOT EXCEED 0.10MM.
- D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- E) IT IS RECOMMENDED TO HAVE NO TRACES OR VIAS WITHIN THE KEEP OUT AREA.

DIM	MILLIMETERS			
Diivi	MIN.	NOM.	MAX.	
Α	0.70	0.75	0.80	
A1	0.00	ı	0.05	
A3	0.20 REF			
b	0.27	0.32	0.37	
D	3.20	3.30	3.40	
D2	2.04	2.14	2.24	
D3	1.13	1.23	1.33	
D4	0.48	0.58	0.68	
E	4.90	5.00	5.10	
E2	2.40	2.50	2.60	
E3	1.56 REF			
E4	0.10	0.20	0.30	
е	0.65 BSC			
k	0.30	0.40	0.50	
L	0.44	0.54	0.64	
L4	1.04	1.14	1.24	
L5	0.75	0.85	0.95	
z	0.51 REF			

DOCUMENT NUMBER:	98AON13669G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	PQFN8 3.3X5, 0.65P		PAGE 1 OF 1

ON Semiconductor and (III) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.